

**REVISIONS**

LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Delete references to device class M requirements. Update boilerplate paragraphs to current MIL-PRF-38535 requirements. - ro	13-02-04	C. SAFFLE
B	Update document paragraphs to current requirements. - ro	25-02-04	L. LEEPER BRANHAM



**Revision Status of Sheets**

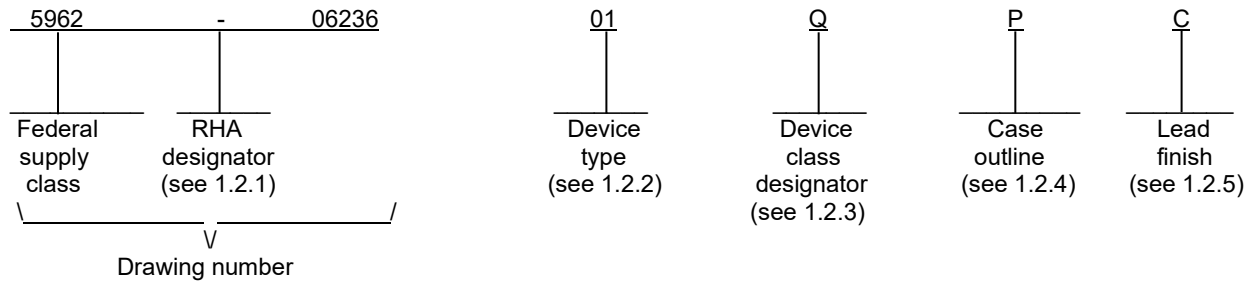
REV																				
SHEET																				
REV	B	B	B	B	B	B	B	B	B											
SHEET	1	2	3	4	5	6	7	8	9											

PMIC N/A		PREPARED BY RICK OFFICER		DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990 <a href="https://www.dla.mil/LandandMaritime">https://www.dla.mil/LandandMaritime</a>	
<b>STANDARD MICROCIRCUIT DRAWING</b>  THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE		CHECKED BY RAJESH PITHADIA		MICROCIRCUIT, LINEAR, LOW NOISE, HIGH SPEED VOLTAGE FEEDBACK OPERATIONAL AMPLIFIERS, MONOLITHIC SILICON	
		APPROVED BY ROBERT M. HEBER			
		DRAWING APPROVAL DATE 07-04-12			
AMSC N/A		REVISION LEVEL B		SIZE A	CAGE CODE <b>67268</b>
				SHEET 1 OF 9	
				<b>5962-06236</b>	

1. SCOPE

1.1 Scope. This drawing documents two product assurance class levels consisting of high reliability (device class Q) and space application (device class V). A choice of case outlines and lead finishes are available and are reflected in the Part or Identifying Number (PIN). When available, a choice of Radiation Hardness Assurance (RHA) levels is reflected in the PIN.

1.2 PIN. The PIN is as shown in the following example:



1.2.1 RHA designator. Device classes Q and V RHA marked devices meet the MIL-PRF-38535 specified RHA levels and are marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.

1.2.2 Device types. The device types identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>
01	EL5132	Low noise, high speed voltage feedback operational amplifier
02	EL5133	Low noise, high speed voltage feedback operational amplifier

1.2.3 Device class designator. The device class designator is a single letter identifying the product assurance level as follows:

<u>Device class</u>	<u>Device requirements documentation</u>
Q or V	Certification and qualification to MIL-PRF-38535

1.2.4 Case outline. The case outline are as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
P	GDIP1-T8 or CDIP2-T8	8	Dual-in-line

1.2.5 Lead finish. The lead finish is as specified in MIL-PRF-38535 for device classes Q and V.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-06236</b>
		REVISION LEVEL <b>B</b>	SHEET <b>2</b>

1.3 Absolute maximum ratings. <sup>1/</sup>

Supply voltage from +VS to -VS .....	13.2 V
Current at -I <sub>IN</sub> , +I <sub>IN</sub> , and CE .....	±5 mA
Continuous output current .....	20 mA
Power dissipation (PD) .....	192 mW
Storage temperature range .....	-65°C to +150°C
Operating junction temperature (T <sub>J</sub> ) .....	+150°C
Thermal resistance, junction-to-case (θ <sub>JC</sub> ) .....	See MIL-STD-883
Thermal resistance, junction-to-ambient (θ <sub>JA</sub> ) .....	135°C/W
Electrostatic discharge (ESD):	
Human body model (HBM) .....	3500 V
Machine model (MM) .....	350 V

1.4 Recommended operating conditions.

Supply voltage from +VS to -VS .....	10 V
Ambient operating temperature range (TA) .....	-55°C to +125°C

2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATION

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard Microcircuits.  
MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

DEPARTMENT OF DEFENSE HANDBOOKS

MIL-HDBK-103 - List of Standard Microcircuit Drawings.  
MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at <https://quicksearch.dla.mil/>.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

<sup>1/</sup> Stresses above the absolute maximum rating may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-06236</b>
		REVISION LEVEL <b>B</b>	SHEET <b>3</b>

### 3. REQUIREMENTS

3.1 Item requirements. The individual item requirements for device classes Q and V shall be in accordance with MIL-PRF-38535 as specified herein, or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein for device classes Q and V.

3.2.1 Case outline. The case outline shall be in accordance with 1.2.4 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.3 Electrical performance characteristics and postirradiation parameter limits. Unless otherwise specified herein, the electrical performance characteristics and postirradiation parameter limits are as specified in table I and shall apply over the full ambient operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are defined in table I.

3.5 Marking. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked. For packages where marking of the entire SMD PIN number is not feasible due to space limitations, the manufacturer has the option of not marking the "5962-" on the device. For RHA product using this option, the RHA designator shall still be marked. Marking for device classes Q and V shall be in accordance with MIL-PRF-38535.

3.5.1 Certification/compliance mark. The certification mark for device classes Q and V shall be a "QML" or "Q" as required in MIL-PRF-38535.

3.6 Certificate of compliance. For device classes Q and V, a certificate of compliance shall be required from a QML-38535 listed manufacturer in order to supply to the requirements of this drawing (see 6.6.1 herein). The certificate of compliance submitted to DLA Land and Maritime-VA prior to listing as an approved source of supply for this drawing shall affirm that the manufacturer's product meets, for device classes Q and V, the requirements of MIL-PRF-38535 and herein.

3.7 Certificate of conformance. A certificate of conformance as required for device classes Q and V in MIL-PRF-38535 shall be provided with each lot of microcircuits delivered to this drawing.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-06236</b>
		REVISION LEVEL <b>B</b>	SHEET <b>4</b>

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions <sup>1/</sup> -55°C ≤ TA ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Offset voltage	V <sub>OS</sub>		1, 2, 3	01, 02	-2.5	+2.5	mV
Input bias current	I <sub>IB</sub>	V <sub>IN</sub> = 0 V	1, 2, 3	01, 02	8	20	μA
Input offset current	I <sub>OS</sub>	V <sub>IN</sub> = 0 V	1, 2, 3	01, 02	-1250	+1250	nA
Power supply rejection ratio	PSRR	+V <sub>S</sub> = ±4.75 V to ±5.25 V	1, 2, 3	01, 02	65		dB
Common mode rejection ratio	CMRR	V <sub>IN</sub> = ±3.0 V	1, 2, 3	01, 02	80		dB
Supply current - enabled	I <sub>S-on</sub>		1, 2, 3	01, 02	6.5	16	mA
Supply current - disabled	I <sub>S-off</sub>		1, 2, 3	01, 0 2		70	μA
Open loop gain	AVOL	V <sub>OUT</sub> = ±3.0 V, R <sub>L</sub> = 1 kΩ to GND	1, 2, 3	01, 02	68		dB
Output voltage swing	V <sub>O</sub>	R <sub>F</sub> = 900 Ω, R <sub>G</sub> = 100 Ω, R <sub>L</sub> = 150 Ω	1, 2, 3	01, 02	±3.1		V
Short circuit current	I <sub>SC</sub>	R <sub>L</sub> = 10 Ω	1, 2, 3	01, 02	70		mA
ENABLE pin input for current high	I <sub>IH-ENB</sub>	$\overline{CE}$ = 5 V	1, 2, 3	01		25	μA
ENABLE pin input for current low	I <sub>IL-ENB</sub>	$\overline{CE}$ = 0 V	1, 2, 3	01	-1	1	μA
$\overline{CE}$ input high voltage for power down	V <sub>IH</sub>		1, 2, 3	01	+V <sub>S</sub> - 0.5		V
$\overline{CE}$ input low voltage for power down	V <sub>IL</sub>		1, 2, 3	01		+V <sub>S</sub> - 3	V

<sup>1/</sup> Unless otherwise specified, +V<sub>S</sub> = 5 V, -V<sub>S</sub> = -5 V, load resistance (R<sub>L</sub>) = 500 Ω, feedback resistance (R<sub>F</sub>) = 10 kΩ, and gain resistance (R<sub>G</sub>) = 100 Ω.

**STANDARD  
MICROCIRCUIT DRAWING**  
DLA LAND AND MARITIME  
COLUMBUS, OHIO 43218-3990

SIZE  
**A**

**5962-06236**

REVISION LEVEL  
**B**

SHEET **5**

Device types	01	02
Case outline	P	
Terminal number	Terminal symbol	
1	NC	NC
2	-INPUT	-INPUT
3	+INPUT	+INPUT
4	-VS	-VS
5	NC	NC
6	OUTPUT	OUTPUT
7	+VS	+VS
8	$\overline{CE}$	NC

NC = No connection

Terminal symbol	Description
NC	Not connected
-INPUT	Inverting input
+INPUT	Non-inverting input
-VS	Negative power supply
OUTPUT	Amplifier output
+VS	Positive power supply
$\overline{CE}$	Enable and disable input

FIGURE 1. Terminal connections.

**STANDARD  
MICROCIRCUIT DRAWING**  
DLA LAND AND MARITIME  
COLUMBUS, OHIO 43218-3990

SIZE  
**A**

**5962-06236**

REVISION LEVEL  
**B**

SHEET **6**

4. VERIFICATION

4.1 Sampling and inspection. For device classes Q and V, sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

4.2 Screening. For device classes Q and V, screening shall be in accordance with MIL-PRF-38535, and shall be conducted on all devices prior to qualification and technology conformance inspection.

4.2.1 Additional criteria for device classes Q and V.

- a. The burn-in test duration, test condition and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document revision level control of the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.
- b. Interim and final electrical test parameters shall be as specified in table II herein.
- c. Additional screening for device class V beyond the requirements of device class Q shall be as specified in MIL-PRF-38535, appendix B.

4.3 Qualification inspection for device classes Q and V. Qualification inspection for device classes Q and V shall be in accordance with MIL-PRF-38535. Inspections to be performed shall be those specified in MIL-PRF-38535 and herein for groups A, B, C, D, and E inspections (see 4.4.1 through 4.4.4).

4.4 Conformance inspection. Technology conformance inspection for classes Q and V shall be in accordance with MIL-PRF-38535 including groups A, B, C, D, and E inspections, and as specified herein.

4.4.1 Group A inspection.

- a. Tests shall be as specified in table II herein.
- b. Subgroups 4, 5, 6, 7, 8, 9, 10, and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.

4.4.2 Group C inspection. The group C inspection end-point electrical parameters shall be as specified in table II herein.

4.4.2.1 Additional criteria for device classes Q and V. The steady-state life test duration, test condition and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The test circuit shall be maintained under document revision level control by the device manufacturer's TRB in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.

4.4.3 Group D inspection. The group D inspection end-point electrical parameters shall be as specified in table II herein.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-06236</b>
		REVISION LEVEL <b>B</b>	SHEET <b>7</b>

TABLE II. Electrical test requirements.

Test requirements	Subgroups (in accordance with MIL-PRF-38535, table III)	
	Device class Q	Device class V
Interim electrical parameters (see 4.2)	---	---
Final electrical parameters (see 4.2)	1, 2, 3 <u>1/</u>	1, 2, 3 <u>1/</u>
Group A test requirements (see 4.4)	1, 2, 3	1, 2, 3
Group C end-point electrical parameters (see 4.4)	1, 2, 3	1, 2, 3
Group D end-point electrical parameters (see 4.4)	1	1
Group E end-point electrical parameters (see 4.4)	—	—

1/ PDA applies to subgroup 1.

4.4.4 Group E inspection. Group E inspection is required only for parts intended to be marked as radiation hardness assured (see 3.5 herein).

- a. End-point electrical parameters shall be as specified in table II herein.
- b. For device classes Q and V, the devices or test vehicle shall be subjected to radiation hardness assured tests as specified in MIL-PRF-38535 for the RHA level being tested. All device classes must meet the postirradiation end-point electrical parameter limits as defined in table I at  $T_A = +25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ , after exposure, to the subgroups specified in table II herein.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-06236</b>
		REVISION LEVEL <b>B</b>	SHEET <b>8</b>

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38535 for device classes Q and V.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.1.1 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor prepared specification or drawing.

6.2 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished using DD Form 1692, Engineering Change Proposal, or email communication.

6.3 Record of users. Military and industrial users should inform DLA Land and Maritime when a system application requires configuration control and which SMD's are applicable to that system. DLA Land and Maritime will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DLA Land and Maritime-VA, telephone (614) 692-8108.

6.4 Comments. Comments on this drawing should be directed to DLA Land and Maritime-VA, Columbus, Ohio 43218-3990, or telephone (614) 692-0591.

6.5 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535 and MIL-HDBK-1331.

6.6 Sources of supply.

6.6.1 Sources of supply for device classes Q and V. Sources of supply for device classes Q and V are listed in MIL-HDBK-103 and QML-38535. The vendors listed in MIL-HDBK-103 and QML-38535 have submitted a certificate of compliance (see 3.6 herein) to DLA Land and Maritime-VA and have agreed to this drawing.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		<b>5962-06236</b>
		REVISION LEVEL <b>B</b>	SHEET <b>9</b>

STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 25-02-04

Approved sources of supply for SMD 5962-06236 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38535 during the next revision. MIL-HDBK-103 and QML-38535 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DLA Land and Maritime-VA. This information bulletin is superseded by the next dated revision of MIL-HDBK-103 and QML-38535. DLA Land and Maritime maintains an online database of all current sources of supply at <https://landandmaritimeapps.dla.mil/programs/smcr/>.

Standard microcircuit drawing PIN <u>1/</u>	Vendor CAGE number	Vendor similar PIN <u>2/</u>
5962-0623601QPC	<u>3/</u>	EL5132
5962-0623602QPC	<u>3/</u>	EL5133

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.
- 2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.
- 3/ Not available from an approved source of supply. The last known supplier is listed below.

Vendor CAGE  
number

Vendor name  
and address

34371

Renesas Electronics America, Inc.  
1650 Robert J. Conlan Blvd. NE  
Palm Bay, FL 32905-3406

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in the information bulletin.