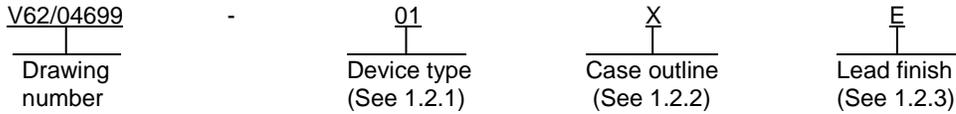




1. SCOPE

1.1 Scope. This drawing documents the general requirements of a high performance dual 4-line to 1-line data selector/ multiplexer with three-state outputs microcircuit, with an operating temperature range of -40°C to +125°C.

1.2 Vendor Item Drawing Administrative Control Number. The manufacturer's PIN is the item of identification. The vendor item drawing establishes an administrative control number for identifying the item on the engineering documentation:



1.2.1 Device type(s).

<u>Device type</u>	<u>Generic</u>	<u>Circuit function</u>
01	74HC253-EP	Dual 4-line to 1-line data selector/multiplexer with three-state outputs

1.2.2 Case outline(s). The case outlines are as specified herein.

<u>Outline letter</u>	<u>Number of pins</u>	<u>JEDEC PUB 95</u>	<u>Package style</u>
X	16	MS-012	Plastic small-outline package

1.2.3 Lead finishes. The lead finishes are as specified below or other lead finishes as provided by the device manufacturer:

<u>Finish designator</u>	<u>Material</u>
A	Hot solder dip
B	Tin-lead plate
C	Gold plate
D	Palladium
E	Gold flash palladium
Z	Other

<b>DEFENSE SUPPLY CENTER, COLUMBUS COLUMBUS, OHIO</b>	SIZE <b>A</b>	CODE IDENT NO. <b>16236</b>	DWG NO. <b>V62/04699</b>
		REV      B	PAGE    2

1.3 Absolute maximum ratings. 1/

Supply voltage range ( $V_{CC}$ ) .....	-0.5 V to 7.0 V
Input clamp current ( $I_{IK}$ ) ( $V_I < 0$ or $V_I > V_{CC}$ ) .....	$\pm 20$ mA 2/
Output clamp current ( $I_{OK}$ ) ( $V_O < 0$ or $V_O > V_{CC}$ ) .....	$\pm 20$ mA 2/
Continuous output current ( $I_O$ ) ( $V_O = 0$ to $V_{CC}$ ) .....	$\pm 25$ mA
Continuous current through $V_{CC}$ or GND .....	$\pm 50$ mA
Storage temperature range ( $T_{STG}$ ) .....	-65°C to 150°C
Package thermal impedance ( $\theta_{JA}$ ): 3/	
X package .....	73°C/W

1.4 Recommended operating conditions. 4/ 5/

Supply voltage range ( $V_{CC}$ ) .....	2.0 V to 6.0 V
Input voltage range ( $V_I$ ) .....	0.0 V to $V_{CC}$
Output voltage range ( $V_O$ ) .....	0.0 V to $V_{CC}$
Minimum high level input voltage ( $V_{IH}$ ):	
$V_{CC} = 2.0$ V .....	1.5 V
$V_{CC} = 4.5$ V .....	3.15 V
$V_{CC} = 6.0$ V .....	4.2 V
Maximum low level input voltage ( $V_{IL}$ ):	
$V_{CC} = 2.0$ V .....	0.5 V
$V_{CC} = 4.5$ V .....	1.35 V
$V_{CC} = 6.0$ V .....	1.8 V
Maximum input transition rise or fall time ( $\Delta t/\Delta v$ ):	
$V_{CC} = 2.0$ V .....	1000 ns
$V_{CC} = 4.5$ V .....	500 ns
$V_{CC} = 6.0$ V .....	400 ns
Operating free-air temperature range ( $T_A$ ) .....	-40°C to +125°C

2. APPLICABLE DOCUMENTS

JEDEC – SOLID STATE TECHNOLOGY ASSOCIATION (JEDEC)

JEP95	–	Registered and Standard Outlines for Semiconductor Devices
JESD51-7	–	High Effective Thermal Conductivity Test Board for Leaded Surface Mount Packages

(Copies of these documents are available online at <http://www.jedec.org> or from JEDEC – Solid State Technology Association, 3103 North 10th Street, Suite 240–S, Arlington, VA 22201-2107).

- 1/ Stresses beyond those listed under “absolute maximum rating” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- 2/ The input and output voltage ratings may be exceeded if the input and output current ratings are observed.
- 3/ The package thermal impedance is calculated in accordance with JESD 51-7.
- 4/ All unused inputs of the device must be held at  $V_{CC}$  or GND to ensure proper device operation.
- 5/ Use of this product beyond the manufacturers design rules or stated parameters is done at the user’s risk. The manufacturer and/or distributor maintain no responsibility or liability for product used beyond the stated limits.

<b>DEFENSE SUPPLY CENTER, COLUMBUS COLUMBUS, OHIO</b>	<b>SIZE A</b>	<b>CODE IDENT NO. 16236</b>	<b>DWG NO. V62/04699</b>
		REV B	PAGE 3

### 3. REQUIREMENTS

3.1 Marking. Parts shall be permanently and legibly marked with the manufacturer's part number as shown in 6.3 herein and as follows:

- A. Manufacturer's name, CAGE code, or logo
- B. Pin 1 identifier
- C. ESDS identification (optional)

3.2 Unit container. The unit container shall be marked with the manufacturer's part number and with items A and C (if applicable) above.

3.3 Electrical characteristics. The maximum and recommended operating conditions and electrical performance characteristics are as specified in 1.3, 1.4, and table I herein.

3.4 Design, construction, and physical dimensions. The design, construction, and physical dimensions are as specified herein.

3.5 Diagrams.

3.5.1 Case outline(s). The case outline(s) shall be as shown in 1.2.2 and figure 1.

3.5.2 Truth table. The truth table shall be as shown in figure 2.

3.5.3 Logic diagram. The logic diagram shall be as shown in figure 3.

3.5.4 Terminal connections. The terminal connections shall be as shown in figure 4.

3.5.5 Test circuit and timing waveforms. The test circuit and timing waveforms shall be as shown in figure 5.

<b>DEFENSE SUPPLY CENTER, COLUMBUS COLUMBUS, OHIO</b>	<b>SIZE A</b>	<b>CODE IDENT NO. 16236</b>	<b>DWG NO. V62/04699</b>
		REV      B	PAGE      4

TABLE I. Electrical performance characteristics. 1/

Test	Symbol	Conditions	V <sub>CC</sub>	Temperature, T <sub>A</sub>	Device type	Limits		Unit
						Min	Max	
High level output voltage	V <sub>OH</sub>	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> <u>2/</u> I <sub>OH</sub> = -20 μA	2.0 V	25°C, -40°C to 125°C	All	1.9		V
			4.5 V			4.4		
			6.0 V			5.9		
		V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> <u>2/</u> I <sub>OH</sub> = -6 mA	4.5 V	25°C		3.98		
			6.0 V	-40°C to 125°C		3.7		
				25°C		5.48		
Low level output voltage	V <sub>OL</sub>	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> <u>2/</u> I <sub>OL</sub> = 20 μA	2.0 V	25°C, -40°C to 125°C	All		0.1	V
			4.5 V			0.1		
			6.0 V			0.1		
		V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> <u>2/</u> I <sub>OL</sub> = 6 mA	4.5 V	25°C			0.26	
			6.0 V	-40°C to 125°C			0.4	
				25°C			0.26	
Input current	I <sub>I</sub>	V <sub>I</sub> = V <sub>CC</sub> or 0 V	6.0 V	25°C	All		±0.1	μA
				-40°C to 125°C		±1.0		
Three-state output leakage current	I <sub>OZ</sub>	V <sub>O</sub> = V <sub>CC</sub> or 0 V	6.0 V	25°C	All		±0.5	μA
				-40°C to 125°C		±10.0		
Quiescent supply current	I <sub>CC</sub>	V <sub>I</sub> = V <sub>CC</sub> or 0 V I <sub>O</sub> = 0 A	6.0 V	25°C	All		8.0	μA
				-40°C to 125°C		160.0		
Input capacitance	C <sub>I</sub>		2.0 V to 6.0 V	25°C, -40°C to 125°C	All		10	pF
Power dissipation capacitance per multiplexer	C <sub>PD</sub>	No load		25°C	All	45 typical		pF
Propagation delay time, A or B to any Y	t <sub>pd1</sub>	C <sub>L</sub> = 50 pF See figure 5	2.0 V	25°C	All		150	ns
				-40°C to 125°C		225		
			4.5 V	25°C		30		
				-40°C to 125°C		45		
			6.0 V	25°C		26		
				-40°C to 125°C		38		
		C <sub>L</sub> = 50 pF See figure 5	2.0 V	25°C		235		
				-40°C to 125°C		355		
			4.5 V	25°C		47		
				-40°C to 125°C		71		
			6.0 V	25°C		41		
				-40°C to 125°C		60		
Propagation delay time, output disable, $\overline{OE}$ to Y	t <sub>dis</sub>	C <sub>L</sub> = 50 pF See figure 5		25°C			135	ns
				-40°C to 125°C		203		
				25°C		30		
				-40°C to 125°C		45		
				25°C		35		
				-40°C to 125°C		38		

See footnote at end of table.

<b>DEFENSE SUPPLY CENTER, COLUMBUS COLUMBUS, OHIO</b>	SIZE <b>A</b>	CODE IDENT NO. <b>16236</b>	DWG NO. <b>V62/04699</b>
		REV <b>B</b>	PAGE <b>5</b>

TABLE I. Electrical performance characteristics - Continued. 1/

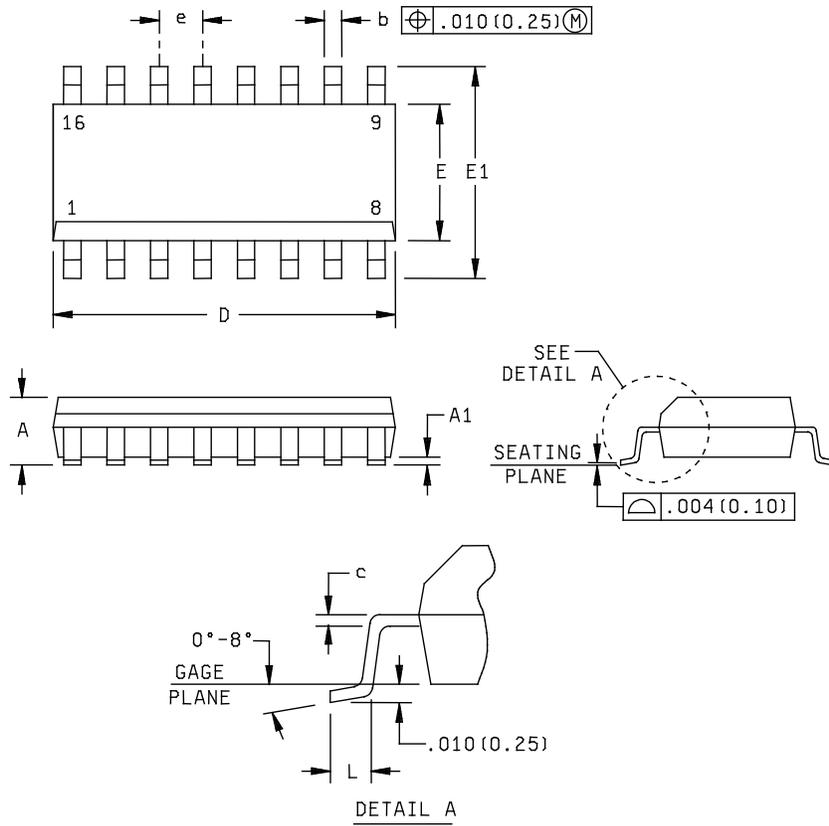
Test	Symbol	Conditions	V <sub>CC</sub>	Temperature, T <sub>A</sub>	Device type	Limits		Unit
						Min	Max	
Propagation delay time, data (any C) to Y	t <sub>pd2</sub>	C <sub>L</sub> = 50 pF See figure 5	2.0 V	25°C	All		126	ns
				-40°C to 125°C			210	
			4.5 V	25°C			28	
				-40°C to 125°C			42	
			6.0 V	25°C			23	
				-40°C to 125°C			36	
		C <sub>L</sub> = 150 pF See figure 5	2.0 V	25°C		220		
				-40°C to 125°C		335		
			4.5 V	25°C		44		
				-40°C to 125°C		67		
			6.0 V	25°C		38		
				-40°C to 125°C		57		
Propagation delay time, output enable, $\overline{\text{OE}}$ to Y	t <sub>en</sub>	C <sub>L</sub> = 50 pF See figure 5	2.0 V	25°C	All		100	ns
				-40°C to 125°C			150	
			4.5 V	25°C			20	
				-40°C to 125°C			30	
			6.0 V	25°C			17	
				-40°C to 125°C			26	
		C <sub>L</sub> = 150 pF See figure 5	2.0 V	25°C		185		
				-40°C to 125°C		280		
			4.5 V	25°C		37		
				-40°C to 125°C		56		
			6.0 V	25°C		32		
				-40°C to 125°C		48		
Output transition time	t <sub>t</sub>	C <sub>L</sub> = 50 pF See figure 5	2.0 V	25°C	All		60	ns
				-40°C to 125°C			90	
			4.5 V	25°C			12	
				-40°C to 125°C			18	
			6.0 V	25°C			10	
				-40°C to 125°C			15	
		C <sub>L</sub> = 150 pF See figure 5	2.0 V	25°C		210		
				-40°C to 125°C		315		
			4.5 V	25°C		42		
				-40°C to 125°C		63		
			6.0 V	25°C		36		
				-40°C to 125°C		53		

1/ Testing and other quality control techniques are used to the extent deemed necessary to assure product performance over the specified temperature range. Product may not necessarily be tested across the full temperature range and all parameters may not necessarily be tested. In the absence of specific parametric testing, product performance is assured by characterization and/or design.

2/ The values to be used for V<sub>IH</sub> and V<sub>IL</sub> shall be the V<sub>IH</sub> minimum and V<sub>IL</sub> maximum values listed in section 1.4 herein.

<b>DEFENSE SUPPLY CENTER, COLUMBUS COLUMBUS, OHIO</b>	<b>SIZE A</b>	<b>CODE IDENT NO. 16236</b>	<b>DWG NO. V62/04699</b>
		<b>REV B</b>	<b>PAGE 6</b>

Case X



Dimensions									
Symbol	Inches		Millimeters		Symbol	Inches		Millimeters	
	Min	Max	Min	Max		Min	Max	Min	Max
A		.069		1.75	E	.150	.157	3.81	4.00
A1	.004	.010	0.10	0.25	E1	.228	.244	5.80	6.20
b	.014	.020	0.35	0.51	e	.050 BSC		1.27 BSC	
c	.008 NOM		0.20 NOM		L	.016	.044	0.40	1.12
D	.386	.394	9.80	10.00					

NOTES:

1. All linear dimensions are in inches (millimeters).
2. This case outline is subject to change without notice.
3. Body dimensions do not include mold flash or protrusion, not to exceed .006 inches (0.15 millimeters).
4. Fall within JEDEC MS-012.

FIGURE 1. Case outline.

DEFENSE SUPPLY CENTER, COLUMBUS COLUMBUS, OHIO	SIZE A	CODE IDENT NO. 16236	DWG NO. V62/04699
		REV B	PAGE 7

Inputs							Output
Select <sup>1/</sup>		Data				$\overline{OE}$	Y
B	A	C0	C1	C2	C3		
X	X	X	X	X	X	H	Z
L	L	L	X	X	X	L	L
L	L	H	X	X	X	L	H
L	H	X	L	X	X	L	L
L	H	X	H	X	X	L	H
H	L	X	X	L	X	L	L
H	L	X	X	H	X	L	H
H	H	X	X	X	L	L	L
H	H	X	X	X	H	L	H

H = High voltage level

L = Low voltage level

Z = High impedance

X = Don't care

<sup>1/</sup> Select inputs A and B are common to both sections.

FIGURE 2. Truth table.

<b>DEFENSE SUPPLY CENTER, COLUMBUS COLUMBUS, OHIO</b>	<b>SIZE A</b>	<b>CODE IDENT NO. 16236</b>	<b>DWG NO. V62/04699</b>
		REV B	PAGE 8

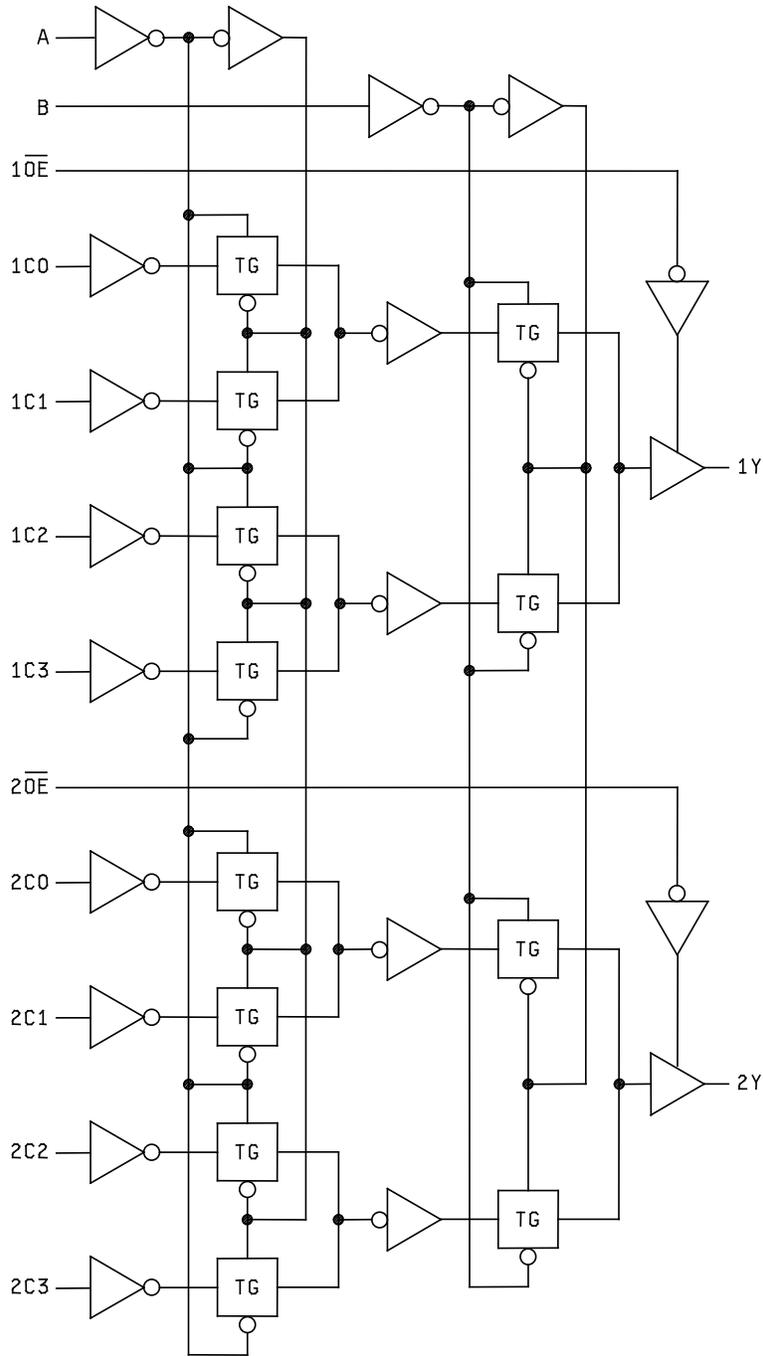


FIGURE 3. Logic diagram.

<b>DEFENSE SUPPLY CENTER, COLUMBUS COLUMBUS, OHIO</b>	<b>SIZE A</b>	<b>CODE IDENT NO. 16236</b>	<b>DWG NO. V62/04699</b>
		<b>REV     B</b>	<b>PAGE     9</b>

Device type 01			
Case outlines: X			
Terminal number	Terminal symbol	Terminal number	Terminal symbol
1	$1\overline{OE}$	9	2Y
2	B	10	2C0
3	1C3	11	2C1
4	1C2	12	2C2
5	1C1	13	2C3
6	1C0	14	A
7	1Y	15	$2\overline{OE}$
8	GND	16	$V_{CC}$

FIGURE 4. Terminal connections.

PARAMETER	$R_L$	$C_L$	S1	S2
$t_{en}$	1 k $\Omega$	50 pF or 150 pF	OPEN	CLOSED
			CLOSED	OPEN
$t_{dis}$	1 k $\Omega$	50 pF	OPEN	CLOSED
			CLOSED	OPEN
$t_{pd}$ or $t_t$	—	50 pF or 150 pF	OPEN	OPEN

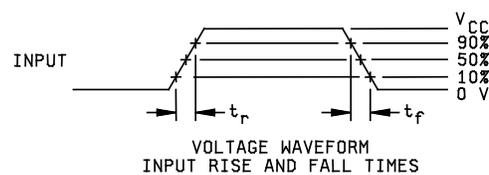
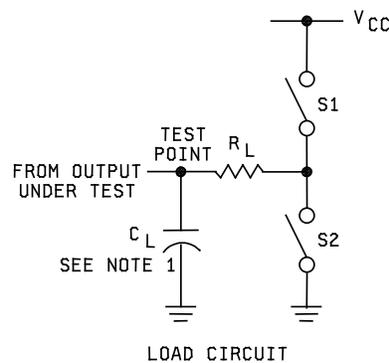
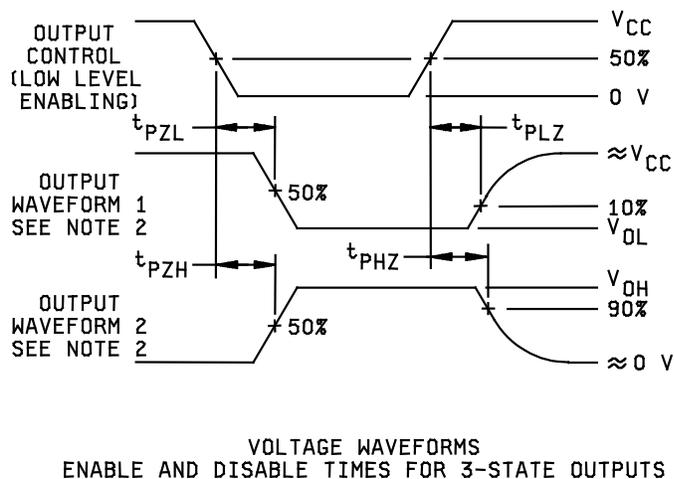
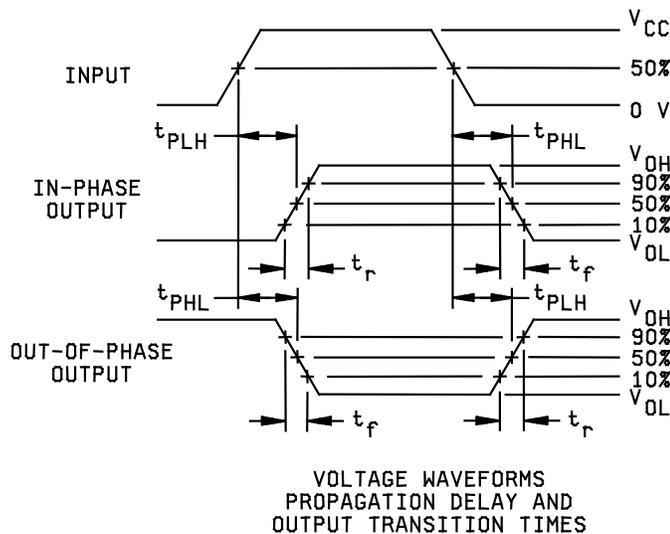


FIGURE 5. Test circuit and timing waveforms.

<b>DEFENSE SUPPLY CENTER, COLUMBUS COLUMBUS, OHIO</b>	<b>SIZE A</b>	<b>CODE IDENT NO. 16236</b>	<b>DWG NO. V62/04699</b>
		REV    B	PAGE    10



NOTES:

1.  $C_L$  includes probe and test-fixture capacitance.
2. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
3. Phase relationships between waveforms were chosen arbitrarily. All input pulses are supplied by generators having the following characteristics:  $PRR \leq 1 \text{ MHz}$ ,  $Z_O = 50 \Omega$ ,  $t_r = 6 \text{ ns}$ ,  $t_f = 6 \text{ ns}$ .
4. The outputs are measured one at a time with one input transition per measurement.
5.  $t_{PLZ}$  and  $t_{PHZ}$  are the same as  $t_{dis}$ ;  $t_{PZL}$  and  $t_{PZH}$  are the same as  $t_{en}$ ;  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{pd}$ .

FIGURE 5. Test circuit and timing waveforms - Continued.

DEFENSE SUPPLY CENTER, COLUMBUS COLUMBUS, OHIO	SIZE	CODE IDENT NO.	DWG NO.
	A	16236	V62/04699
	REV	B	PAGE 11

4. VERIFICATION

4.1 Product assurance requirements. The manufacturer is responsible for performing all inspection and test requirements as indicated in their internal documentation. Such procedures should include proper handling of electrostatic sensitive devices, classification, packaging, and labeling of moisture sensitive devices, as applicable.

5. PREPARATION FOR DELIVERY

5.1 Packaging. Preservation, packaging, labeling, and marking shall be in accordance with the manufacturer's standard commercial practices for electrostatic discharge sensitive devices.

6. NOTES

6.1 ESDS. Devices are electrostatic discharge sensitive and are classified as ESDS class 1 minimum.

6.2 Configuration control. The data contained herein is based on the salient characteristics of the device manufacturer's data book. The device manufacturer reserves the right to make changes without notice. This drawing will be modified as changes are provided.

6.3 Suggested source(s) of supply. Identification of the suggested source(s) of supply herein is not to be construed as a guarantee of present or continued availability as a source of supply for the item. DLA Land and Maritime maintains an online database of all current sources of supply at <http://www.landandmaritime.dla.mil/Programs/Smcr/>.

Vendor item drawing administrative control number <sup>1/</sup>	Device manufacturer CAGE code	Vendor part number	Top side marking
V62/04699-01XE	01295	SN74HC253QDREP	SHC253QEP

<sup>1/</sup> The vendor item drawing establishes an administrative control number for identifying the item on the engineering documentation.

CAGE code

01295

Source of supply

Texas Instruments, Inc.  
 Semiconductor Group  
 8505 Forest Lane  
 P.O. Box 660199  
 Dallas, TX 75243  
 Point of contact: U.S. Highway 75 South  
 P.O. Box 84, M/S 853  
 Sherman, TX 75090-9493

<b>DEFENSE SUPPLY CENTER, COLUMBUS COLUMBUS, OHIO</b>	<b>SIZE A</b>	<b>CODE IDENT NO. 16236</b>	<b>DWG NO. V62/04699</b>
		<b>REV B</b>	<b>PAGE 12</b>