

REVISIONS

LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Make pin number correction to case outline letter X as specified in 1.2.2 and figure 1. - ro	04-06-25	R. MONNIN
B	Add device type -02 and case outline Y. - CFS	06-05-30	Thomas M. Hess
C	Activate case outline X for device type 02. - CFS	07-10-11	Thomas M. Hess
D	Update boilerplate to current MIL-PRF-38535 requirements. - PHN	15-11-24	Thomas M. Hess
E	Update boilerplate paragraphs to current VID description requirements. - PHN	22-07-14	Muhammad A. Akbar



**CURRENT DESIGN ACTIVITY CAGE CODE 16236
HAS CHANGED NAMES TO:
DLA LAND AND MARITIME
COLUMBUS, OHIO 43218-3990**

Prepared in accordance with ASME Y14.24

Vendor Item Drawing

Revision Status of Sheets

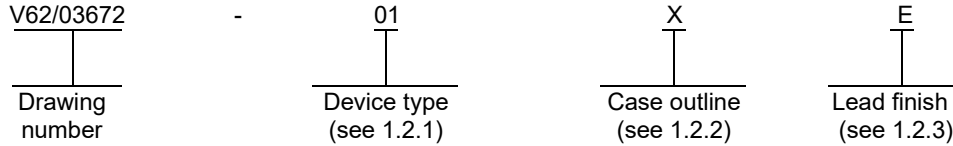
REV SHEET																				
REV SHEET																				
REV SHEET	E	E	E	E	E	E	E	E	E	E										
SHEET	1	2	3	4	5	6	7	8	9	10										

PMIC N/A Original date of drawing YY MM DD 03-07-29	PREPARED BY Charles F. Saffle		DEFENSE SUPPLY CENTER, COLUMBUS COLUMBUS, OHIO 43218-3990 https://www.dla.mil/LandandMaritime	
	CHECKED BY Charles F. Saffle		TITLE MICROCIRCUIT, LINEAR, QUAD DIFFERENTIAL COMPARATOR, MONOLITHIC SILICON	
	APPROVED BY Thomas M. Hess			
	SIZE A	CAGE CODE 16236	DWG NO. V62/03672	
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1. SCOPE

1.1 Scope. This drawing documents the general requirements of a high performance quad differential comparator microcircuit, with an operating temperature range of -40°C to +125°C for device type 01, and an operating temperature range of -55°C to +125°C for device type 02.

1.2 Vendor Item Drawing Administrative Control Number. The manufacturer's PIN is the item of identification. The vendor item drawing establishes an administrative control number for identifying the item on the engineering documentation:



1.2.1 Device type(s).

<u>Device type</u>	<u>Generic</u>	<u>Circuit function</u>
01	LM239A-EP	Quad differential comparator
02	LM239A-EP	Quad differential comparator

1.2.2 Case outline(s). The case outline(s) are as specified herein.

<u>Outline letter</u>	<u>Number of pins</u>	<u>JEDEC PUB 95</u>	<u>Package style</u>
X	14	MS-012	Plastic small outline
Y	14	MO-153	Plastic small outline

1.2.3 Lead finishes. The lead finishes are as specified below or other lead finishes as provided by the device manufacturer:

<u>Finish designator</u>	<u>Material</u>
A	Hot solder dip
B	Tin-lead plate
C	Gold plate
D	Palladium
E	Gold flash palladium
F	Tin-lead alloy
Z	Other

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1.3 Absolute maximum ratings. 1/ 2/

Supply voltage (V _{CC})	36 V
Differential input voltage (V _{ID})	±36 V 3/
Input voltage range (V _{IN}) (either input)	-0.3 V to 36 V
Output voltage (V _O)	36 V
Output current (I _O)	20 mA
Duration of output short circuit to ground	Unlimited 4/
Package thermal impedance (θ _{JA})	86°C/W 5/
Lead temperature 1.6 mm (1/16 inch) from case for 10 seconds	260°C
Maximum operating junction temperature (T _J)	136°C
Storage temperature range (T _{STG})	-65°C to 150°C 6/

1.4 Recommended operating conditions. 7/

Operating free-air temperature range (T _A):	
Device type 01	-40°C to +125°C
Device type 02	-55°C to +125°C

-
- 1/ Stresses beyond those listed under “absolute maximum rating” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- 2/ All voltage values, except differential voltages, are with respect to network ground.
- 3/ Differential voltages are at IN+ with respect to IN-.
- 4/ Short circuits from outputs to V_{CC} can cause excessive heating and eventual destruction.
- 5/ The package thermal impedance is calculated in accordance with JESD 51-7.
- 6/ Long term high temperature storage and/or extended use at maximum recommended operating conditions may result in a reduction of overall device life.
- 7/ Use of this product beyond the manufacturers design rules or stated parameters is done at the user’s risk. The manufacturer and/or distributor maintain no responsibility or liability for product used beyond the stated limits.

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2. APPLICABLE DOCUMENTS

JEDEC – SOLID STATE TECHNOLOGY ASSOCIATION (JEDEC)

- JEP95 – Registered and Standard Outlines for Semiconductor Devices
- JESD51-7 – High Effective Thermal Conductivity Test Board for Leaded Surface Mount Packages

(Copies of these documents are available online at <https://www.jedec.org>).

3. REQUIREMENTS

3.1 Marking. Parts shall be permanently and legibly marked with the manufacturer's part number as shown in 6.3 herein and as follows:

- A. Manufacturer's name, CAGE code, or logo
- B. Pin 1 identifier
- C. ESDS identification (optional)

3.2 Unit container. The unit container shall be marked with the manufacturer's part number and with items A and C (if applicable) above.

3.3 Electrical characteristics. The maximum and recommended operating conditions and electrical performance characteristics are as specified in 1.3, 1.4, and table I herein.

3.4 Design, construction, and physical dimension. The design, construction, and physical dimensions are as specified herein.

3.5 Diagrams.

3.5.1 Case outline. The case outline shall be as shown in 1.2.2 and figure 1.

3.5.2 Terminal connections. The terminal connections shall be as shown in figure 2.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions ^{1/} ^{2/} V _{CC} = 5 V	Temperature, ^{3/} T _A	Device type	Lim its		Unit	
					Min	Max		
Input offset voltage	V _{IO}	V _{CC} = 5 V to 30 V, V _O = 1.4 V, V _{IC} = V _{ICR(min)}	+25°C	All		2.5	mV	
			-40°C to +125°C	01		5.5		
			-55°C to +125°C	02		5.5		
Input offset current	I _{IO}	V _O = 1.4 V	+25°C	All		50	nA	
			-40°C to +125°C	01		150		
			-55°C to +125°C	02		150		
Input bias current	I _{IB}	V _O = 1.4 V	+25°C	All		-250	nA	
			-40°C to +125°C	01		-400		
			-55°C to +125°C	02		-400		
Common mode input voltage range	V _{ICR}		+25°C	All	0 to V _{CC} -1.5		V	
			-40°C to +125°C	01	0 to V _{CC} -2			
			-55°C to +125°C	02	0 to V _{CC} -2			
Large signal differential voltage amplification	A _{VD}	V _{CC} = 15 V, V _O = 1.4 V to 11.4 V, R _L ≥ 15 kΩ to V _{CC}	+25°C	All	50		V/mV	
High level output current	I _{OH}	V _{OH} = 5 V, V _{ID} = 1 V	+25°C	All		50	nA	
		V _{OH} = 30 V, V _{ID} = 1 V	-40°C to +125°C	01		1		μA
			-55°C to +125°C	02		1		
Low level output voltage	V _{OL}	I _{OL} = 4 mA, V _{ID} = -1 V	+25°C	All		400	mV	
			-40°C to +125°C	01		700		
			-55°C to +125°C	02		700		
Low level output current	I _{OL}	V _{OL} = 1.5 V, V _{ID} = -1 V	+25°C	All	6		mA	
Supply current (four comparators)	I _{CC}	V _O = 2.5 V, no load	+25°C	All		2	mA	

See footnotes at end of table.

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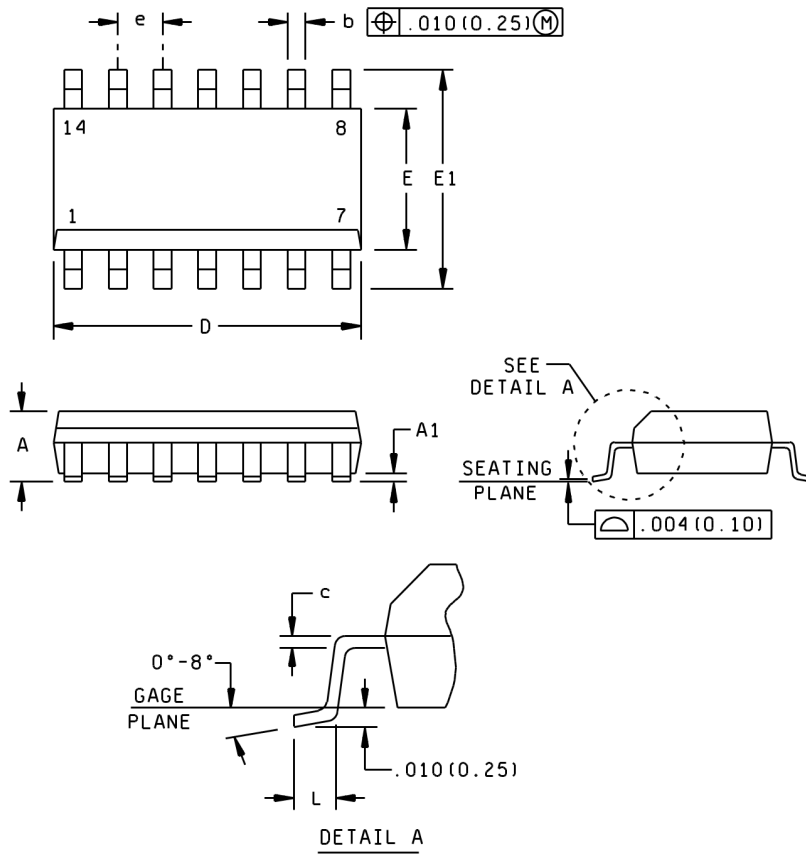
TABLE I. Electrical performance characteristics – continued.

Test	Symbol	Conditions <u>1/</u> <u>2/</u> $V_{CC} = 5\text{ V}$	Temperature, T_A <u>3/</u>	Device type	Limits		Unit
					Min	Max	
Response time		$C_L = 15\text{ pF}$, <u>4/</u> <u>5/</u> R_L connected to 5 V through 5.1 k Ω , 100 mV input step with 5 mV overdrive	+25°C	All	1.3	typical	μs
		$C_L = 15\text{ pF}$, <u>4/</u> <u>5/</u> R_L connected to 5 V through 5.1 k Ω , TTL level input step			0.3	typical	

- 1/ All characteristics are measured with zero common mode input voltage, unless otherwise specified.
- 2/ Testing and other quality control techniques are used to the extent deemed necessary to assure product performance over the specified temperature range. Product may not necessarily be tested across the full temperature range and all parameters may not necessarily be tested. In the absence of specific parametric testing, product performance is assured by characterization and/or design.
- 3/ Full range (MIN to MAX) for device type 01 is –40°C to +125°C. Full range (MIN to MAX) for device type 02 is –55°C to +125°C. All characteristics are measured with zero common mode input voltage, unless otherwise specified.
- 4/ C_L includes probe and jig capacitance.
- 5/ The response time specified is the interval between the input step function and the instant when the output crosses 1.4 V.

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Case X



Dimensions									
Symbol	Millimeters		Inches		Symbol	Millimeters		Inches	
	Min	Max	Min	Max		Min	Max	Min	Max
A	---	1.75	---	.069	E	3.81	4.00	.150	.157
A1	0.10	0.25	.004	.010	E1	5.80	6.20	.228	.244
b	0.35	0.51	.014	.020	e	1.27	NOM	.050	NOM
c	0.20	NOM	.008	NOM	L	0.40	1.12	.016	.044
D	8.55	8.75	.337	.344					

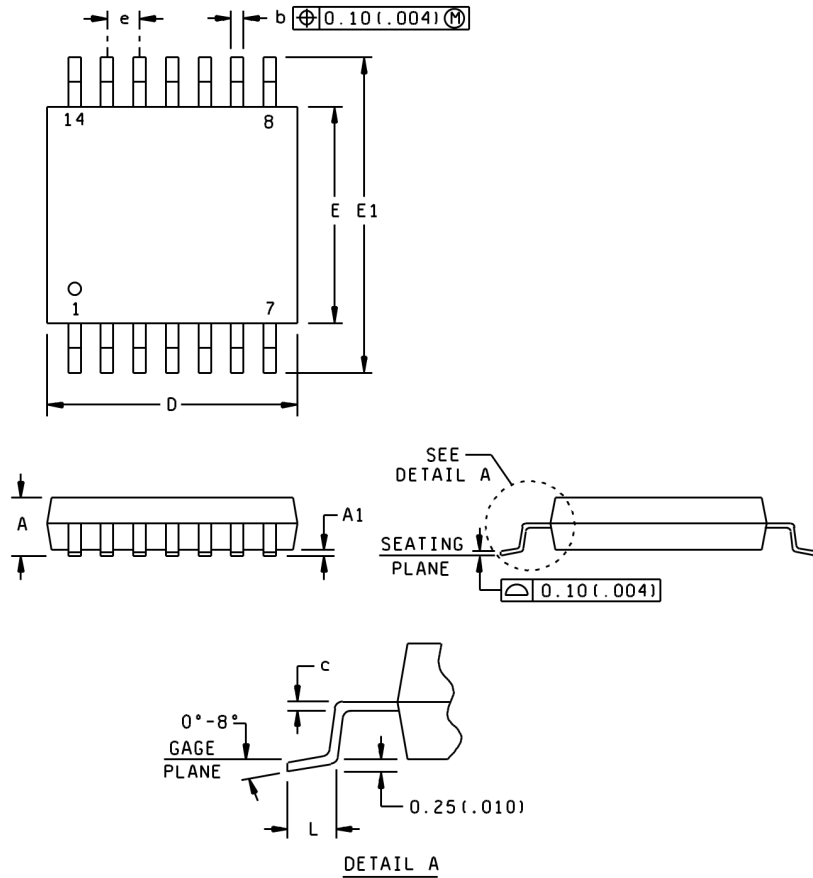
NOTES:

1. This drawing is subject to change without notice.
2. Body dimensions do not include mold flash or protrusion not to exceed 0.006 inches (0.15 mm).
3. Falls within JEDEC MS-012.
4. All linear dimensions are shown in inches (millimeters). Metric equivalents are given for general information only.

FIGURE 1. Case outlines - Continued.

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Case Y



Dimensions									
Symbol	Millimeters		Inches		Symbol	Millimeters		Inches	
	Min	Max	Min	Max		Min	Max	Min	Max
A	---	1.20	---	.047	E	4.30	4.50	.169	.177
A1	0.05	0.15	.002	.006	E1	6.20	6.60	.244	.260
b	0.19	0.30	.007	.012	e	0.65	NOM	.026	NOM
c	0.15	NOM	.006	NOM	L	0.50	0.75	.020	.030
D	4.90	5.10	.193	.201					

NOTES:

1. This drawing is subject to change without notice.
2. Body dimensions do not include mold flash or protrusion not to exceed 0.15 mm.
3. Falls within JEDEC MO-153.
4. All linear dimensions are shown in millimeters (inches). Inches equivalents are given for general information only.

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Device type:	01	02
Case outline:	X	X and Y
Terminal number	Terminal symbol	Terminal symbol
1	1 OUT	1 OUT
2	2 OUT	2 OUT
3	Vcc	Vcc
4	2 IN-	2 IN-
5	2 IN+	2 IN+
6	1 IN-	1 IN-
7	1 IN+	1 IN+
8	3 IN-	3 IN-
9	3 IN+	3 IN+
10	4 IN-	4 IN-
11	4 IN+	4 IN+
12	GND	GND
13	4 OUT	4 OUT
14	3 OUT	3 OUT

FIGURE 2. Terminal connections.

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4. VERIFICATION

4.1 Product assurance requirements. The manufacturer is responsible for performing all inspection and test requirements as indicated in their internal documentation. Such procedures should include proper handling of electrostatic sensitive devices, classification, packaging, and labeling of moisture sensitive devices, as applicable.

5. PREPARATION FOR DELIVERY

5.1 Packaging. Preservation, packaging, labeling, and marking shall be in accordance with the manufacturer’s standard commercial practices for electrostatic discharge sensitive devices.

6. NOTES

6.1 ESDS. Devices are electrostatic discharge sensitive and are classified as ESDS class 1 minimum.

6.2 Configuration control. The data contained herein is based on the salient characteristics of the device manufacturer’s data book. The device manufacturer reserves the right to make changes without notice. This drawing will be modified as changes are provided.

6.3 Suggested source(s) of supply. Identification of the suggested source(s) of supply herein is not to be construed as a guarantee of present or continued availability as a source of supply for the item. DLA Land and Maritime maintains an online database of all current sources of supply at <https://landandmaritimeapps.dla.mil/programs/smcr/>.

Vendor item drawing administrative control number <u>1/</u>	Device manufacturer CAGE code	Vendor part number	Top side marking
V62/03672-01XE	01295	LM239AQDREP <u>2/</u>	LM239AEP
V62/03672-02XE	01295	LM239AMDREP <u>2/</u>	LM239AME
V62/03672-02YE	01295	LM239AMPWREP	LM239AE

1/ The vendor item drawing establishes an administrative control number for identifying the item on the engineering documentation.

2/ This package is only available taped and reeled with standard quantities of 2500 pieces per reel.

CAGE code

01295

Source of supply

Texas Instruments, Inc.
Semiconductor Group
8505 Forest Lane
P.O. Box 660199
Dallas, TX 75243

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