REVISIONS			
LTR	**DESCRIPTION**	**DATE**	**APPROVED**
A	Add device types 10 and 11. - phn	08-12-08	Thomas M. Hess
B	Update boilerplate paragraphs to current requirements. Correct top-side part marking designators on sheet 10 for device types 01 through 09. - CFS	09-03-19	Thomas M. Hess
C	Add typical limits to output current limit and input current $\overline{EN}$ parameters under Table I. Add inch dimensions to figure 1. Make changes to notes 1 and 3 under figure 1. Add terminal symbol descriptions to figure 2. Update document paragraphs to current requirements. - ro	15-04-09	Charles F. Saffle

CURRENT DESIGN ACTIVITY CAGE CODE 16236
HAS CHANGED NAMES TO:
DLA LAND AND MARITIME
COLUMBUS, OHIO 43218-3990

Prepared in accordance with ASME Y14.24

Vendor item drawing

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REV STATUS OF PAGES	**REV**		C	C	C	C	C	C	C	C	C	C	C	C	C							
	PAGE		1	2	3	4	5	6	7	8	9	10	11	12	13							

PMIC N/A	**PREPARED BY** Phu H. Nguyen	**DEFENSE SUPPLY CENTER, COLUMBUS** **COLUMBUS, OHIO 43218-3990**
Original date of drawing YY-MM-DD 03-06-09	**CHECKED BY** Phu H. Nguyen	**TITLE** MICROCIRCUIT, LINEAR, FAST-TRANSIENT-RESPONSE 1-A LOW DROPOUT VOLTAGE REGULATORS, MONOLITHIC SILICON
	APPROVED BY Thomas M. Hess	
	SIZE **A** / **CODE IDENT. NO.** **16236**	**DWG NO.** **V62/03632**
	REV C	**PAGE** 1 **OF** 13

1. SCOPE

1.1 Scope. This drawing documents the general requirements of a fast-transient-response1-A low dropout voltage regulators, with an operating temperature range of -40°C to +125°C (device types 01 to 09) and an extended operating temperature range of -55°C to +125°C (device types 10 and 11).

1.2 Vendor Item Drawing Administrative Control Number. The manufacturers PIN is the item of identification. The vendor item drawing establishes an administrative control number for identifying the item on the engineering documentation:

V62/03632	01	X	E
Drawing number	Device type (See 1.2.1)	Case outline (See 1.2.2)	Lead finish (See 1.2.3)

1.2.1 Device type(s). 1/

Device type	Generic		Output voltage	Circuit function
01	TPS76801-EP	2/	+1.2 V to +5.5 V	Fast-transient-response 1-A low-dropout voltage regulator.
02	TPS76815-EP	2/	+1.5 V	Fast-transient-response 1-A low-dropout voltage regulator.
03	TPS76818-EP	2/	+1.8 V	Fast-transient-response 1-A low-dropout voltage regulator.
04	TPS76825-EP	2/	+2.5 V	Fast-transient-response 1-A low-dropout voltage regulator.
05	TPS76827-EP	2/	+2.7 V	Fast-transient-response 1-A low-dropout voltage regulator.
06	TPS76828-EP	2/	+2.8 V	Fast-transient-response 1-A low-dropout voltage regulator.
07	TPS76830-EP	2/	+3.0 V	Fast-transient-response 1-A low-dropout voltage regulator.
08	TPS76833-EP	2/	+3.3 V	Fast-transient-response 1-A low-dropout voltage regulator.
09	TPS76850-EP	2/	+5.0 V	Fast-transient-response 1-A low-dropout voltage regulator.
10	TPS76801-EP	3/	+1.2 V to +5.5 V	Fast-transient-response 1-A low-dropout voltage regulator.
11	TPS76850-EP	3/	+5.0 V	Fast-transient-response 1-A low-dropout voltage regulator.

1.2.2 Case outline(s). The case outline(s) are as specified herein.

Outline letter	Number of pins	JEDEC PUB 95	Package style
X	20	JEDEC MO-153	Plastic small outline

1.2.3 Lead finishes. The lead finishes are as specified below or other lead finishes as provided by the device manufacturer:

Finish designator	Material
A	Hot solder dip
B	Tin-lead plate
C	Gold plate
D	Palladium
E	Gold flash palladium
Z	Other

1/ Users are cautioned to review the manufacturers data manual for additional user information relating to these devices.
2/ Operated at -40°C to +125°C.
3/ Operated at -55°C to +125°C.

1.3 Absolute maximum ratings. 4/

Input voltage range (V_I) ..... -0.3 V to +13.5 V 5/
Voltage range at $\overline{EN}$ ..... -0.3 V to V_I + 0.3 V
Maximum PG voltage ..... +16.5 V
Peak output current ..... Internally limited
Continuous total power dissipation ..... See dissipation rating tables
Output voltage (V_O) (OUT, FB) ..... +7.0 V
Operating virtual junction temperature range (T_J):
Device types 01 to 09 ..... -40°C to +125°C
Device types 10 and 11 ..... -55°C to +125°C
Storage temperature range (T_{STG}) ..... -65°C to +150°C
ESD rating, (HBM) ..... 2 kV

Dissipation Rating Table – Ambient Temperatures

Case outline	Air flow (CFM)	T_A < 25°C power rating	Derating factor above T_A = 25°C	T_A = 70°C power rating	T_A = 85°C power rating
X 6/	0	2.9 W	23.5 mW/°C	1.9 W	1.5 W
	300	4.3 W	34.6 mW/°C	2.8 W	2.2 W
X 7/	0	3.0 W	23.8 mW/°C	1.9 W	1.5 W
	300	7.2 W	57.9 mW/°C	4.6 W	3.8 W

1.4 Recommended operating conditions. 8/

Input voltage range (V_I) ..... 2.7 V to 10.0 V 9/
Output voltage (V_O) ..... 1.2 V to 5.5 V
Output current (I_O) ..... 0 A to 1.0 A 10/
Operating virtual junction temperature range (T_J) ..... -40°C to +125°C 10/

4/ Stresses beyond those listed under “absolute maximum rating” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
5/ All voltage values are with respect to network terminal ground.
6/ This parameter is measured with the recommended copper heat sink pattern on a 1-layer printed circuit board (PCB), 5 in x 5 in PCB, 1 oz. copper, 2 in x 2 in coverage.
7/ This parameter is measured with the recommended copper heat sink pattern on a 8-layer PCB, 1.5 in x 2 in PCB, 1 oz. copper with layer 1, 2, 4, 5, 7, and 8 at 5% coverage (0.9 in^2) and layers 3 and 6 at 100% coverage (6 in^2). For more information, refer to the manufacturer technical brief SLMA002.
8/ Use of this product beyond the manufacturers design rules or stated parameters is done at the user’s risk. The manufacturer and/or distributor maintain no responsibility or liability for product used beyond the stated limits.
9/ To calculate the minimum input voltage for your maximum output current, use the following equation: $V_{I(min)} = V_{O(max)} + V_{DO(max\ load)}$.
10/ Continuous current and operating junction temperature are limited by internal protection circuitry, but it is not recommended that the device operate under conditions beyond those specified in this table for extended periods of time.

2. APPLICABLE DOCUMENTS

JEDEC Solid State Technology Association

JEDEC PUB 95 – Registered and Standard Outlines for Semiconductor Devices

(Copies of these documents are available online at http:/www.jedec.org or from JEDEC – Solid State Technology Association, 3103 North 10th Street, Suite 240–S, Arlington, VA 22201-2107).

3. REQUIREMENTS

3.1 Marking. Parts shall be permanently and legibly marked with the manufacturer's part number as shown in 6.3 herein and as follows:

A. Manufacturer's name, CAGE code, or logo
B. Pin 1 identifier
C. ESDS identification (optional)

3.2 Unit container. The unit container shall be marked with the manufacturer's part number and with items A and C (if applicable) above.

3.3 Electrical characteristics. The maximum and recommended operating conditions and electrical performance characteristics are as specified in 1.3, 1.4, and table I herein.

3.4 Design, construction, and physical dimension. The design, construction, and physical dimensions are as specified herein.

3.5 Diagrams.

3.5.1 Case outline. The case outline shall be as shown in 1.2.2 and figure 1.

3.5.2 Terminal connections. The terminal connections shall be as specified on figure 2.

3.5.3 Block diagrams. The block diagrams shall be as specified on figure 3.

TABLE I. Electrical performance characteristics. 1/

Test	Symbol	Conditions $V_I = V_{O(Typ)} + 1$ V I_O = 1 mA, $\overline{EN}$ = 0 V, C_O = 10 μF, Operating at free air temperature range unless otherwise specified	Temperature, T_J	Device type	Limits		Unit
					Min	Max	
Output voltage 2/ (10 μA to 1 A load)		1.5 V ≤ V_O ≤ 5.5 V	+25°C	01, 10	V_O typical		V
			-40°C to 125°C	01, 10	0.98 V_O	1.02 V_O	
		2.7 V < V_{IN} < 10 V	+25°C	02	1.5 typical		
			-40°C to 125°C		1.470	1.530	
		2.8 V < V_{IN} < 10 V	+25°C	03	1.8 typical		
			-40°C to 125°C		1.764	1.836	
		3.5 V < V_{IN} < 10 V	+25°C	04	2.5 typical		
			-40°C to 125°C		2.450	2.550	
		3.7 V < V_{IN} < 10 V	+25°C	05	2.7 typical		
			-40°C to 125°C		2.646	2.754	
		3.8 V < V_{IN} < 10 V	+25°C	06	2.8 typical		
			-40°C to 125°C		2.744	2.856	
		4.0 V < V_{IN} < 10 V	+25°C	07	3.0 typical		
			-40°C to 125°C		2.940	3.060	
		4.3 V < V_{IN} < 10 V	+25°C	08	3.3 typical		
			-40°C to 125°C		3.234	3.366	
		6.0 V < V_{IN} < 10 V	+25°C	09, 11	5.0 typical		
			-40°C to 125°C		4.900	5.100	

See footnotes at end of table.

TABLE I. Electrical performance characteristics – Continued. 1/

Test	Symbol	Conditions $V_I = V_{O(Typ)} + 1$ V $I_O = 1$ mA, $\overline{EN} = 0$ V, $C_O = 10$ µF, Operating at free air temperature range unless otherwise specified	Temperature, T_J	Device type	Limits		Unit
					Min	Max	
Quiescent current 2/ (GND current)		10 µA < I_O < 1 A	+25°C	All	85 typical		µA
$\overline{EN} = 0$ V		$I_O = 1$ A	-40°C to 125°C	01 - 09		125	
Output voltage 2/ 3/ line regulation ($\Delta V_O/V_O$)		$V_O + 1$ V < V_I < 10 V	+25°C	All	0.01 typical		%/V
Load regulation			+25°C	All	3 typical		mV
Output noise voltage	V_N	BW = 200 Hz to 100 kHz, $I_C = 1$ A, $C_O = 10$ µF	+25°C	03	55 typical		µVrms
Output current limit	I_{CL}	$V_O = 0$ V	+25°C	All	1.7 typical		A
			-40°C to 125°C			2	
Thermal shutdown junction temperature	T_{SD}		+25°C	All	150 typical		°C
Standby current	I_{STBY}	$\overline{EN} = V_I$, 2.7 V < V_I < 10 V	+25°C	All	1 typical		µA
			-40°C to 125°C	01 - 09		10	
FB input current	I_{FB}	FB = 1.5 V	+25°C	01, 10	2 typical		nA

See footnotes at end of table.

TABLE I. Electrical performance characteristics – Continued. 1/

Test	Symbol	Conditions $V_I = V_{O(Typ)} + 1$ V I_O = 1 mA, $\overline{EN}$ = 0 V, C_O = 10 μF, Operating at free air temperature range unless otherwise specified	Temperature, T_J	Device type	Limits		Unit
					Min	Max	
High level enable input voltage			-40°C to 125°C	All	1.7		V
Low level enable input voltage			-40°C to 125°C	All		0.9	V
Power supply ripple 2/ ripple		f = 1 kHz, C_O = 10 μF	+25°C	All	60 typical		dB
PG							
Minimum input voltage for valid PG		$I_{O(PG)}$ = 300 μA	+25°C	All	1.1 typical		V
Trip threshold voltage		V_O decreasing	-40°C to 125°C	All	92	98	%/V_O
Hysteresis voltage		Measured at V_O	+25°C	All	0.5 typical		%/V_O
Output low voltage		V_I = 2.7 V, $I_{O(PG)}$ = 1 A	+25°C	All	0.15 typical		V
			-40°C to 125°C			0.4	
Leakage current		$V_{(PG)}$ = 5 V	-40°C to 125°C	All		1	μA
Input current ($\overline{EN}$)		$\overline{EN}$ = 0 V	+25°C	All	0 typical		μA
			-40°C to 125°C		-1	1	
		$\overline{EN}$ = VI	-40°C to 125°C		-1	1	

See footnotes at end of table.

TABLE I. Electrical performance characteristics – Continued. 1/

Test	Symbol	Conditions $V_I = V_{O(Typ)} + 1$ V I_O = 1 mA, $\overline{EN}$ = 0 V, C_O = 10 μF, Operating at free air temperature range unless otherwise specified	Temperature, T_J	Device type	Limits		Unit
					Min	Max	
Dropout voltage 4/		I_O = 1 A	+25°C	06	500 typical		mV
			-40°C to 125°C			825	
			+25°C	07	450 typical		
			-40°C to 125°C			675	
			+25°C	08	350 typical		
			-40°C to 125°C			575	
			+25°C	09	230 typical		
			-40°C to 125°C			380	

1/ Testing and other quality control techniques are used to the extent deemed necessary to assure product performance over the specified temperature range. Product may not necessarily be tested across the full temperature range and all parameters may not necessarily be tested. In the absence of specific parametric testing, product performance is assured by characterization and/or design.

2/ Minimum IN operating voltage is 2.7 V or $V_{O(Typ)}$ + 1V, whichever is greater. Maximum IN voltage 10 V.

3/ If $V_O \leq 1.8$ V then V_{Imax} = 10 V, V_{Imin} = 2.7 V:

$$\text{Line Reg. (mV)} = (\%/V) \times \frac{V_O(V_{Imax} - 2.7V)}{100} \times 1000$$

If $V_O \geq 2.5$ V then V_{Imax} = 10 V, $V_{Imin} = V_O$ + 1 V:

$$\text{Line Reg. (mV)} = (\%/V) \times \frac{V_O(V_{Imax} - (V_O + 1V))}{100} \times 1000$$

4/ IN voltage equals $V_{O(Typ)}$ – 100 mV; device type 01 output voltage set to 3.3 V nominal with external resistor divider. Device types 02, 03, 04 and 05 dropout voltage limited by input voltage range limitations (i.e., device type 07 input voltage needs to drop to 2.9 V for purpose of this test).

Case X

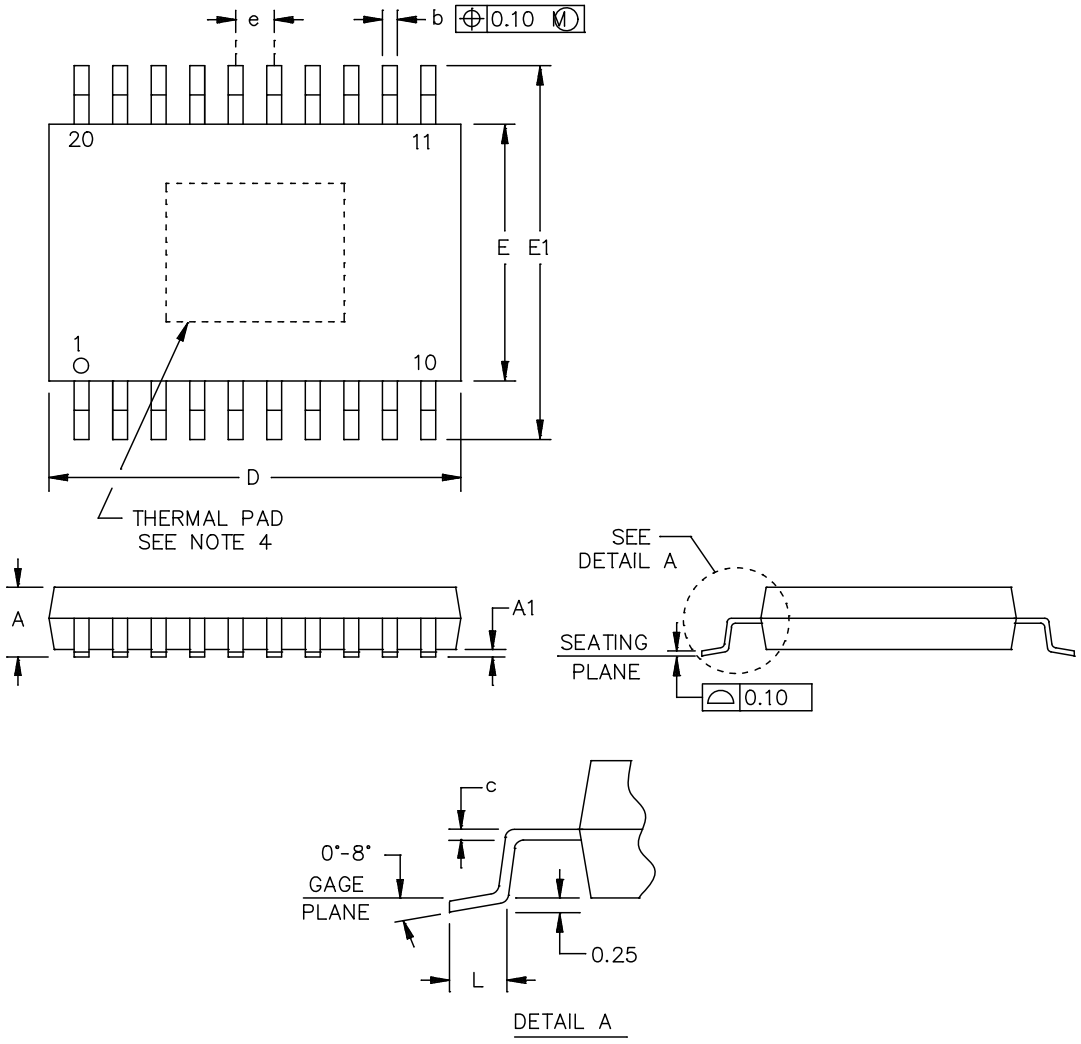


FIGURE 1. Case outline.

DEFENSE SUPPLY CENTER, COLUMBUS COLUMBUS, OHIO	SIZE A	CODE IDENT NO. 16236	DWG NO. V62/03632
		REV C	PAGE 9

Symbol	Dimensions			
	Inches		Millimeters	
	Min	Max	Min	Max
A	---	.047	---	1.20
A1	.001	.005	0.05	0.15
b	.007	.011	0.19	0.30
c	.005 nominal		0.15 nominal	
D	.251	.259	6.40	6.60
E	.169	.177	4.30	4.50
E1	.244	.259	6.20	6.60
e	.025 BSC		0.65 BSC	
L	.019	.029	0.50	0.75

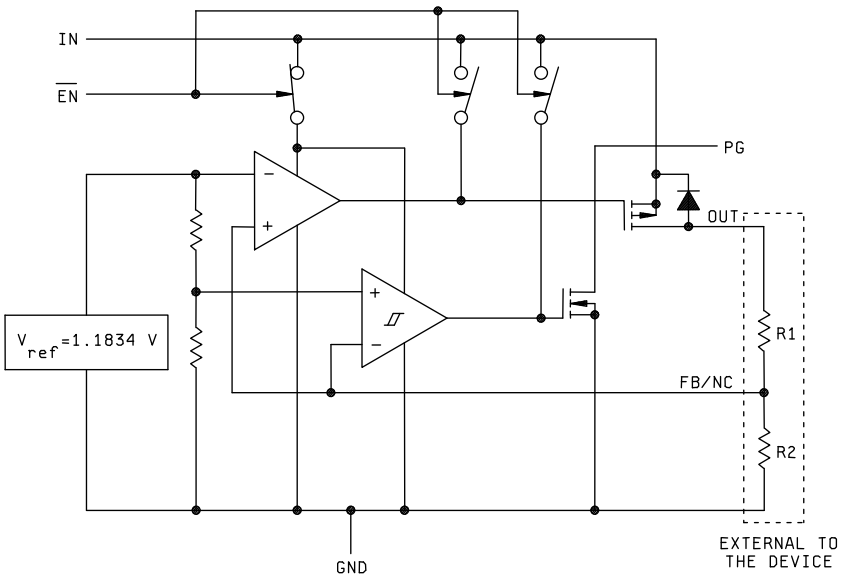
Notes:
1. Controlling dimensions are millimeter, inch dimensions are given for reference only.
2. This drawing is subject to change without notice.
3. Body dimensions do not include mold flash or protrusions. Mold flash and protrusion shall not exceed 0.15 mm (0.006 inch) per side.
4. The package thermal performance may be enhanced by bonding the thermal pad to an external thermal plane. This pad is electrically and thermally connected to the backside of the die and possibly selected leads.
5. Falls within JEDEC MO-153.

FIGURE 1. Case outline.

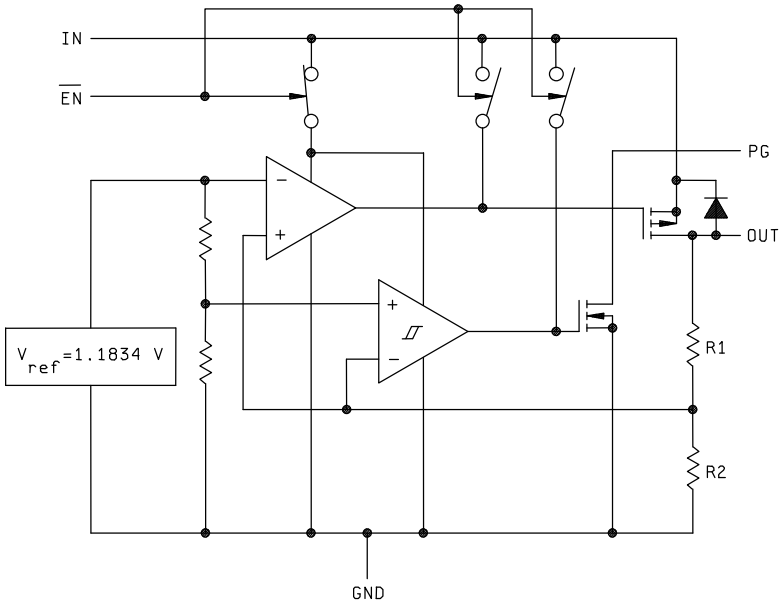
Device types	All	
Case outline	X	
Terminal number	Terminal symbol	Description
1	GND/HSINK	Ground/heatsink.
2	GND/HSINK	Ground/heatsink.
3	GND	Low dropout (LDO) ground.
4	NC	No connect.
5	$\overline{EN}$	Enable input.
6	IN	Input voltage.
7	IN	Input voltage.
8	NC	No connect.
9	GND/HSINK	Ground/heatsink.
10	GND/HSINK	Ground/heatsink.
11	GND/HSINK	Ground/heatsink.
12	GND/HSINK	Ground/heatsink.
13	OUT	Regulated output voltage.
14	OUT	Regulated output voltage.
15	FB/NC	Feedback input voltage for adjustable device (no connect for fixed options).
16	PG	Power good (PG) output
17	NC	No connect.
18	NC	No connect.
19	GND/HSINK	Ground/heatsink.
20	GND/HSINK	Ground/heatsink.

NC: No internal connection

FIGURE 2. Terminal connections.



Functional block diagram – adjustable version.



Functional block diagram – fixed voltage version.

FIGURE 3. Block diagrams.

4. VERIFICATION

4.1 Product assurance requirements. The manufacturer is responsible for performing all inspection and test requirements as indicated in their internal documentation. Such procedures should include proper handling of electrostatic sensitive devices, classification, packaging, and labeling of moisture sensitive devices, as applicable.

5. PREPARATION FOR DELIVERY

5.1 Packaging. Preservation, packaging, labeling, and marking shall be in accordance with the manufacturer's standard commercial practices for electrostatic discharge sensitive devices.

6. NOTES

6.1 ESDS. Devices are electrostatic discharge sensitive and are classified as ESDS class 1 minimum.

6.2 Configuration control. The data contained herein is based on the salient characteristics of the device manufacturer's data book. The device manufacturer reserves the right to make changes without notice. This drawing will be modified as changes are provided.

6.3 Suggested source(s) of supply. Identification of the suggested source(s) of supply herein is not to be construed as a guarantee of present or continued availability as a source of supply for the item. DLA Land and Maritime maintains an online database of all current sources of supply at http://www.landandmaritime.dla.mil/Programs/Smcr/.

Vendor item drawing administrative control number 1/	Device manufacturer CAGE code	Top-side marking	Vendor part number 2/
V62/03632-01XE	01295	76801QE	TPS76801QPWPREP 3/
V62/03632-02XE	01295	76815QE	TPS76815QPWPREP
V62/03632-03XE	01295	76818QE	TPS76818QPWPREP
V62/03632-04XE	01295	76825QE	TPS76825QPWPREP
V62/03632-05XE	4/	76827QE	TPS76827QPWPREP
V62/03632-06XE	4/	76828QE	TPS76828QPWPREP
V62/03632-07XE	4/	76830QE	TPS76830QPWPREP
V62/03632-08XE	01295	76833QE	TPS76833QPWPREP
V62/03632-09XE	01295	76850QE	TPS76850QPWPREP
V62/03632-10XE	01295	76801ME	TPS76801MPWPREP
V62/03632-11XE	01295	76850ME	TPS76850MPWPREP

1/ The vendor item drawing establishes an administrative control number for identifying the item on the engineering documentation.
2/ The PWP package is available taped and reeled. Note R suffix to the device type (e.g., TPS76801QPWPREP).
3/ This device is programmable using an external resistor divider (see manufacturer application information).
4/ Not available from an approved source of supply.

CAGE code	Source of supply
01295	Texas Instruments, Inc. Semiconductor Group 8505 Forest Lane P.O. Box 660199 Dallas, TX 75243 Point of contact: U.S. Highway 75 South P.O. Box 84, M/S 853 Sherman, TX 75090-9493