

REVISIONS

LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Add device type 02. Add case outlines C and 2. Update boilerplate. - rrp	97-09-03	Raymond Monnin
B	Update to current requirements. Editorial changes throughout. - drw	04-12-15	Raymond Monnin
C	Add device type 03. - drw	11-03-24	Charles Saffle
D	Update paragraphs to MIL-PRF-38535 requirements. - drw	19-07-22	Charles F. Saffle



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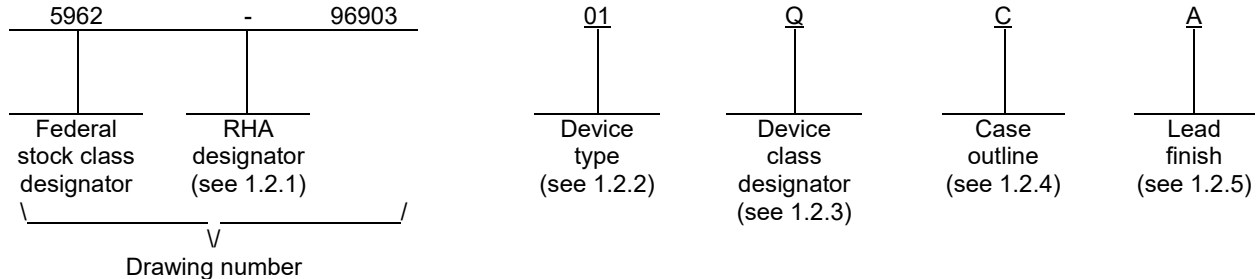
REV STATUS	REV	D	D	D	D	D	D	D	D	D	D	D	D	D						
OF SHEETS	SHEET	1	2	3	4	5	6	7	8	9	10	11								

PMIC N/A	PREPARED BY Rajesh Pithadia	<p align="center">DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990 https://www.dla.mil/LandandMaritime</p>																	
<p align="center">STANDARD MICROCIRCUIT DRAWING</p> <p>THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE</p> <p align="center">AMSC N/A</p>	CHECKED BY Rajesh Pithadia																		
	APPROVED BY Raymond Monnin	<p align="center">MICROCIRCUIT, LINEAR, DUAL LINE RECEIVER, TTL COMPATIBLE, MONOLITHIC SILICON</p>																	
	DRAWING APPROVAL DATE 96-09-17																		
	REVISION LEVEL D	SIZE A	CAGE CODE 67268	5962-96903															
		SHEET		1 OF 11															

1. SCOPE

1.1 Scope. This drawing documents two product assurance class levels consisting of high reliability (device class Q) and space application (device class V). A choice of case outlines and lead finishes are available and are reflected in the Part or Identifying Number (PIN). When available, a choice of Radiation Hardness Assurance (RHA) levels is reflected in the PIN.

1.2 PIN. The PIN is as shown in the following example:



1.2.1 RHA designator. Device classes Q and V RHA marked devices meet the MIL-PRF-38535 specified RHA levels and are marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.

1.2.2 Device types. The device types identify the circuit function as follows:

Device type	Generic number	Circuit function
01	55107A	Dual line receiver, TTL compatible
02	55108A	Dual line receiver, TTL compatible, open collector outputs
03	55107B	Dual line receiver, TTL compatible with input diode protection against power-off loading of input bus. <u>1/</u>

1.2.3 Device class designator. The device class designator is a single letter identifying the product assurance level as follows:

Device class	Device requirements documentation
Q or V	Certification and qualification to MIL-PRF-38535

1.2.4 Case outlines. The case outlines are as designated in MIL-STD-1835 as follows:

Outline letter	Descriptive designator	Terminals	Package style
C	GDIP1-T14 or CDIP2-T14	14	Dual-in-line
D	GDFP1-F14 or CDFP2-F14	14	Flat pack
2	CQCC1-N20	20	Square leadless chip carrier

1.2.5 Lead finish. The lead finish is as specified in MIL-PRF-38535 for device classes Q and V.

1/ Device type 03 has input protection diodes (see figure 5) such that the normal signal levels on the differential inputs will not be degraded if $V_{CC+} = GND$.

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1.3 Absolute maximum ratings. 1/, 2/

Supply voltage (V _{CC+}).....	+7 V dc
Supply voltage (V _{CC-}).....	-7 V dc
Differential input voltage (V _{ID}) 3/.....	±6 V dc
Common-mode input voltage (V _{IC}) 4/.....	±5 V dc
Strobe input voltage.....	+5.5 V dc
Storage temperature range (T _{STG}).....	-65°C to +150°C
Lead temperature, soldering 1.6 mm (1/16 inch) from case for 10 seconds.....	+260°C
Thermal resistance, junction-to-case (θ _{JC}).....	See MIL-STD-1835
Thermal-resistance, junction-to-ambient (θ _{JA}):	
Case C.....	120°C/W
Case D.....	180°C/W
Case 2.....	65°C/W
Continuous power dissipation (P _D) 5/:	
Case D.....	1000 mW
Cases C and 2.....	1375 mW

1.4 Recommended operating conditions. 6/

Supply voltage range (V _{CC+}).....	+4.5 V dc to +5.5 V dc
Supply voltage range (V _{CC-}).....	-4.5 V dc to -5.5 V dc
High-level input voltage between differential inputs (V _{IDH}).....	+0.025 V dc to +5 V dc
Low-level input voltage between differential inputs (V _{IDL}).....	-5 V dc to -0.025 V dc
Common-mode input voltage (V _{IC}) 7/.....	-3 V dc to +3 V dc
Input voltage, any differential input to GND 7/.....	-5 V dc to +3 V dc
High-level input voltage at strobe inputs (V _{IH(S)}).....	+2V dc to +5.5 V dc
Low-level input voltage at strobe inputs (V _{IL(S)}).....	0V dc to +0.8 V dc
Low-level output current (I _{OL}).....	-16 mA max
Operating free-air temperature (T _A).....	-55°C to +125°C

2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATION

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard Microcircuits.
MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

- 1/ Stresses above the absolute maximum rating may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.
- 2/ All voltage values, except differential voltages, are with respect to network ground terminal.
- 3/ Differential voltage values are at the non-inverting (A) terminal with respect to the inverting (B) terminal.
- 4/ Common-mode input voltage is the average of the voltages at the A and B inputs.
- 5/ Above T_A = 25°C, derate at a factor of 8.0 mW/°C for case outline D and 11.0 mW/°C for case outlines C and 2.
- 6/ When using only one channel of the line receiver, the strobe G of the unused channel should be grounded and at least one of the differential inputs of the unused receiver should be terminated at some voltage between -3 V and 3 V.
- 7/ The common-mode voltage may be as low as -4 V provided that the more positive of the two inputs is not more negative than -3 V.

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DEPARTMENT OF DEFENSE HANDBOOKS

- MIL-HDBK-103 - List of Standard Microcircuit Drawings.
- MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at <https://quicksearch.dla.mil>.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements for device classes Q and V shall be in accordance with MIL-PRF-38535 as specified herein, or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein for device classes Q and V.

3.2.1 Case outlines. The case outlines shall be in accordance with 1.2.4 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.3 Truth table. The truth table shall be as specified on figure 2.

3.2.4 Logic diagram. The logic diagram shall be as specified on figure 3.

3.3 Electrical performance characteristics and postirradiation parameter limits. Unless otherwise specified herein, the electrical performance characteristics and postirradiation parameter limits are as specified in table I and shall apply over the full ambient operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are defined in table I.

3.5 Marking. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked. For packages where marking of the entire SMD PIN number is not feasible due to space limitations, the manufacturer has the option of not marking the "5962-" on the device. For RHA product using this option, the RHA designator shall still be marked. Marking for device classes Q and V shall be in accordance with MIL-PRF-38535.

3.5.1 Certification/compliance mark. The certification mark for device classes Q and V shall be a "QML" or "Q" as required in MIL-PRF-38535.

3.6 Certificate of compliance. For device classes Q and V, a certificate of compliance shall be required from a QML-38535 listed manufacturer in order to supply to the requirements of this drawing (see 6.6.1 herein). The certificate of compliance submitted to DLA Land and Maritime-VA prior to listing as an approved source of supply for this drawing shall affirm that the manufacturer's product meets, for device classes Q and V, the requirements of MIL-PRF-38535 and herein.

3.7 Certificate of conformance. A certificate of conformance as required for device classes Q and V in MIL-PRF-38535 shall be provided with each lot of microcircuits delivered to this drawing.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C unless otherwise specified		Group A subgroups	Device type	Limits		Unit	
						Min	Max		
High-level output voltage	V _{OH}	V _{CC±} = ±4.5 V, V _{IL(S)} = 0.8 V, V _{IDH} = 25 mV, I _{OH} = -400 μA, V _{IC} = -3 V to 3 V		1, 2, 3	01, 03	2.4		V	
Low-level output voltage	V _{OL}	V _{CC±} = ±4.5 V, V _{IL(S)} = 2 V, V _{IDL} = 25 mV, I _{OL} = 16 mA, V _{IC} = -3 V to 3 V		1, 2, 3	All		0.4	V	
High-level input current	A	I _{IH1}	V _{CC±} = ±5.5 V	V _{ID} = 5 V	1, 2, 3	All		75	μA
	B						V _{ID} = -5 V		
Low-level input current	A	I _{IL1}	V _{CC±} = ±5.5 V	V _{ID} = -5 V	1, 2, 3	All		-10	μA
	B						V _{ID} = 5 V		
High-level input current into 1G or 2G		I _{IH2}	V _{CC±} = ±5.5 V, V _{IH(G)} = 2.4 V		1, 2, 3	All		40	μA
			V _{CC±} = ±5.5 V, V _{IH(G)} = 5.5 V					1	
Low-level input current into 1G or 2G		I _{IL2}	V _{CC±} = ±5.5 V, V _{IL(G)} = 0.4 V		1, 2, 3	All		-1.6	mA
High-level input current into S		I _{IH3}	V _{CC±} = ±5.5 V, V _{IH(S)} = 2.4 V		1, 2, 3	All		80	μA
			V _{CC±} = ±5.5 V, V _{IH(S)} = 5.5 V					2	
Low-level input current into S		I _{IL3}	V _{CC±} = ±5.5 V, V _{IL(S)} = 0.4 V		1, 2, 3	All		-3.2	mA
High-level output current	I _{OH}	V _{CC±} = ±5.5 V, V _{OH} = 5.5 V		1, 2, 3	02		250	μA	
Short-circuit output current 1/	I _{OS}	V _{CC±} = ±5.5 V		1, 2, 3	01, 03	-18	-70	mA	
Supply current from V _{CC+} , outputs high	I _{CCH+}	V _{CC±} = ±5.5 V, T _A = 25°C		1	All		30	mA	
Supply current from V _{CC-} , outputs high	I _{CCH-}	V _{CC±} = ±5.5 V, T _A = 25°C		1	All		-15	mA	

See footnote at end of table.

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TABLE I. Electrical performance characteristics - continued.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C unless otherwise specified		Group A subgroups	Device type	Limits		Unit
						Min	Max	
Functional tests		See 4.4.1c		7, 8	All			
Propagation delay time, low-to-high-level output, from differential inputs A and B	t _{PLH(D)}	V _{CC±} = ±5 V, R _L = 390Ω, T _A = 25°C, See figure 4	C _L = 50 pF	9	01, 03		25	ns
			C _L = 15 pF		02		25	
Propagation delay time, high-to-low-level output, from differential inputs A and B	t _{PHL(D)}	V _{CC±} = ±5 V, R _L = 390Ω, T _A = 25°C, See figure 4	C _L = 50 pF	9	01, 03		25	ns
			C _L = 15 pF		02		25	
Propagation delay time, low-to-high-level output, from strobe inputs G or S	t _{PLH(S)}	V _{CC±} = ±5 V, R _L = 390Ω, T _A = 25°C, See figure 4	C _L = 50 pF	9	01, 03		15	ns
			C _L = 15 pF		02		20	
Propagation delay time, low-to-high-level output, from strobe inputs G or S	t _{PHL(S)}	V _{CC±} = ±5 V, R _L = 390Ω, T _A = 25°C, See figure 4	C _L = 50 pF	9	01, 03		15	ns
			C _L = 15 pF		02		20	

1/ Not more than one output should be shorted at a time

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Device type	01, 02 and 03	
Case outline	C and D	2
Terminal number	Terminal symbol	
1	1A	NC
2	1B	1A
3	NC	1B
4	1Y	NC
5	1G	NC
6	S	1Y
7	GND	NC
8	2G	1G
9	2Y	S
10	NC	GND
11	2B	NC
12	2A	2G
13	V _{CC-}	2Y
14	V _{CC+}	NC
15	---	NC
16	---	2B
17	---	NC
18	---	2A
19	---	V _{CC-}
20	---	V _{CC+}

Pin description	
Terminal symbol	Description
mA (m = 1 to 2)	Non-inverting data input A
mB (m = 1 to 2)	Inverting data input B
mG (m = 1 to 2)	Strobe input G
S	Strobe input S
mY (m = 1 to 2)	Output Y

FIGURE 1. Terminal connections.

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DIFFERENTIAL INPUTS mA - mB	STROBES		OUTPUT Y
	mG	S	
$V_{ID} \geq 25 \text{ mV}$	X	X	H
$-25 \text{ mV} < V_{ID} < 25 \text{ mV}$	X	L	H
	L	X	H
	H	H	Indeterminate
$V_{ID} \leq -25 \text{ mV}$	X	L	H
	L	X	H
	H	H	L

H = High voltage level
L = Low voltage level
X = Irrelevant

FIGURE 2. Truth table.

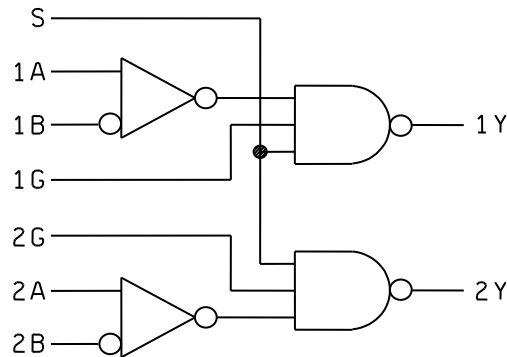


FIGURE 3. Logic diagram.

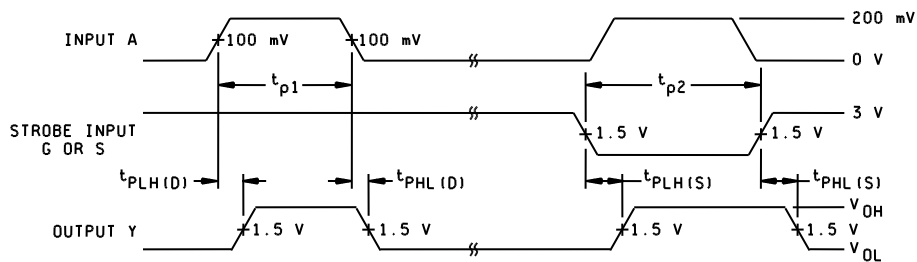
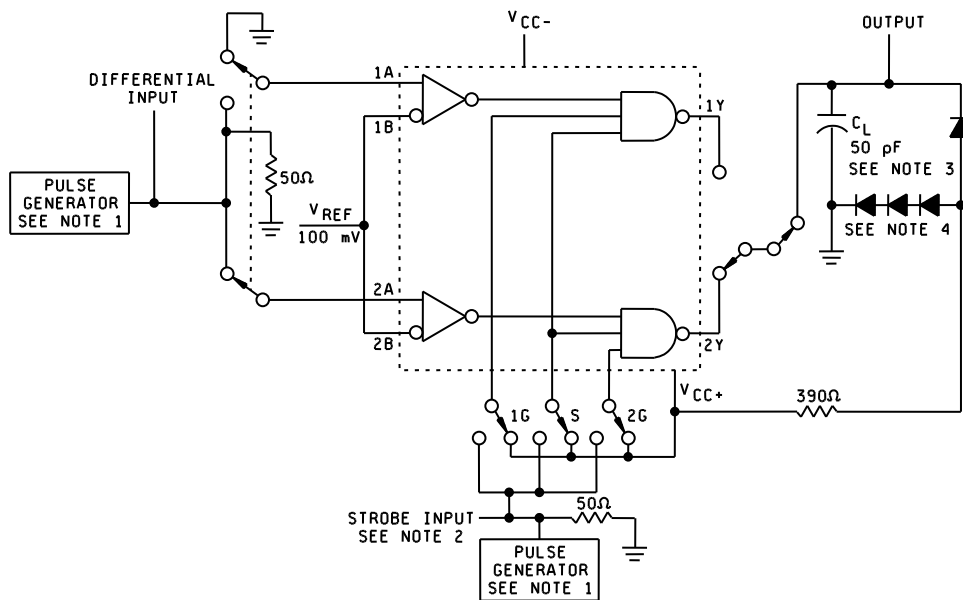
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5962-96903

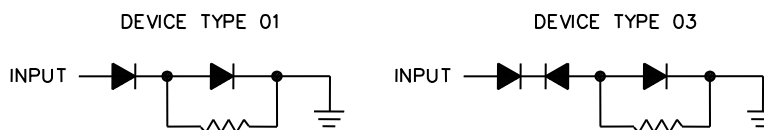
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- Notes:
1. The pulse generators have the following characteristics: $Z_O = 50\Omega$, $t_r = 10 \pm 5$ ns, $t_f = 10 \pm 5$ ns, $t_{pd1} = 500$ ns with $PRR \leq 1$ MHz, $t_{pd2} = 1$ μ s with $PRR \leq 500$ kHz.
 2. Strobe input pulse is applied to Strobe 1G when inputs 1A-1B are being tested, to Strobe S when inputs 1A-1B or 2A-2B are being tested, and to Strobe 2G when inputs 2A-2B are being tested.
 3. C_L = includes probe and jig capacitance.
 4. All diodes are 1N916.

FIGURE 4. Test circuit and timing waveforms.



- Note:
1. For device type 03, $V_{CC+} = GND$ providing bus protection against power off condition.

FIGURE 5. Equivalent input circuit.

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4. VERIFICATION

4.1 Sampling and inspection. For device classes Q and V, sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

4.2 Screening. For device classes Q and V, screening shall be in accordance with MIL-PRF-38535, and shall be conducted on all devices prior to qualification and technology conformance inspection.

4.2.1 Additional criteria for device classes Q and V.

- a. The burn-in test duration, test condition and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document revision level control of the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.
- b. Interim and final electrical test parameters shall be as specified in table II herein.
- c. Additional screening for device class V beyond the requirements of device class Q shall be as specified in MIL-PRF-38535, appendix B.

4.3 Qualification inspection for device classes Q and V. Qualification inspection for device classes Q and V shall be in accordance with MIL-PRF-38535. Inspections to be performed shall be those specified in MIL-PRF-38535 and herein for groups A, B, C, D, and E inspections (see 4.4.1 through 4.4.4).

4.4 Conformance inspection. Technology conformance inspection for classes Q and V shall be in accordance with MIL-PRF-38535 including groups A, B, C, D, and E inspections, and as specified herein.

4.4.1 Group A inspection.

- a. Tests shall be as specified in table II herein.
- b. Subgroups 4, 5, 6, 10, and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.
- c. For device classes Q and V, subgroups 7 and 8 shall include verifying the functionality of the device.

4.4.2 Group C inspection. The group C inspection end-point electrical parameters shall be as specified in table II herein.

4.4.2.1 Additional criteria for device classes Q and V. The steady-state life test duration, test condition and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The test circuit shall be maintained under document revision level control by the device manufacturer's TRB in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.

4.4.3 Group D inspection. The group D inspection end-point electrical parameters shall be as specified in table II herein.

4.4.4 Group E inspection. Group E inspection is required only for parts intended to be marked as radiation hardness assured (see 3.5 herein).

- a. End-point electrical parameters shall be as specified in table II herein.
- b. For device classes Q and V, the devices or test vehicle shall be subjected to radiation hardness assured tests as specified in MIL-PRF-38535 for the RHA level being tested. All device classes must meet the postirradiation end-point electrical parameter limits as defined in table I at $T_A = +25^\circ\text{C} \pm 5^\circ\text{C}$, after exposure, to the subgroups specified in table II herein.

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TABLE II. Electrical test requirements.

Test requirements	Subgroups (in accordance with MIL-PRF-38535, table III)	
	Device class Q	Device class V
Interim electrical parameters (see 4.2)	---	---
Final electrical parameters (see 4.2)	1, 2, 3, 7, 8, 9 <u>1/</u>	1, 2, 3, 7, 8, 9 <u>2/</u>
Group A test requirements (see 4.4)	1, 2, 3, 7, 8, 9	1, 2, 3, 7, 8, 9
Group C end-point electrical parameters (see 4.4)	1, 2, 3	1, 2, 3
Group D end-point electrical parameters (see 4.4)	1, 2, 3	1, 2, 3
Group E end-point electrical parameters (see 4.4)	---	---

1/ PDA applies to subgroup 1.

2/ PDA applies to subgroups 1 and 7.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38535 for device classes Q and V.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.1.1 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor prepared specification or drawing.

6.2 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished using DD Form 1692, Engineering Change Proposal.

6.3 Record of users. Military and industrial users should inform DLA Land and Maritime when a system application requires configuration control and which SMD's are applicable to that system. DLA Land and Maritime will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DLA Land and Maritime-VA, telephone (614) 692-8108.

6.4 Comments. Comments on this drawing should be directed to DLA Land and Maritime-VA, Columbus, Ohio 43218-3990, or telephone (614) 692-0540.

6.5 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535 and MIL-HDBK-1331.

6.6 Sources of supply.

6.6.1 Sources of supply for device classes Q and V. Sources of supply for device classes Q and V are listed in MIL-HDBK-103 and QML-38535. The vendors listed in MIL-HDBK-103 and QML-38535 have submitted a certificate of compliance (see 3.6 herein) to DLA Land and Maritime-VA and have agreed to this drawing.

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STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 19-07-22

Approved sources of supply for SMD 5962-96903 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38535 during the next revision. MIL-HDBK-103 and QML-38535 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DLA Land and Maritime-VA. This information bulletin is superseded by the next dated revision of MIL-HDBK-103 and QML-38535. DLA Land and Maritime maintains an online database of all current sources of supply at <https://landandmaritimeapps.dla.mil/programs/smcr/>.

Standard microcircuit drawing PIN <u>1/</u>	Vendor CAGE number	Vendor similar PIN <u>2/</u>
5962-9690301QCA	01295	SNJ55107AJ
5962-9690301QDA	01295	SNJ55107AW
5962-9690301Q2A	01295	SNJ55107AFK
5962-9690302QCA	<u>3/</u>	SNJ55108AJ
5962-9690302QDA	<u>3/</u>	SNJ55108AW
5962-9690302Q2A	<u>3/</u>	SNJ55108AFK
5962-9690303QCA	0DKS7	GEM42503QCA
5962-9690303QCC	0DKS7	GEM42503QCC
5962-9690303Q2A	0DKS7	GEM42503Q2A
5962-9690303Q2C	0DKS7	GEM42503Q2C

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.
- 2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.
- 3/ Not available from an approved source of supply.

Vendor CAGE
number

Vendor name
and address

01295

Texas Instruments, Inc.
Semiconductor Group
8505 Forest Ln.
PO Box 660199
Dallas, TX 75243

0DKS7

SRI International
201 Washington Road
Princeton, NJ 08540-6449

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