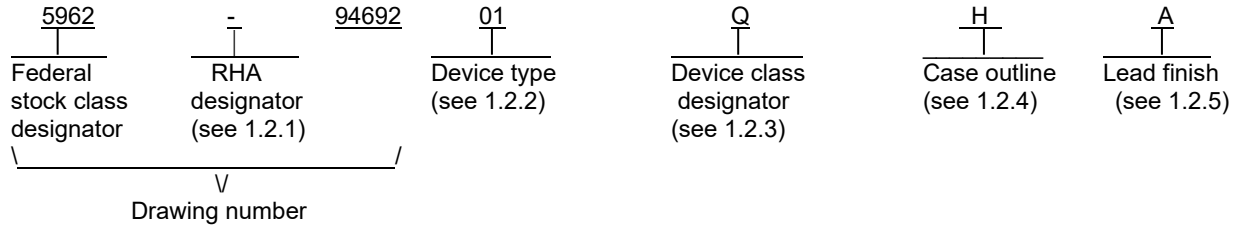


1. SCOPE

1.1 Scope. This drawing documents two product assurance class levels consisting of high reliability (device class Q) and space application (device class V). A choice of case outlines and lead finishes are available and are reflected in the Part or Identifying Number (PIN). When available, a choice of Radiation Hardness Assurance (RHA) levels is reflected in the PIN.

1.2 PIN. The PIN is as shown in the following example:



1.2.1 RHA designator. Device classes Q and V RHA marked devices meet the MIL-PRF-38535 specified RHA levels and are marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.

1.2.2 Device type(s). The device type(s) identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>
01	TLC2262	Rail-to-rail, dual, operational amplifier
02	TLC2264	Rail-to-rail, quad, operational amplifier
03	TLC2262A	Rail-to-rail, dual, operational amplifier with enhanced VIO
04	TLC2264A	Rail-to-rail, quad, operational amplifier with enhanced VIO

1.2.3 Device class designator. The device class designator is a single letter identifying the product assurance level as follows:

<u>Device class</u>	<u>Device requirements documentation</u>
Q or V	Certification and qualification to MIL-PRF-38535

1.2.4 Case outline(s). The case outline(s) are as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
C	GDIP1-T14 or CDIP2-T14	14	Dual-in-line
D	GDFP1-F14 or CDFP2-F14	14	Flat pack
H	GDFP1-F10 or CDFP2-F10	10	Flat pack
P	GDIP1-T8 or CDIP2-T8	8	Dual-in-line
2	CQCC1-N20	20	Square leadless chip carrier

1.2.5 Lead finish. The lead finish is as specified in MIL-PRF-38535 for device classes Q and V.

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1.3 Absolute maximum ratings. 1/

Supply voltage range (VDD)	-0.5 V dc to +16.0 V dc <u>2/</u>
Differential input voltage (VID)	-16.0 V to +16.0 V <u>3/</u>
Input voltage range (VIN)	-VDD - 0.3 V to +VDD <u>2/</u>
Input current, each input (IIN)	+5.0 mA to -5.0 mA
Output current (IOUT)	+50.0 mA to -50.0 mA
Total current into +VDD	+50.0 mA to -50.0 mA
Total current out of -VDD	+50.0 mA to -50.0 mA
Duration of short-circuit current at or below +25°C	Unlimited <u>4/</u>
Operating free-air temperature range (TA)	-55°C to +125°C
Storage temperature range (TSTG)	-65°C to +150°C
Lead temperature (soldering 10 seconds)	+260°C
Thermal resistance, junction-to-case (θJC)	See MIL-STD-1835
Maximum junction temperature (TJ)	+150°C
Maximum power dissipation (PD): <u>5/</u>	
Cases C and 2	1375 mW
Cases D and H	700 mW
Case P	1050 mW

1.4 Recommended operating conditions.

Supply voltage (±VDD)	±2.2 V dc to ±8.0 V dc
Input voltage range (VIN)	-VDD to +VDD - 1.5 V
Common-mode input voltage (VIC)	-VDD to +VDD - 1.5 V
Ambient operating temperature range (TA)	-55°C to +125°C

- 1/ Stresses above the absolute maximum rating may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.
- 2/ All voltage values, except differential voltages, are with respect to the midpoint between +VDD and -VDD.
- 3/ Differential voltages are at the noninverting input with respect to the inverting input. Excessive current flows if the input is brought below -VDD - 0.3 V.
- 4/ The output may be shorted to either supply. Temperature and/or supply voltages must be limited to ensure that the maximum dissipation rating is not exceeded.
- 5/ Above TA = +25°C, derate by the following factors; cases C and 2 at 11.0 mW/°C, cases D and H at 5.5 mW/°C, and case P at 8.4 mW/°C.

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2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATION

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard Microcircuits.
MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

DEPARTMENT OF DEFENSE HANDBOOKS

MIL-HDBK-103 - List of Standard Microcircuit Drawings.
MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at <https://quicksearch.dla.mil>.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements for device classes Q and V shall be in accordance with MIL-PRF-38535 as specified herein, or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein for device classes Q and V.

3.2.1 Case outlines. The case outlines shall be in accordance with 1.2.4 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.3 Electrical performance characteristics and postirradiation parameter limits. Unless otherwise specified herein, the electrical performance characteristics and postirradiation parameter limits are as specified in table I and shall apply over the full ambient operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are defined in table I.

3.5 Marking. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked. For packages where marking of the entire SMD PIN number is not feasible due to space limitations, the manufacturer has the option of not marking the "5962-" on the device. For RHA product using this option, the RHA designator shall still be marked. Marking for device classes Q and V shall be in accordance with MIL-PRF-38535.

3.5.1 Certification/compliance mark. The certification mark for device classes Q and V shall be a "QML" or "Q" as required in MIL-PRF-38535.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ TA ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits ^{1/}		Unit
					Min	Max	
Input offset voltage	V _{IO}	V _{DD} = ±2.5 V, V _{IC} = 0 V, R _S = 50 Ω, V _{OUT} = 0 V	1	01, 02		2500	μV
				03, 04		950	
			2, 3	01, 02		3000	
				03, 04		1500	
		V _{DD} = ±5 V, V _{IC} = 0 V, R _S = 50 Ω, V _{OUT} = 0 V	1	01, 02		2500	
				03, 04		950	
			2, 3	01, 02		3000	
				03, 04		1500	
Input offset current	I _{IO}	V _{DD} = ±2.5 V, V _{IC} = 0 V, R _S = 50 Ω, V _{OUT} = 0 V, T _A = +125°C	2	All		800	pA
		V _{DD} = ±5 V, V _{IC} = 0 V, R _S = 50 Ω, V _{OUT} = 0 V, T _A = +125°C				800	
Input bias current	I _{IB}	V _{DD} = ±2.5 V, V _{IC} = 0 V, R _S = 50 Ω, V _{OUT} = 0 V, T _A = +125°C	2	All		800	pA
		V _{DD} = ±5 V, V _{IC} = 0 V, R _S = 50 Ω, V _{OUT} = 0 V, T _A = +125°C				800	
Common-mode input voltage range	V _{ICR}	V _{DD} = 5 V, V _{IO} ≤ 5 mV, R _S = 50 Ω	1	All	0 to 4		V
			2, 3		0 to 3.5		
		V _{DD} = ±5 V, V _{IO} ≤ 5 mV, R _S = 50 Ω	1		-5 to 4		
			2, 3		-5 to 3.5		
High-level output voltage	V _{OH}	V _{DD} = 5 V, V _{IC} = 2.5 V, I _{OH} = -100 μA	1	All	4.85		V
			2, 3		4.82		
		V _{DD} = 5 V, V _{IC} = 2.5 V, I _{OH} = -200 μA	1		4.7		
			2, 3		4.5		

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits <u>1/</u>		Unit
					Min	Max	
Low-level output voltage	VOL	VDD = 5 V, VIC = 2.5 V, IOL = 500 μA	1, 2, 3	All		0.15	V
		VDD = 5 V, VIC = 2.5 V, IOL = 4 mA	1			1.0	
			2, 3			1.2	
Maximum positive peak output voltage swing	+VOM	VDD = ±5 V, VIC = 0 V, IOUT = -100 μA	1	All	4.85		V
			2, 3		4.82		
		VDD = ±5 V, VIC = 0 V, IOUT = -200 μA	1		4.7		
			2, 3		4.5		
Maximum negative peak output voltage swing	-VOM	VDD = ±5 V, VIC = 0 V, IOUT = 500 μA	1, 2, 3	All	-4.85		V
		VDD = ±5 V, VIC = 0 V, IOUT = 4 mA	1		-4.0		
			2, 3		-3.8		
Large-signal differential voltage amplification	AVD	VDD = 5 V, VIC = 2.5 V, <u>2/</u> VOUT = 1 V to 4 V, RL = 100 kΩ	1	All	80		V/mV
			2, 3		50		
		VDD = ±5 V, VIC = 0 V, <u>2/</u> VOUT = ±4 V, RL = 100 kΩ	1		80		
			2, 3		50		
Common-mode rejection ratio	CMRR	VDD = 5 V, VOUT = 2.5 V, VIC = 0 V to 2.7 V, RS = 50 Ω	1, 2, 3	All	70		dB
		VDD = ±5 V, VOUT = 0 V, VIC = -5 V to 2.7 V, RS = 50 Ω			75		
Supply voltage rejection ratio (ΔVDD/ΔVIO)	kSVR	±VDD = ±2.2 V to ±8 V, VIC = VDD/2, no load	1, 2, 3	All	80		dB

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions -55°C ≤ TA ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits ^{1/}		Unit
					Min	Max	
Supply current (both channels)	IDD	VDD = 5 V, VOUT = 2.5 V, no load	1, 2, 3	01, 03		500	μA
				02, 04		1000	
		VDD = ±5 V, VOUT = 0 V, no load		01, 03		500	
				02, 04		1000	
Slew rate at unity gain	SR	VDD = 5 V, RL = 50 kΩ, ^{2/} VOUT = 1 V to 3 V, CL = 100 pF	4	All	0.35		V/μs
			5, 6		0.25		
		VDD = ±5 V, ^{2/} RL = 50 kΩ, VOUT = ±1.9 V, CL = 100 pF	4		0.35		
			5, 6		0.25		

^{1/} The algebraic convention, whereby the most negative value is a minimum and the most positive is a maximum, is used in this table. Negative current shall be defined as conventional current flow out of a device terminal.

^{2/} Referenced to 2.5 V.

3.6 Certificate of compliance. For device classes Q and V, a certificate of compliance shall be required from a QML-38535 listed manufacturer in order to supply to the requirements of this drawing (see 6.6.1 herein). The certificate of compliance submitted to DLA Land and Maritime-VA prior to listing as an approved source of supply for this drawing shall affirm that the manufacturer's product meets, for device classes Q and V, the requirements of MIL-PRF-38535 and herein.

3.7 Certificate of conformance. A certificate of conformance as required for device classes Q and V in MIL-PRF-38535 shall be provided with each lot of microcircuits delivered to this drawing.

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Device types	01, 03			02, 04	
Case outlines	H	P	2	C and D	2
Terminal number	Terminal symbol				
1	NC	OUTPUT 1	NC	OUTPUT 1	NC
2	OUTPUT 1	-INPUT 1	OUTPUT 1	-INPUT 1	OUTPUT 1
3	-INPUT 1	+INPUT 1	NC	+INPUT 1	-INPUT 1
4	+INPUT 1	-VDD	NC	+VDD	+INPUT 1
5	-VDD	+INPUT 2	-INPUT 1	+INPUT 2	NC
6	+INPUT 2	-INPUT 2	NC	-INPUT 2	+VDD
7	-INPUT 2	OUTPUT 2	+INPUT 1	OUTPUT 2	NC
8	OUTPUT 2	+VDD	NC	OUTPUT 3	+INPUT 2
9	+VDD	---	NC	-INPUT 3	-INPUT 2
10	NC	---	-VDD	+INPUT 3	OUTPUT 2
11	---	---	NC	-VDD	NC
12	---	---	+INPUT 2	+INPUT 4	OUTPUT 3
13	---	---	NC	-INPUT 4	-INPUT 3
14	---	---	NC	OUTPUT 4	+INPUT 3
15	---	---	-INPUT 2	---	NC
16	---	---	NC	---	-VDD
17	---	---	OUTPUT 2	---	NC
18	---	---	NC	---	+INPUT 4
19	---	---	NC	---	-INPUT 4
20	---	---	+VDD	---	OUTPUT 4

NC = No connection

FIGURE 1. Terminal connections.

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4. VERIFICATION

4.1 Sampling and inspection. For device classes Q and V, sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

4.2 Screening. For device classes Q and V, screening shall be in accordance with MIL-PRF-38535, and shall be conducted on all devices prior to qualification and technology conformance inspection.

4.2.1 Additional criteria for device classes Q and V.

- a. The burn-in test duration, test condition and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document revision level control of the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.
- b. Interim and final electrical test parameters shall be as specified in table II herein.
- c. Additional screening for device class V beyond the requirements of device class Q shall be as specified in MIL-PRF-38535, appendix B.

4.3 Qualification inspection for device classes Q and V. Qualification inspection for device classes Q and V shall be in accordance with MIL-PRF-38535. Inspections to be performed shall be those specified in MIL-PRF-38535 and herein for groups A, B, C, D, and E inspections (see 4.4.1 through 4.4.4).

4.4 Conformance inspection. Technology conformance inspection for classes Q and V shall be in accordance with MIL-PRF-38535 including groups A, B, C, D, and E inspections, and as specified herein.

4.4.1 Group A inspection.

- a. Tests shall be as specified in table II herein.
- b. Subgroups 7, 8, 9, 10, and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.

4.4.2 Group C inspection. The group C inspection end-point electrical parameters shall be as specified in table II herein.

4.4.2.1 Additional criteria for device classes Q and V. The steady-state life test duration, test condition and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The test circuit shall be maintained under document revision level control by the device manufacturer's TRB in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.

4.4.3 Group D inspection. The group D inspection end-point electrical parameters shall be as specified in table II herein.

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TABLE II. Electrical test requirements.

Test requirements	Subgroups (in accordance with MIL-PRF-38535, table III)	
	Device class Q	Device class V
Interim electrical parameters (see 4.2)	---	---
Final electrical parameters (see 4.2)	1,2,3,4,5,6 <u>1/</u>	1,2,3,4, <u>1/</u> 5,6
Group A test requirements (see 4.4)	1,2,3,4,5,6	1,2,3,4,5,6
Group C end-point electrical parameters (see 4.4)	1	1
Group D end-point electrical parameters (see 4.4)	1	1
Group E end-point electrical parameters (see 4.4)	---	---

1/ PDA applies to subgroup 1 with the exception of VIO.

4.4.4 Group E inspection. Group E inspection is required only for parts intended to be marked as radiation hardness assured (see 3.5 herein).

- a. End-point electrical parameters shall be as specified in table II herein.
- b. For device classes Q and V, the devices or test vehicle shall be subjected to radiation hardness assured tests as specified in MIL-PRF-38535 for the RHA level being tested. All device classes must meet the postirradiation end-point electrical parameter limits as defined in table I at TA = +25°C ±5°C, after exposure, to the subgroups specified in table II herein.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38535 for device classes Q and V.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.1.1 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor prepared specification or drawing.

6.2 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished using DD Form 1692, Engineering Change Proposal or e-mail communication.

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6.3 Record of users. Military and industrial users should inform DLA Land and Maritime when a system application requires configuration control and which SMD's are applicable to that system. DLA Land and Maritime will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DLA Land and Maritime-VA, telephone (614) 692-8108.

6.4 Comments. Comments on this drawing should be directed to DLA Land and Maritime-VA, Columbus, Ohio 43218-3990, or telephone (614) 692-0540.

6.5 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535 and MIL-HDBK-1331.

6.6 Sources of supply.

6.6.1 Sources of supply for device classes Q and V. Sources of supply for device classes Q and V are listed in MIL-HDBK-103 and QML-38535. The vendors listed in MIL-HDBK-103 and QML-38535 have submitted a certificate of compliance (see 3.6 herein) to DLA Land and Maritime-VA and have agreed to this drawing.

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STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 22-03-09

Approved sources of supply for SMD 5962-94692 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38535 during the next revision. MIL-HDBK-103 and QML-38535 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DLA Land and Maritime-VA. This information bulletin is superseded by the next dated revision of MIL-HDBK-103 and QML-38535. DLA Land and Maritime maintains an online database of all current sources of supply at <https://landandmaritimeapps.dla.mil/programs/smcr/>.

Standard microcircuit drawing PIN <u>1/</u>	Vendor CAGE number	Vendor similar PIN <u>2/</u>
5962-9469201QHA	01295	TLC2262MUB
5962-9469201QPA	<u>3/</u>	TLC2262MJGB
5962-9469201Q2A	<u>3/</u>	TLC2262MFKB
5962-9469202QCA	<u>3/</u>	TLC2264MJB
5962-9469202QDA	<u>3/</u>	TLC2264MWB
5962-9469202Q2A	<u>3/</u>	TLC2264MFKB
5962-9469203QHA	<u>3/</u>	TLC2262AMUB
5962-9469203QPA	01295	TLC2262AMJGB
5962-9469203Q2A	<u>3/</u>	TLC2262AMFKB
5962-9469204QCA	01295	TLC2264AMJB
5962-9469204QDA	<u>3/</u>	TLC2264AMWB
5962-9469204Q2A	01295	TLC2264AMFKB

1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.

2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

3/ Not available from an approved source of supply.

Vendor CAGE
number

01295

Vendor name
and address

Texas Instruments, Incorporated
Semiconductor Group
8505 Forest Lane
P.O. Box 660199
Dallas, TX 75243

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in the information bulletin.