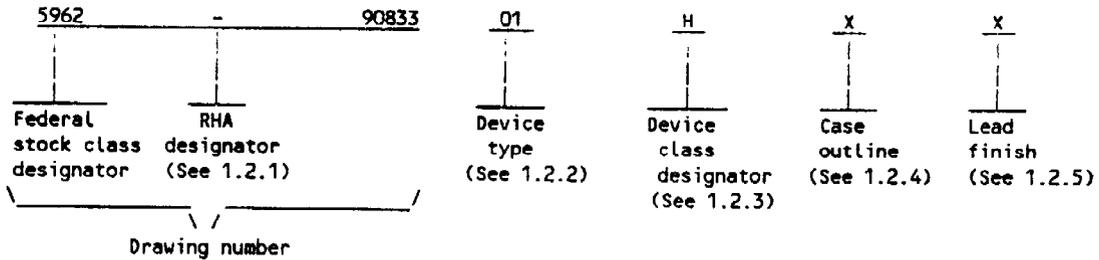


1. SCOPE

1.1 Scope. This drawing forms a part of a one part - one part number documentation system (see 6.6 herein). This drawing describes device requirements for hybrid microcircuits to be processed in accordance with MIL-H-38534. Two product assurance classes, military high reliability (device class H) and space application (device class K) and a choice of case outlines and lead finishes are available and are reflected in the complete Part or Identifying Number (PIN). When available, a choice of radiation hardness assurance levels are reflected in the complete PIN.

1.2 PIN. The PIN shall be as shown in the following example:



1.2.1 Radiation hardness assurance (RHA) designator. Device classes H and K RHA marked devices shall meet the MIL-H-38534 specified RHA levels and shall be marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.

1.2.2 Device type(s). The device type(s) shall identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>
01	CLC201A	High speed, wideband, operational amplifier

1.2.3 Device class designator. This device class designator shall be a single letter identifying the product assurance level as follows:

<u>Device class</u>	<u>Device requirements documentation</u>
H or K	Certification and qualification to MIL-H-38534

1.2.4 Case outline(s). The case outline(s) shall be as designated in appendix C of MIL-H-38510 and as follows:

<u>Outline letter</u>	<u>Case outline</u>
X	See figure 1, (12-lead, .605" x .181"), can package

1.2.5 Lead finish. The lead finish shall be as specified in MIL-H-38534 for classes H and K. Finish letter "X" shall not be marked on the microcircuit or its packaging. The "X" designation is for use in specifications when lead finishes A, B, and C are considered acceptable and interchangeable without preference.

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1.3 Absolute maximum ratings.

Supply voltage ($\pm V_{CC}$)	-----	± 20 V dc
Output current	-----	± 100 mA
Storage temperature range	-----	-65°C to $+150^{\circ}\text{C}$
Lead temperature (soldering, 10 seconds)	-----	$+300^{\circ}\text{C}$
Junction temperature (T_J)	-----	$+175^{\circ}\text{C}$
Power dissipation (P_D)	-----	1/
Thermal resistance, case-to-ambient (Θ_{CA})	-----	65°C/W 2/
Thermal resistance, junction-to-case:		
Θ_{JC} (circuit)	-----	32°C/W 3/
Θ_{JC} (output)	-----	100°C/W 4/

1.4 Recommended operating conditions.

Supply voltage range	-----	± 5 V dc to ± 15 V dc
Gain range	-----	± 1 to ± 50
Ambient operating temperature range (T_A)	-----	-55°C to $+125^{\circ}\text{C}$

2. APPLICABLE DOCUMENTS

2.1 Government specifications, standards, bulletin, and handbook. Unless otherwise specified, the following specifications, standards, bulletin, and handbook of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATIONS

MILITARY

- MIL-M-38510 - Microcircuits, General Specification for.
- MIL-H-38534 - Hybrid Microcircuits, General Specification for.

STANDARDS

MILITARY

- MIL-STD-480 - Configuration Control-Engineering Changes, Deviations and Waivers.
- MIL-STD-883 - Test Methods and Procedures for Microelectronics.

HANDBOOK

MILITARY

- MIL-HDBK-780 - Standardized Military Drawings.

(Copies of the specifications, standards, and handbook required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

- 1/ The power dissipation can be determined based on the application and the junction temperature verified to be ($T_J \leq +175^{\circ}\text{C}$) and using the thermal resistance values given.
- 2/ Still air, no heatsink.
- 3/ Thermal resistance of circuit; $P_{\text{circuit}} = 2(V_{CC}) I_{CC}$.
- 4/ Thermal resistance of output transistors; $P_{\text{output}} = V_{CE}(I_E) \times (\% \text{ duty cycle})$.

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2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with MIL-H-38534 and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-H-38534 and herein.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.4 herein and figure 1.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 2.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full specified operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 Marking. Marking shall be in accordance with MIL-H-38534. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in QML-38534.

3.6 Manufacturer eligibility. In addition to the general requirements of MIL-H-38534, the manufacturer of the part described herein shall submit for DESC-ECC review and approval electrical test data (variables format) on 22 devices from the initial quality conformance inspection group A lot sample, produced on the certified line, for each device type listed herein. The data should also include a summary of all parameters manually tested, and for those which, if any, are guaranteed.

3.7 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to supply to this drawing. The certificate of compliance submitted to DESC-ECC shall affirm that the manufacturer's product meets the requirements of MIL-H-38534 and the requirements herein.

3.8 Certificate of conformance. A certificate of conformance as required in MIL-H-38534 shall be provided with each lot of microcircuits delivered to this drawing.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions ^{1/} -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Limits		Unit
				Min	Max	
Small signal bandwidth -3 dB bandwidth	SSBW	V _{OUT} < 2 V _{p-p}	4, 6	85		MHz
			5	75		
Gain flatness peaking low	GFPL	f = 100 kHz to 25 MHz,	4		0.3	dB
			5		0.5	
			6		0.4	
Gain flatness peaking high	GFPH	f > 25 MHz,	4		0.6	dB
			5, 6		1.0	
Gain flatness rolloff	GFR	f = 50 MHz,	4		0.4	dB
			5, 6		0.6	
Linear phase deviation ^{2/}	LPD	f < 50 MHz	4		2	degree
			5		3	
			6		2.5	
Overshoot ^{2/}	OS	2 volt step	4		12	%
			5, 6		15	
Slew rate ^{2/} (overdriven input)	SR	V _{OUT} = 20 V _{p-p} f = 10 MHz	4, 5, 6	3		V/ns
Noninverting input ^{2/} capacitance	C _{IN}		4, 5, 6		3	pF
Noninverting input ^{2/} resistance	R _{IN}		4, 5, 6	100		kΩ
Output impedance ^{2/}	R _O		4, 5, 6		0.1	Ω
2nd harmonic distortion	HD2	f = 20 MHz, 2 V _{p-p}	7, 8A, 8B		-45	dBc ^{3/}
3rd harmonic distortion	HD3	f = 20 MHz, 2 V _{p-p}	7, 8A, 8B		-50	
Noise floor ^{2/}	SNF	f > 5.0 MHz	7, 8A, 8B		-150	dBm (1Hz)
Integrated noise ^{2/}	INV	f = 5.0 MHz to 100 MHz	7, 8A, 8B		70	μV

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions ^{1/} -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Limits		Unit
				Min	Max	
Input offset voltage	V _{IO}		1		1.0	mV
			2		2.5	
			3		2.2	
Input offset voltage temperature coefficient ^{2/}	ΔV _{IO}		2, 3		15	μV/°C
	ΔT					
+ Input bias current	+I _{IB}	Input resistance is 100 kΩ.	1, 2		20	μA
			3		36	
+ Input bias current temperature coefficient ^{2/}	Δ+I _{IB}		2, 3		125	nA/°C
	ΔT					
- Input bias current	-I _{IB}	Input resistance is 100 kΩ.	1	-10		μA
			2	-30		
			3	-26		
- Input bias current temperature coefficient ^{2/}	Δ-I _{IB}		2, 3		200	nA/°C
	ΔT					
Power supply rejection ratio	PSRR	ΔV _{CC} = ± 0.5 V	1, 2, 3		45	dB
Common mode rejection ^{2/} ratio	CMRR	ΔV _{CM} = 1 V	4, 5, 6		40	dB
Supply current	I _{CC}	No load	1		34	mA
			2, 3		36	

^{1/} Unless otherwise specified, R_L = 200.0Ω, R_F = 2000Ω, ±V_{CC} = ±15 V, A_V = +20.

^{2/} Parameter shall be tested as part of device initial characterization and after design and process changes which will affect this parameter. Parameter shall be guaranteed to the limits specified in table I for all lots not specifically tested.

^{3/} Symbol dBc is the standard reference for a signal referenced to a signal level.

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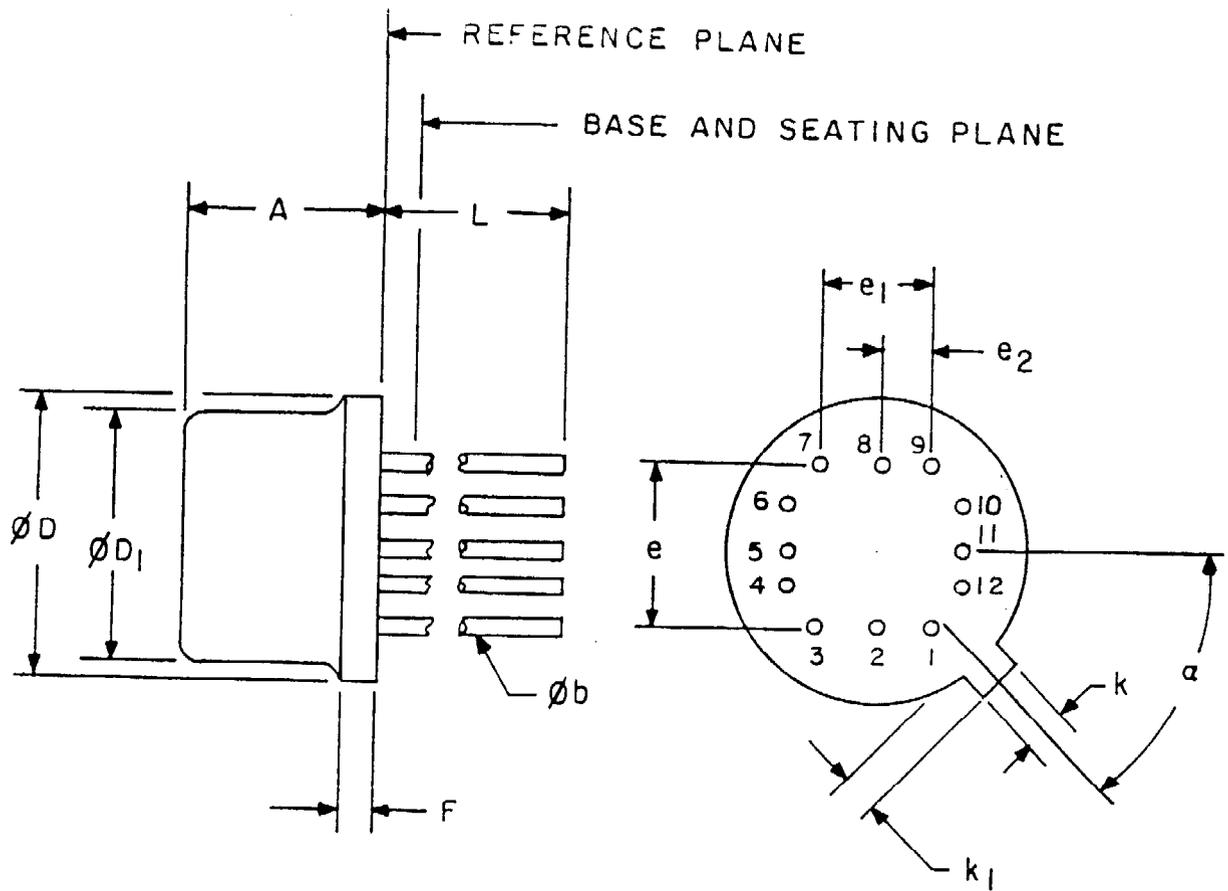


FIGURE 1. Case outline X.

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Symbol	Inches		Millimeters		Notes
	Min	Max	Min	Max	
A	.142	.181	3.61	4.60	
ϕb	.016	.019	0.41	0.48	
ϕD	.595	.605	15.11	15.37	
ϕD_1	.543	.555	13.79	14.10	
e	.400 BSC		10.16 BSC		3
e ₁	.200 BSC		5.08 BSC		3
e ₂	.100 BSC		2.54 BSC		3
F	.016	.030	0.41	0.76	
k	.026	.036	0.66	0.91	
k ₁	.026	.036	0.66	0.91	2
L	.310	.340	7.87	8.64	
a	45° BSC		45° BSC		3

NOTES:

1. The product may be measured by direct methods or by gauge.
2. Measured from the maximum diameter of the product.
3. Leads having a maximum diameter .019 inch (0.48 mm) measured in gauging the plane .054 inch (1.37 mm) +.001 inch (0.03 mm), -.000 (0.00 mm) below the base plane of the product shall be within .007 inch (.18mm) of their true position relative to a maximum width tab.

FIGURE 1. Case outline X - Continued.

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Device type	01
Case outline	X
Terminal number	Terminal symbol
1	+V _{CC} (supply voltage)
2	No connection
3	GND (case)
4	No connection
5	-V _{IN}
6	+V _{IN}
7	GND (case)
8	R _f (internal feedback)
9	-V _{CC} (supply voltage)
10	-V _{CC} (collector supply)
11	V _{OUT}
12	+V _{CC} (collector supply)

NOTE: Pin 8 provides access to a 2 k Ω feedback resistor which can be connected to the output or left open if an external feedback resistor is desired.

FIGURE 2. Terminal connections.

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4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-H-38534.

4.2 Screening. Screening shall be in accordance with MIL-H-38534. The following additional criteria shall apply:

- a. Burn-in test, method 1015 of MIL-STD-883.
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.7 herein).
 - (2) T_A as specified in accordance with table I of method 1015 of MIL-STD-883.
- b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5008, group A test table)
Interim electrical parameters	1
Final electrical test parameters	1*,2,3,4,7
Group A test requirements	1,2,3,4,5, 6,7,8A,8B
Group C end-point electrical parameters	1
Group E end-point electrical parameters for RHA devices	Subgroups** (per method 5005, group A test table)

*PDA applies to subgroup 1.

**When applicable to this standardized military drawing, the subgroup shall be defined.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with MIL-H-38534 and as specified herein.

4.3.1 Group A inspection. Group A inspection shall be in accordance with MIL-H-38534 and as follows:

- a. Tests shall be as specified in table II herein.
- b. Subgroups 9, 10, and 11 in table X, method 5008 of MIL-STD-883 shall be omitted.

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4.3.2 Group B inspection. Group B inspection shall be in accordance with MIL-H-38534.

4.3.3 Group C inspection. Group C inspection shall be in accordance with MIL-H-38534 and as follows:

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test conditions, method 1005 of MIL-STD-883.
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.7 herein).
 - (2) T_A as specified in accordance with table I of method 1005 of MIL-STD-883.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

4.3.4 Group D inspection. Group D inspection shall be in accordance with MIL-H-38534.

4.3.5 Group E inspection. Group E inspection is required only for parts intended to be marked as radiation hardness assured (see 3.5 herein). RHA levels for device classes H and K shall be M, D, R, and H. RHA quality conformance inspection sample tests shall be performed at the RHA level specified in the acquisition document.

- a. RHA tests for device classes H and K for levels M, D, R, and H shall be performed through each level to determine at what levels the devices meet the RHA requirements. These RHA tests shall be performed for initial qualification and after design or process changes which may affect the RHA performance of the device.
- b. End-point electrical parameters shall be as specified in table II herein.
- c. Prior to total dose irradiation, each selected sample shall be assembled in its qualified package. It shall pass the specified group A electrical parameters in table I for subgroups specified in table II herein.
- d. For device classes H and K, the devices shall be subjected to radiation hardness assured tests as specified in MIL-H-38534 for RHA level being tested, and meet the postirradiation end-point electrical parameter limits as defined in table I at $T_A = +25^\circ\text{C} \pm 5$ percent, after exposure.
- e. Prior to and during total dose irradiation testing, the devices shall be biased to establish a worst case condition as specified in the radiation exposure circuit.
- f. For device classes H and K, subgroups 1 and 2 in table V, method 5005 of MIL-STD-883 shall be tested as appropriate for device construction.
- g. When specified in the purchase order or contract, a copy of the RHA delta limits shall be supplied.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-H-38534.

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6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-481 using DD Form 1693, Engineering Change Proposal (Short Form).

6.4 Record of users. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DESC-ECC, telephone (513) 296-8527.

6.5 Comments. Comments on this drawing should be directed to DESC-ECC, Dayton, Ohio 45444, or telephone (513) 296-8525.

6.6 One part - one part number system. The one part - one part number system described below has been developed to allow for transitions between identical generic devices covered by the four major microcircuit requirements documents (MIL-M-38510, MIL-H-38534, MIL-I-38535, and 1.2.1 of MIL-STD-883) without the necessity for the generation of unique PIN's. The four military requirements documents represent different class levels, and previously when a device manufacturer upgraded military product from one class level to another, the benefits of the upgraded product were unavailable to the Original Equipment Manufacturer (OEM), that was contractually locked into the original unique PIN. By establishing a one part number system covering all four documents, the OEM can acquire to the highest class level available for a given generic device to meet system needs without modifying the original contract parts selection criteria.

<u>Military documentation format</u>	<u>Example PIN under new system</u>	<u>Manufacturing source listing</u>	<u>Document listing</u>
New MIL-M-38510 Military Detail Specifications (in the SMD format)	5962-XXXXXZZ(B or S)YY	QPL-38510 (Part 1 or 2)	MIL-BUL-103
New MIL-H-38534 Standardized Military Drawings	5962-XXXXXZZ(H or K)YY	QML-38534	MIL-BUL-103
New MIL-I-38535 Standardized Military Drawings	5962-XXXXXZZ(Q or V)YY	QML-38535	MIL-BUL-103
New 1.2.1 of MIL-STD-883 Standardized Military Drawings	5962-XXXXXZZ(M)YY	MIL-BUL-103	MIL-BUL-103

6.7 Sources of supply for device classes H and K. Sources of supply for device classes H and K are listed in QML-38534. The vendors listed in QML-38534 have submitted a certificate of compliance (see 3.7 herein) to DESC-ECC and have agreed to this drawing.

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STANDARDIZED MILITARY DRAWING SOURCE APPROVAL BULLETIN

DATE: 91-07-02

Approved sources of supply for SMD 5962-90833 are listed below for immediate acquisition only and shall be added to QML-38534 during the next revision. QML-38534 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DESC-ECC. This bulletin is superseded by the next dated revision of QML-38534.

Standardized military drawing PIN	Vendor CAGE number	Vendor similar PIN 1/
5962-9083301HXX	62839	CLC201A8C

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number

62839

Vendor name and address

Comlinear Corporation
4800 Wheaton Drive
Fort Collins, CO 80525-9483

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in this information bulletin.