

REVISIONS

LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Add device types 05 and 06. Correct coupling digram, figure 4. Add common mode input voltage and test circuit. Table I, change I _{SIH} . Editorial changes throughout.	91-05-17	W. Heckman
B	Changes made in accordance with 5962-R034-92.	91-11-06	Gregory Lude
C	Update drawing to the latest requirements. -sld	05-08-24	Raymond Monnin
D	Added device type 07. -sld	06-05-25	Raymond Monnin
E	Added device type 08. -sld	09-02-04	Robert M. Heber
F	Updated drawing to the latest requirements of MIL-PRF-38534. -sld	15-03-17	Charles F. Saffle



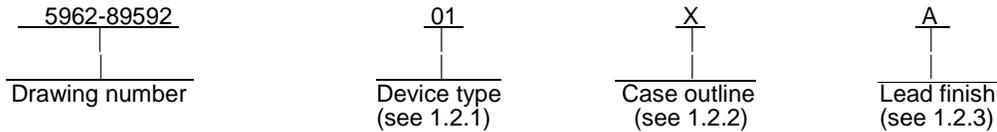
THE ORIGINAL FIRST PAGE OF THIS DRAWING HAS BEEN REPLACED

REV																				
SHEET																				
REV	F	F	F																	
SHEET	15	16	17																	
REV STATUS OF SHEETS	REV			F	F	F	F	F	F	F	F	F	F	F	F	F	F	F	F	
	SHEET			1	2	3	4	5	6	7	8	9	10	11	12	13	14			
PMIC N/A	PREPARED BY Donald R. Osborne						<p align="center">DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990 http://www.landandmaritime.dla.mil/</p>													
<p align="center">STANDARD MICROCIRCUIT DRAWING</p> <p align="center">THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE</p> <p align="center">AMSC N/A</p>	CHECKED BY Ray Monnin																			
	APPROVED BY Michael Frye																			
	DRAWING APPROVAL DATE 20 JUNE 1989																			
	REVISION LEVEL F																			
						SIZE A	CAGE CODE 67268	5962-89592												
						SHEET 1 OF 17														

1. SCOPE

1.1 Scope. This drawing describes device requirements for class H hybrid microcircuits to processed in accordance with MIL-PRF-38534.

1.2 PIN. The PIN shall be as shown in the following example:



1.2.1 Device type(s). The device type(s) identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>
01 and 03	ARX3407	Single channel, driver-receiver (MIL-STD-1553 A and B) low power, receiver standby high
02 and 04 ^{1/}	ARX3404	Single channel, driver-receiver (universal transceiver) low power, receiver standby high
05	ARX3467	Single channel, driver-receiver (MIL-STD-1553 A and B) low power, receiver standby low
06	ARX3464	Single channel, driver-receiver (universal transceiver) low power, receiver standby low
07	ACT4406N	Single channel, driver-receiver (universal transceiver) low power, receiver standby high
08	ACT4404N	Single channel, driver-receiver (universal transceiver) low power, receiver standby low

1.2.2 Case outline(s). The case outline(s) are as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
X	See figure 1	24	Hybrid package
Y	See figure 1	24	Flat package

1.2.3 Lead finish. The lead finish shall be as specified in MIL-PRF-38534.

1.3 Absolute maximum ratings. ^{2/}

Supply voltage ranges:	
V _{CC}	-0.3 V dc to +18 V dc
V _{EE}	+0.3 V dc to - 18 V dc
+5 V supply.....	-0.3 V dc to +7 V dc
Logic input voltage range.....	-0.3 V dc to +5.5 V dc
Receiver differential input voltage.....	40 Vp-p
Receiver common mode input voltage range.....	-10 V to +10 V
Driver peak output current.....	±300 mA
Storage temperature range.....	-65°C to +150°C
Lead temperature (soldering, 10 seconds).....	+300°C
Junction temperature (T _J).....	+160°C

^{1/} The device types 02 and 04 are no longer available, device type 07 can be used as a substitute.

^{2/} Stresses above the absolute maximum ratings may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.

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Power dissipation (P_D) total hybrid:	
100 percent duty cycle ($T_C = +25^\circ\text{C}$):	
Device types 01, 03, and 05.....	3.0 W
Device types 02, 04, 06, 07, and 08.....	3.24 W
Standby mode, all devices.....	1.06 W
Power dissipation (P_D) hottest die:	
100 percent duty cycle:	
Device types 01, 03, and 05.....	484 mW <u>1/</u> <u>2/</u>
Device types 02, 04, 06.....	545 mW <u>2/</u> <u>3/</u>
Standby mode, all devices.....	Derates to zero
Thermal resistance:	
Junction-to-case (θ_{JC}) hottest die:	
Device types 01, 02, 05, and 06.....	88°C/W
Device types 03 and 04.....	58°C/W
Device types 07 and 08.....	5°C/W
Case-to-ambient, all devices.....	21°C/W
Maximum junction-to-case temperature rise for the hottest die at 100 percent duty cycle:	
Device types 01 and 05.....	42.6°C
Device types 02 and 06.....	47.9°C
Device type 03.....	28°C
Device type 04.....	31.6°C
Device types 07 and 08.....	16.25°C

1.4 Recommended operating conditions.

Supply voltage ranges:	
V_{CC}	+11.4 V dc to +15.75 V dc
V_{EE}	-11.4 V dc to -15.75 V dc
+5 V supply.....	+4.5 V dc to +5.5 V dc
Logic input voltage range.....	0 V dc to +5.0 V dc
Receiver differential voltage.....	40 Vp-p
Receiver common mode voltage range.....	-10 V to +10 V
Driver peak output current:	
Device types 01, 02, 05, 06, 07, and 08.....	±180 mA
Device types 03 and 04.....	±120 mA
Maximum serial data rate.....	1.0 MHz
Case operating temperature range (T_C).....	-55°C to +125°C

2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATIONS

MIL-PRF-38534 - Hybrid Microcircuits, General Specification for.

- 1/ Duty cycle at 1.0 MHz for device types 01 and 05. Derate to 82 percent maximum at $T_C = +125^\circ\text{C}$ for a maximum junction temperature of +160°C in the hottest die.
- 2/ Duty cycle for device types 03, 04, 07, and 08 needs no derating over the full operating temperature range.
- 3/ Duty cycle at 1.0 MHz for device types 02 and 06. Derate to 73 percent maximum at $T_C = +125^\circ\text{C}$ for a maximum junction temperature of +160°C in the hottest die.

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DEPARTMENT OF DEFENSE STANDARDS

- MIL-STD-883 - Test Method Standard Microcircuits.
- MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

DEPARTMENT OF DEFENSE HANDBOOKS

- MIL-HDBK-103 - List of Standard Microcircuit Drawings.
- MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at <http://quicksearch.dla.mil/> or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Item requirements. The individual item performance requirements for device classes D, E, G, H, and K shall be in accordance with MIL-PRF-38534. Compliance with MIL-PRF-38534 shall include the performance of all tests herein or as designated in the device manufacturer's Quality Management (QM) plan or as designated for the applicable device class. The manufacturer may eliminate, modify or optimize the tests and inspections herein, however the performance requirements as defined in MIL-PRF-38534 shall be met for the applicable device class. In addition, the modification in the QM plan shall not affect the form, fit, or function of the device for the applicable device class.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38534 and herein.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.4 herein and figure 1.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 2.

3.2.3 Test circuit. The test circuit shall be as specified on figure 3.

3.2.4 Timing waveforms. The timing waveforms shall be as specified in figure 4.

3.2.5 Coupling diagram. The coupling digram shall be as specified in figure 5.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full specified operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are defined in table I.

3.5 Marking of device(s). Marking of device(s) shall be in accordance with MIL-PRF-38534. The device shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's vendor similar PIN may also be marked.

3.6 Data. In addition to the general performance requirements of MIL-PRF-38534, the manufacturer of the device described herein shall maintain the electrical test data (variables format) from the initial quality conformance inspection group A lot sample, for each device type listed herein. Also, the data should include a summary of all parameters manually tested, and for those which, if any, are guaranteed. This data shall be maintained under document revision level control by the manufacturer and be made available to the preparing activity (DLA Land and Maritime -VA) upon request.

3.7 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to supply to this drawing. The certificate of compliance (original copy) submitted to DLA Land and Maritime -VA shall affirm that the manufacturer's product meets the performance requirements of MIL-PRF-38534 and herein.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions <u>1/</u> -55°C ≤ T _C ≤ +125°C unless otherwise specified	Group A subgroups	Device types	Limits		Unit
					Min	Max	
Receiver							
Output low voltage	V _{OL}	I _{OL} = 10 mA	1,2,3	01-06		0.5	V
		I _{OL} = 4 mA		07,08			
Output high voltage	V _{OH}	I _{OH} = -0.4 mA	1,2,3	All	2.5		V
Differential input voltage level	V _I		4,5,6	All		40	V _{p-p}
Common mode input voltage range	V _{ICR}	1 MHz sine wave See figure 3	4,5,6	All	10		V _{p-p}
Differential input impedance	Z _{IN}	1 MHz sine wave	4,5,6	All	10		kΩ
Input capacitance	C _{IN}	1 MHz sine wave <u>2/</u>	4	All		5	pF
Threshold voltage	V _{TH}	<u>3/</u> <u>4/</u>	4,5,6	01-06	0.6	1.05	V _{p-p}
				07,08	0.6	1.10	
Receiver delay	t _{DR}	Input zero crossing <u>2/</u> to DATA or <u>DATA</u> , see figure 4	9,10,11	All		450	ns
Receiver strobe							
Input low voltage	V _{SIL}		1,2,3	All		0.7	V
Input high voltage	V _{SIH}		1,2,3	All	2.0		V
Input low current	I _{SIL}	V _{SIL} = 0.4 V	1,2,3	All	-0.4		mA
Input high current	I _{SIH}	V _{SIH} = 2.7 V	1,2,3	All		80	μA
Strobe delay	t _{DS}	Turn-on or turn-off, <u>2/</u> see figure 4	9,10,11	All		200	ns
See footnotes at end of table.							
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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions <u>1/</u> -55°C ≤ T _C ≤ +125°C unless otherwise specified	Group A subgroups	Device types	Limits		Unit
					Min	Max	
Transmitter							
Input low voltage	V _{IL}		1,2,3	All		0.7	V
Input high voltage	V _{IH}		1,2,3	All	2.0		V
Input low current	I _{IL}	V _{IL} = 0.4 V	1,2,3	All	-0.4		mA
Input high current	I _{IH}	V _{IH} = 2.7 V	1,2,3	All		40	μA
Differential output voltage	V _O	35Ω load <u>4/</u>	1,2,3	All	6.5	8.5	Vp-p
		140Ω load <u>5/</u>			26	34	
Differential output noise	V _{ON}	Inhibited 35Ω load <u>4/</u>	4,5,6	All		10	mVp-p
		140Ω load <u>5/</u>				40	
Differential output impedance	Z _{OUT}	1 MHz sine wave (transmitter off)	4,5,6	All	10		kΩ
Output capacitance	C _{OUT}	1 MHz sine wave <u>2/</u>	4	All		5	pf
Differential output offset voltage	V _{OS}	35Ω load	4,5,6	All	-90	+90	mV pk
		140Ω load <u>5/ 6/</u>			-360	+360	
Receiver filter response	FILTER	f = 2 MHz	4,5,6	All	-4.0		dB
		f = 4 MHz			-13		
Rise time	t _R	35Ω load, see figure 4	9,10,11	01,03, 05	100	300	ns
				02,04, 06	220	300	
				07,08	200	300	

See footnotes at end of table.

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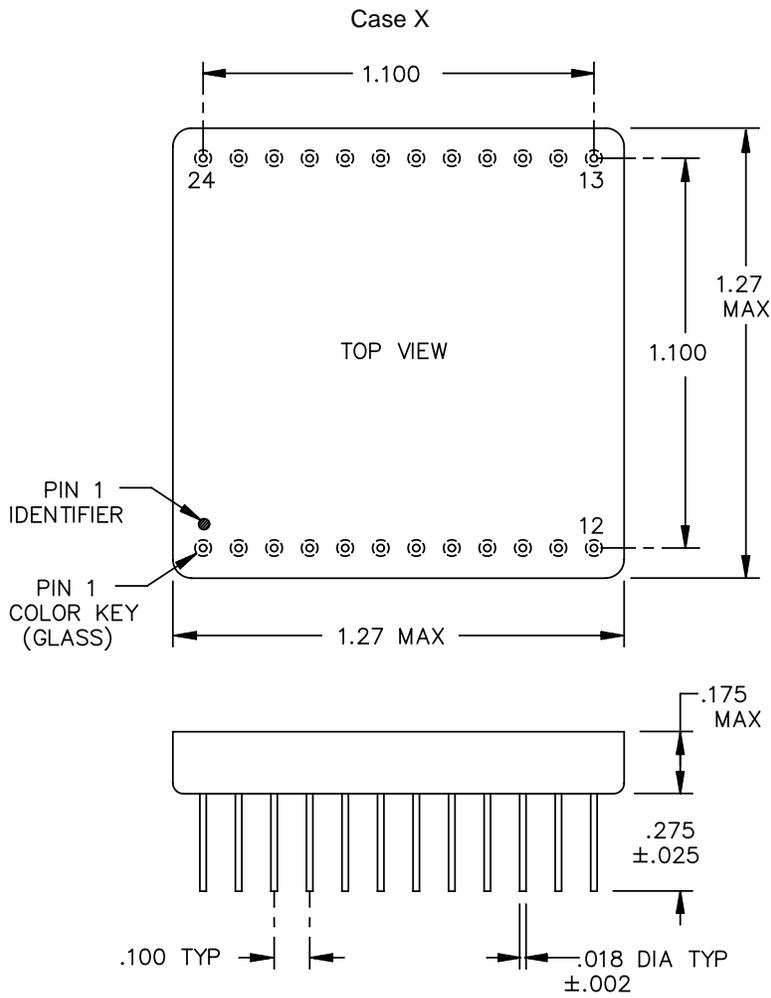
TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions <u>1</u> / -55°C ≤ T _C ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Transmitter - continued.							
Fall time	t _F	35Ω load, see figure 4	9,10,11	01,03,05	100	300	ns
				02,04,06	220	300	
				07,08	200	300	
Transmitter delay	t _{DT}	Transmitter-in to transmitter-out, see figure 4 <u>2</u> /	9,10,11	All		550	ns
Transmitter inhibit							
Input low voltage	V _{IIL}		1,2,3	All		0.7	V
Input high voltage	V _{IIH}		1,2,3	All	2.0		V
Input low current	I _{IIL}	V _{SIL} = 0.4 V	1,2,3	All	-0.4		mA
Input high current	I _{IIH}	V _{SIH} = 2.7 V	1,2,3	All		40	μA
Transmitter inhibit delay (high)	t _{DI-H}	0-1 inhibited output, see figure 4 <u>2</u> /	9,10,11	01-06		450	ns
				07,08		500	
Transmitter inhibit delay (low)	t _{DI-L}	1-0 active output, see figure 4 <u>2</u> /	9,10,11	01-06		450	ns
				07,08		500	
Power supply							
+Supply (V _{CC}) -Supply (V _{EE}) +5 V supply	I _{CC-SB} I _{EE-SB} I _{CC1-SB}	Standby mode	1,2,3	All		20 35 35	mA
+Supply (V _{CC}) -Supply (V _{EE})	I _{CC-50} I _{EE-50}	50% duty cycle	4,5,6	All		75 92.5	mA
+Supply (V _{CC}) -Supply (V _{EE})	I _{CC-100} I _{EE-100}	100% duty cycle	4,5,6	All		130 150	mA
See footnotes at top of next page.							
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TABLE I. Electrical performance characteristics - Continued.

- 1/ (+14.9 ≤ V_{CC} ≤ +15.1 V dc), (-14.9 ≤ V_{EE} ≤ -15.1 V dc), and (+4.9 ≤ +5 V supply ≤ +5.1 V dc).
- 2/ Parameter shall be tested as part of initial characterization and after design and process changes. Parameters shall be guaranteed to the limits specified in table I for all lots not specifically tested.
- 3/ Threshold determined by absence of output on receiver (i.e., full rejection).
- 4/ Measured at point AA' of figure 5.
- 5/ Measured at point BB' of figure 5.
- 6/ Offset is measured 2.5 μs after the mid-bit zero crossing of the last parity bit of a 660 μs transmission cycle of contiguous words (no dead time in between words).

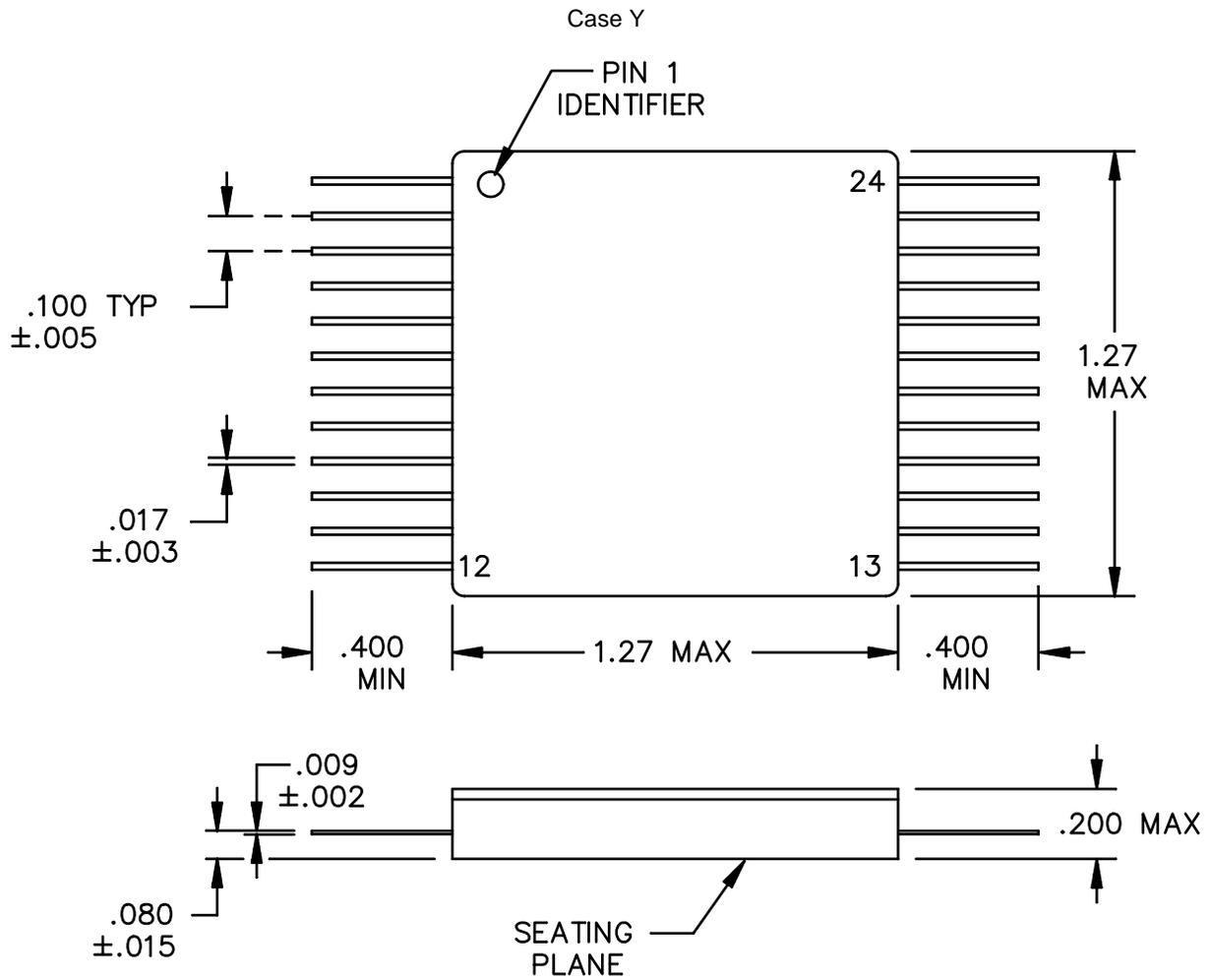
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Inches	mm
.002	0.05
.003	0.08
.005	0.13
.009	0.23
.015	0.38
.017	0.43
.018	0.46
.025	0.64
.080	2.03
.100	2.54
.175	4.46
.200	5.08
.275	6.98
.400	10.16
1.100	27.94
1.27	32.25

FIGURE 1. Case outlines.

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NOTES (for case X and case Y):

1. Dimensions are in inches.
2. Metric equivalents are given for general information only.
3. Unless otherwise specified, tolerance for three place decimals shall be .005 (0.13 mm).

FIGURE 1. Case outline(s) - Continued.

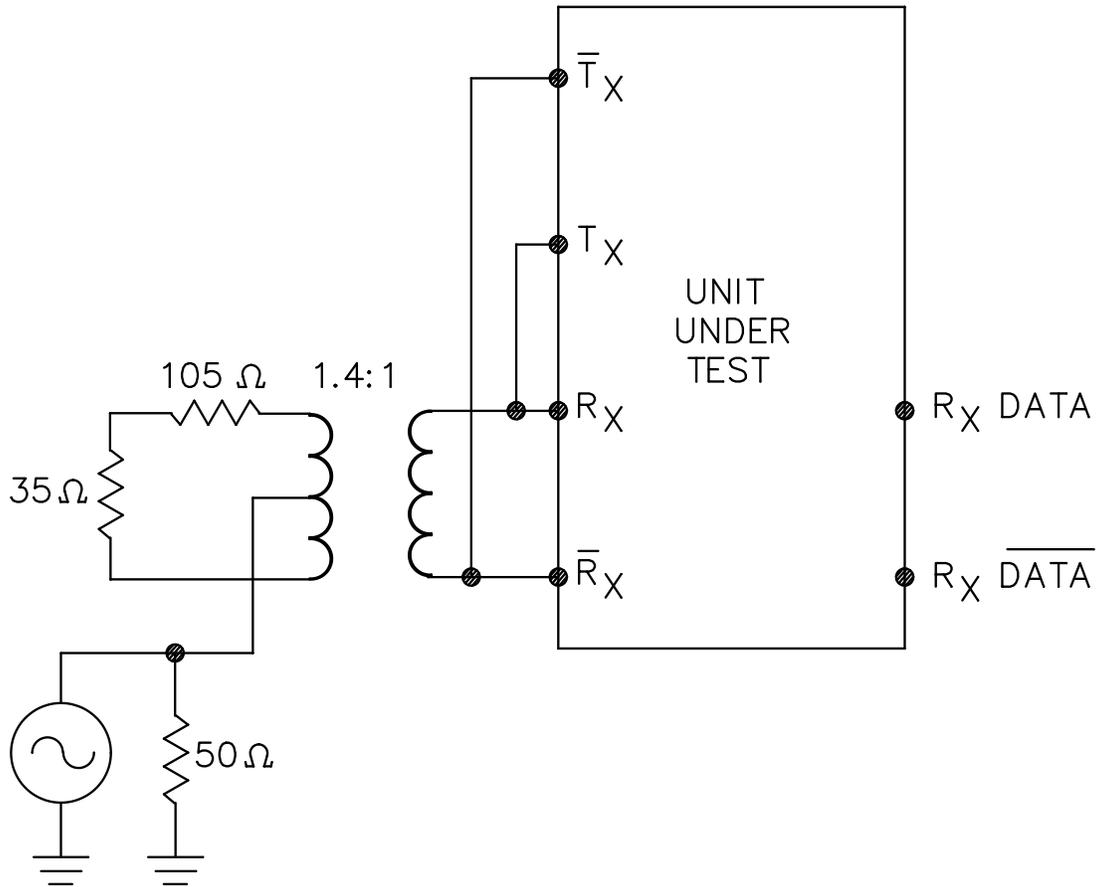
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Device types	All
Case outlines	X and Y
Pin	Function
1	TX DATA OUT
2	TX DATA OUT
3	TX GND
4	+15 V
5	NO CONNECTION
6	NO CONNECTION
7	RX DATA OUT
8	RX STROBE
9	RX GND
10	RX DATA OUT
11	NO CONNECTION
12	NO CONNECTION
13	+15 V
14	NO CONNECTION
15	RX DATA IN
16	RX DATA IN
17	GND
18	CASE GND
19	-15 V
20	+5 V
21	TX INHIBIT
22	TX DATA IN
23	TX DATA IN
24	-15 V

FIGURE 2. Terminal connections.

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COMMON-MODE TEST

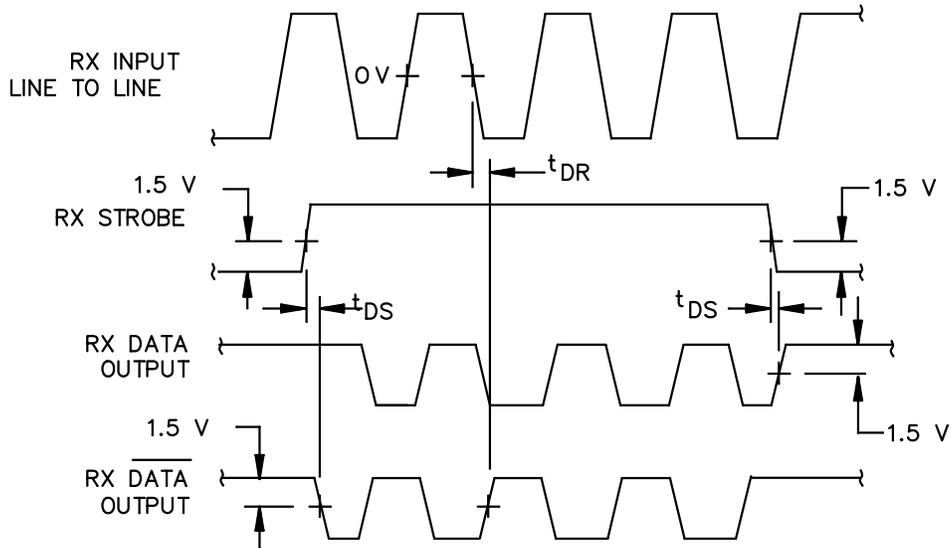


OBSERVE NO TRANSMISSION OF DATA AT RX DATA AND RX $\overline{\text{DATA}}$

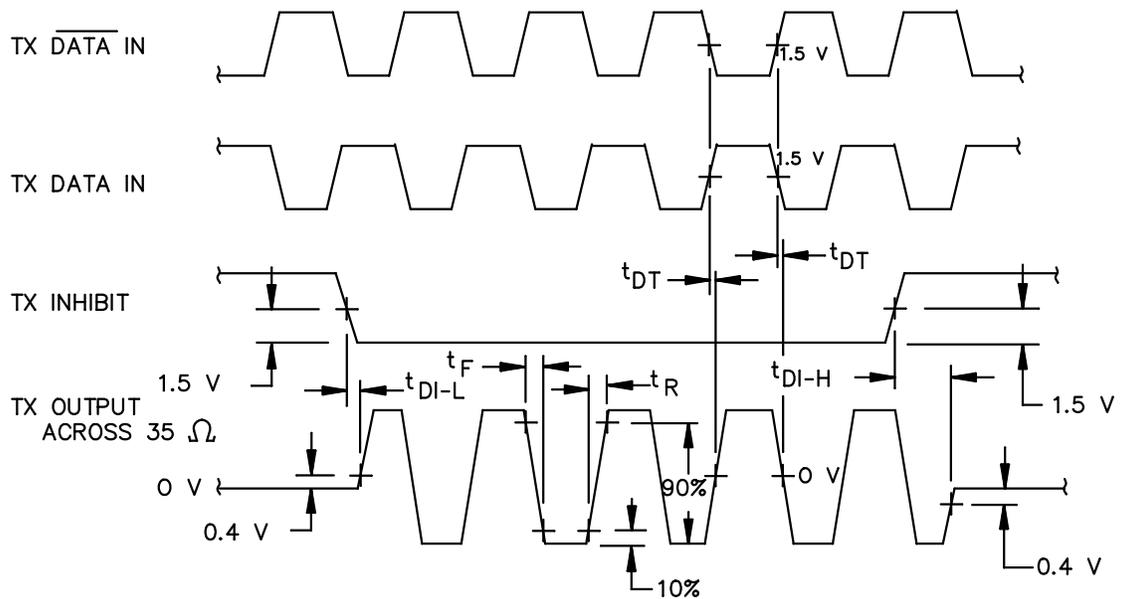
FIGURE 3. Test circuit.

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RECEIVER TIMING



TRANSMITTER TIMING



Device types 01, 02, 03, 04, and 07.

FIGURE 4. Timing waveforms.

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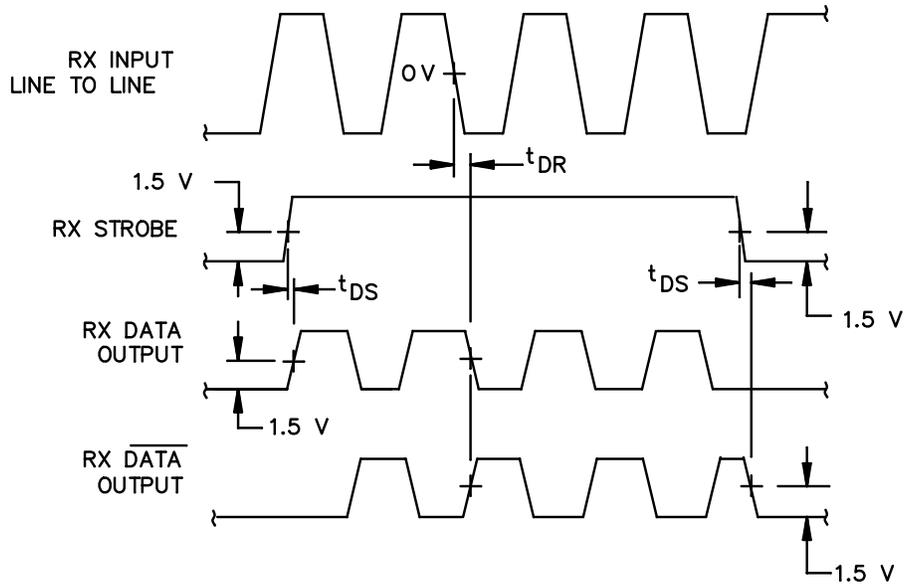
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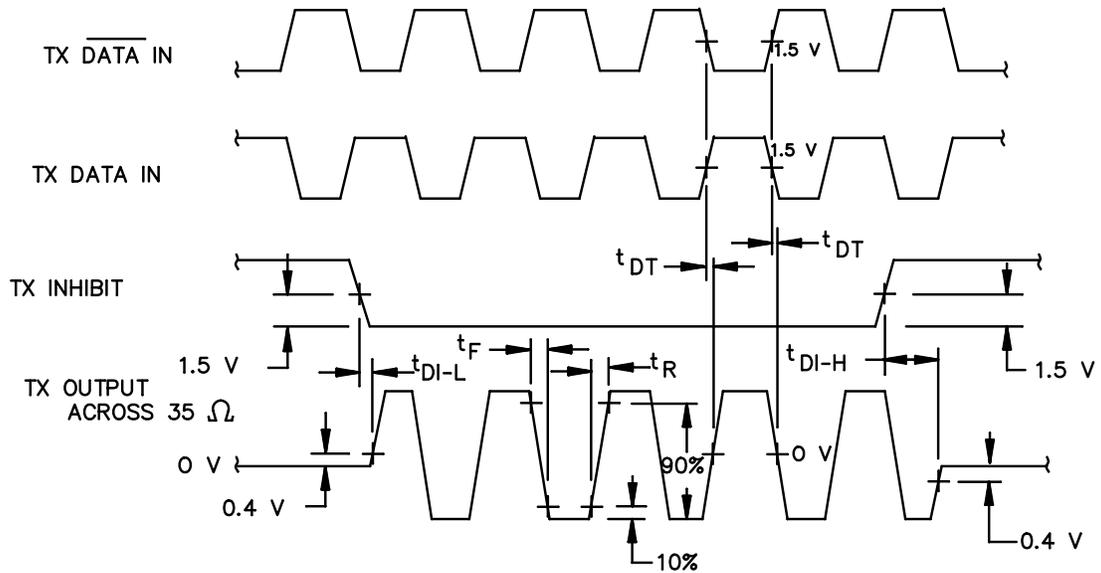
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RECEIVER TIMING



TRANSMITTER TIMING



Device types 05, 06, and 08.

FIGURE 4. Timing waveforms - Continued.

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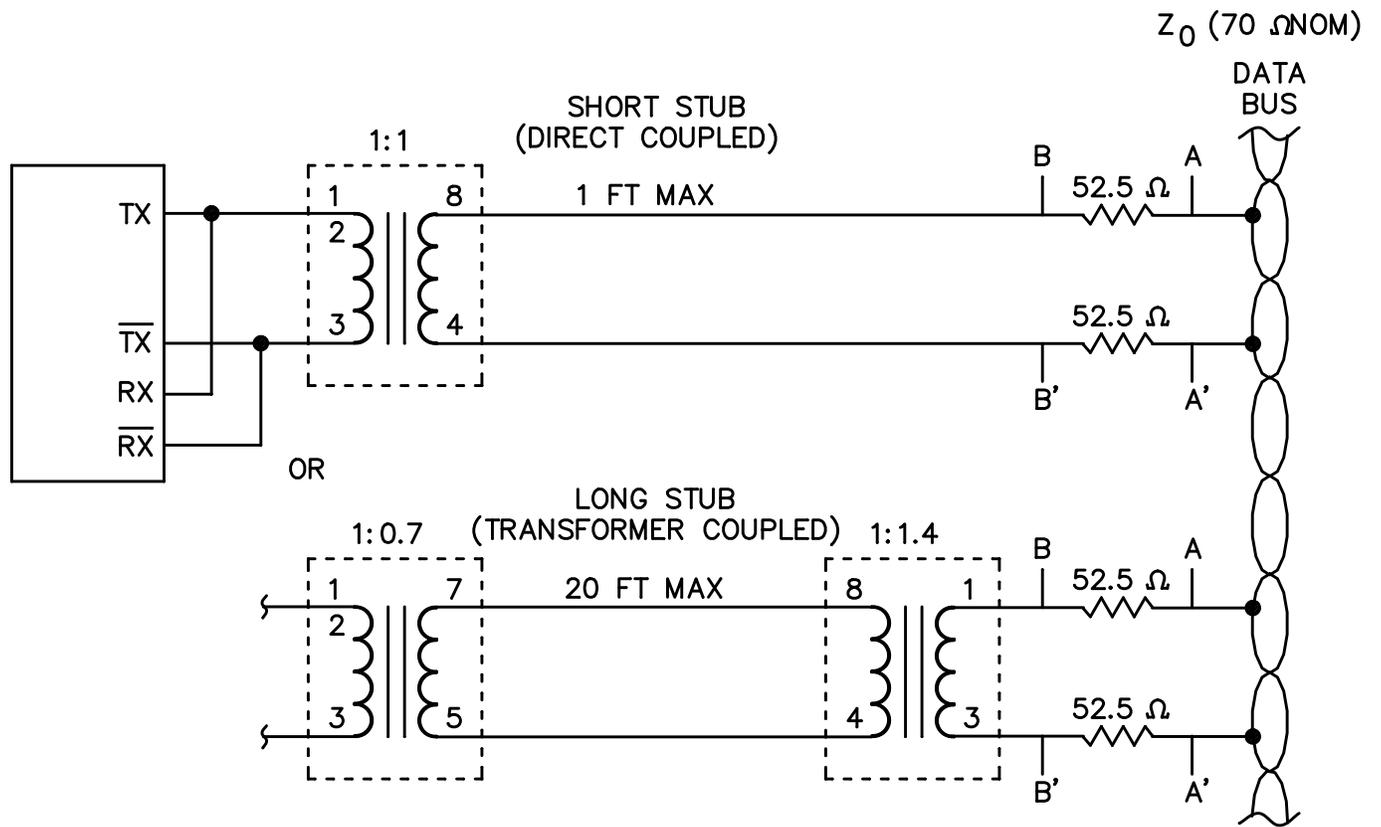


FIGURE 5. Coupling diagram.

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TABLE II. Electrical test requirements.

MIL-PRF-38534 test requirements	Subgroups (in accordance with MIL-PRF-38534, group A test table)
Interim electrical parameters	1
Final electrical parameters	1*, 2, 3, 4, 5, 6, 9, 10, 11
Group A test requirements	1, 2, 3, 4, 5, 6, 9, 10, 11
Group C end-point electrical parameters	1, 2, 3, 4, 5, 6, 9, 10, 11
End-point electrical parameters for Radiation Hardness Assurance (RHA) devices	Not applicable

* PDA applies to subgroup 1.

3.8 Certificate of conformance. A certificate of conformance as required in MIL-PRF-38534 shall be provided with each lot of microcircuits delivered to this drawing.

4. VERIFICATION

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38534 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

4.2 Screening. Screening shall be in accordance with MIL-PRF-38534. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

- (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DLA Land and Maritime -VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.
- (2) T_A as specified in accordance with table I of method 1015 of MIL-STD-883.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Conformance and periodic inspections. Conformance inspection (CI) and periodic inspection (PI) shall be in accordance with MIL-PRF-38534 and as specified herein.

4.3.1 Group A inspection (CI). Group A inspection shall be in accordance with MIL-PRF-38534 and as follows:

- a. Tests shall be as specified in table II herein.
- b. Subgroups 7 and 8 shall be omitted.

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4.3.2 Group B inspection (PI). Group B inspection shall be in accordance with MIL-PRF-38534.

4.3.3 Group C inspection (PI). Group C inspection shall be in accordance with MIL-PRF-38534 and as follows:

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test, method 1005 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DLA Land and Maritime -VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.
 - (2) T_A as specified in accordance with table I of method 1005 of MIL-STD-883.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

4.3.4 Group D inspection (PI). Group D inspection shall be in accordance with MIL-PRF-38534.

4.3.5 Radiation Hardness Assurance (RHA) inspection. RHA inspection is not currently applicable to this drawing.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38534.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated as specified in MIL-PRF-38534.

6.4 Record of users. Military and industrial users shall inform DLA Land and Maritime when a system application requires configuration control and the applicable SMD to that system. DLA Land and Maritime will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DLA Land and Maritime-VA, telephone (614) 692-8108.

6.5 Comments. Comments on this drawing should be directed to DLA Land and Maritime-VA, Columbus, Ohio 43218-3990, or telephone (614) 692-1081.

6.6 Sources of supply. Sources of supply are listed in MIL-HDBK-103 and QML-38534. The vendors, listed in MIL-HDBK-103 and QML-38534, have submitted a certificate of compliance (see 3.7 herein) to DLA Land and Maritime-VA and have agreed to this drawing.

STANDARD MICROCIRCUIT DRAWING DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE A		5962-89592
		REVISION LEVEL F	SHEET 17

STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 15-03-17

Approved sources of supply for SMD 5962-89592 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38534 during the next revisions. MIL-HDBK-103 and QML-38534 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DLA Land and Maritime -VA. This information bulletin is superseded by the next dated revisions of MIL-HDBK-103 and QML-38534. DLA Land and Maritime maintains an online database of all current sources of supply at <http://www.landandmaritime.dla.mil/Programs/Smcr/>.

Standard microcircuit drawing PIN <u>1/</u>	Vendor CAGE number	Vendor similar PIN <u>2/</u>
5962-8959201XA 5962-8959201XC 5962-8959201YA 5962-8959201YC	<u>3/</u> <u>3/</u> <u>3/</u> <u>3/</u>	ARX3407-001-2 ARX3407-001-1 ARX3407-201-2 ARX3407-201-1
5962-8959202XA 5962-8959202XC 5962-8959202YA 5962-8959202YC	<u>3/</u> <u>3/</u> <u>3/</u> <u>3/</u>	ARX3404 ARX3404 ARX3404FP ARX3404FP
5962-8959203XA 5962-8959203XC 5962-8959203YA 5962-8959203YC	<u>3/</u> <u>3/</u> <u>3/</u> <u>3/</u>	ARX3407-001-4 ARX3407-001-3 ARX3407-201-4 ARX3407-201-3
5962-8959204XA 5962-8959204XC 5962-8959204YA 5962-8959204YC	<u>3/</u> <u>3/</u> <u>3/</u> <u>3/</u>	ARX3404-001 ARX3404-001 ARX3404-001FP ARX3404-001FP
5962-8959205XA 5962-8959205XC 5962-8959205YA 5962-8959205YC	<u>3/</u> <u>3/</u> <u>3/</u> <u>3/</u>	ARX3467-001-2 ARX3467-001-1 ARX3467-201-2 ARX3467-201-1
5962-8959206XA 5962-8959206XC 5962-8959206YA 5962-8959206YC	<u>3/</u> <u>3/</u> <u>3/</u> <u>3/</u>	ARX3464-001-2 ARX3464-001-1 ARX3464-201-2 ARX3464-201-1
5962-8959207XA 5962-8959207XC 5962-8959207YA 5962-8959207YC	88379 88379 88379 88379	ACT4406N-001-2 ACT4406N-001-1 ACT4406N-201-2 ACT4406N-201-1
5962-8959208XA 5962-8959208XC 5962-8959208YA 5962-8959208YC	88379 88379 88379 88379	ACT4404N-001-2 ACT4404N-001-1 ACT4404N-201-2 ACT4404N-201-1

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the Vendor to determine its availability.
- 2/ **Caution.** Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.
- 3/ This device is no longer available from the approved source of supply.

STANDARD MICROCIRCUIT DRAWING BULLETIN - CONTINUED.

DATE: 15-03-17

Vendor CAGE
number

88379

Vendor name
and address

Aeroflex Plainview, Incorporated
(Aeroflex Microelectronics Solutions)
35 South Service Road
Plainview, NY 11803-4193

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in the information bulletin.