

REVISIONS

LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
B	Superseded drawing with 5962-38294.	1990 OCT 29	M. A. Frye

This drawing 5962-85525, is superseded by drawing 5962-38294 as of 29 OCT 1990. Specific part substitution information is included in 5962-38294.

REV																				
SHEET																				
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REV STATUS OF SHEETS	REV	B																		
	SHEET	1																		

PMIC N/A	PREPARED BY Kenneth Rice	DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444																	
STANDARDIZED MILITARY DRAWING THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE AMSC N/A	CHECKED BY Charles Reusing	MICROCIRCUIT, MEMORY, DIGITAL CMOS, 8K X 8 STATIC RANDOM ACCESS MEMORY (SRAM), MONOLITHIC SILICON																	
	APPROVED BY Michael A. Frye																		
	DRAWING APPROVAL DATE 24 OCTOBER 1986	SIZE A	CAGE CODE 67268	5962-85525															
	REVISION LEVEL B	SHEET 1 OF 1																	

DESC FORM 193
SEP 87

5962-E1800

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