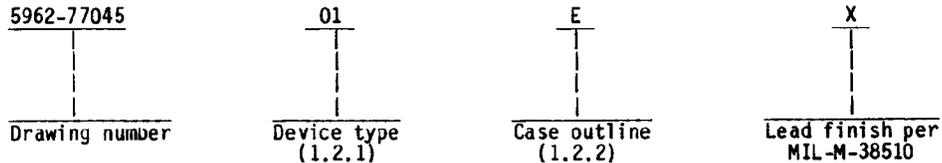


1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:



1.2.1 Device types. The device types shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	4528B	Dual retriggerable/resetable monostable multivibrator
02	4528B	Dual retriggerable/resetable monostable multivibrator

1.2.2 Case outlines. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter	Case outline
E	D-2 (16-lead, 1/4" x 7/8"), dual-in-line package
F	F-5 (16-lead, 1/4" x 3/8"), flat package

1.3 Absolute maximum ratings.

Supply voltage range	-----	-0.5 V dc to +18 V dc
Input voltage range	-----	-0.5 to V_{DD} +0.5 V dc
Storage temperature range	-----	-65°C to +150°C
Maximum power dissipation (P_D) 1/	-----	500 mW dc
Lead temperature (soldering, 10 seconds)	-----	+300°C
Thermal resistance, junction-to-case (θ_{JC}):		
Case E	-----	0.09°C/mW
Case F	-----	0.08°C/mW
Junction temperature (T_J)	-----	+175°C

1.4 Recommended operating conditions.

Supply voltage (V_{CC})	-----	+3 V dc to +18 V dc
Case operating temperature range (T_C)	-----	-55°C to +125°C

1/ Must withstand the added P_D due to snort circuit test (e.g., I_{QS}).

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2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Truth table. The truth table shall be as specified on figure 2.

3.2.3 Logic diagram. The logic diagram shall be as specified on figure 3.

3.2.4 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full recommended case operating temperature range.

3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.

3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions $-55^{\circ}\text{C} \leq T_C \leq +125^{\circ}\text{C}$	Device type	Group A subgroups	Limits		Unit
					Min	Max	
High level output voltage	V_{OH}	$V_{DD} = 5\text{ V}; V_{IN} = 0 \text{ or } V_{DD}$ 15 V	A11	1,2,3	4.95 14.95		V
Low level output voltage	V_{OL}	$V_{DD} = 5\text{ V}; V_{IN} = V_{DD} \text{ or } 0$ 15 V	A11	1,2,3		.05 .05	V
High level input voltage	V_{IH}	$V_{DD} = 5\text{ V}; V_{IN} = V_O = 0.5 \text{ or } 4.5\text{ V}$ 15 V 1.5 or 13.5 V	A11	1,2,3	3.5 11.0		V
Low level input voltage	V_{IL}	$V_{DD} = 5\text{ V}; V_O = 4.5 \text{ or } 0.5\text{ V}$ 15 V 13.5 or 1.5 V	A11	1,2,3		1.5 4.0	V
Output drive current (source)	I_{OH}	$V_{DD} = 5\text{ V}; V_{OH} = 2.5\text{ V}$ 5 V 4.6 V 15 V 13.5 V	A11	1,2,3	-0.7 -0.14 -1.1		mA
Output drive current (sink)	I_{OL}	$V_{DD} = 5\text{ V}; V_{OL} = 0.4\text{ V}$ 15 V 1.5 V	A11	1,2,3	0.36 2.4		mA
Functional tests		See 4.3.1d	A11	7			
Input capacitance	C_{IN}	$V_{IN} = 0$ $T_C = +25^{\circ}\text{C}$	01	4		7.5	pF
			02			10.0	
Quiescent current	I_{DD}	$V_{DD} = 15\text{ V}$	A11	1,2,3		600	μA
Propagation delay time A or B to Q or \bar{Q}	t_{PLH} t_{PHL}	$V_{DD} = 5\text{ V}$ $R_L = 200\text{ k}\Omega$ $C_L = 50\text{ pF} \pm 10\%$ $T_C = +25^{\circ}\text{C}$	A11	9	2	540	ns
			A11	10,11	2	735	ns
Reset propagation delay time, CD to Q or \bar{Q}	t_{PLH} t_{PHL}	$V_{DD} = 5\text{ V}$ $R_L = 200\text{ k}\Omega$ $C_L = 50\text{ pF} \pm 10\%$ $T_C = +25^{\circ}\text{C}$	A11	9	2	540	ns
			A11	10,11	2	735	ns

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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions $-55^{\circ}\text{C} \leq T_C \leq +125^{\circ}\text{C}$	Device type	Group A subgroups	Limits		Unit
					Min	Max	
Output rise time	t_r	$V_{DD} = 5\text{ V}$ $R_L = 5\text{ k}\Omega$; $C_L = 50\text{ pF} \pm 10\%$ $T_C = +25^{\circ}\text{C}$	A11	9	2	350	ns
Output fall time	t_f	$V_{DD} = 5\text{ V}$ $R_L = 5\text{ k}\Omega$; $C_L = 50\text{ pF} \pm 10\%$ $T_C = +25^{\circ}\text{C}$	A11	9	2	175	ns
Minimum input pulse width - A or B	$t_{W(in)}$	$V_{DD} = 5\text{ V}$ $R_L = 5\text{ k}\Omega$; $C_L = 50\text{ pF} \pm 10\%$ $T_C = +25^{\circ}\text{C}$	A11	9		150	ns
Output pulse width Q or \bar{Q}	$t_{W(out)}$	$V_{DD} = 5\text{ V}$ $C_X = 1000\text{ pF}$ $C_L = 50\text{ pF} \pm 10\%$ $R_X = 10\text{ k}\Omega$ $T_C = +25^{\circ}\text{C}$	01	9	4.35	8	μS
Output pulse width Q or \bar{Q}	$t_{W(out)}$	$V_{DD} = 5\text{ V}$ $C_X = 15\text{ pF}$ $C_L = 50\text{ pF} \pm 10\%$ $R_X = 5\text{ k}\Omega$ $T_C = +25^{\circ}\text{C}$	01	9		500	ns
			02			800	
Output pulse width Q or \bar{Q} <u>1/</u>	$t_{W(out)}$	$V_{DD} = 5\text{ V}$ $C_X = 0.1\text{ }\mu\text{F}$ $C_L = 50\text{ pF} \pm 10\%$ $R_X = 261\text{ k}\Omega$ $T_C = +25^{\circ}\text{C}$	A11	9	6	10	ms
Minimum retrigger time	t_{rr}	$V_{DD} = 5\text{ V}$ $C_X = 15\text{ pF}$ $C_L = 50\text{ pF} \pm 10\%$ $R_X = 5\text{ k}\Omega$ $T_C = +25^{\circ}\text{C}$	A11	9	0		ns

1/ Guaranteed, if not tested.

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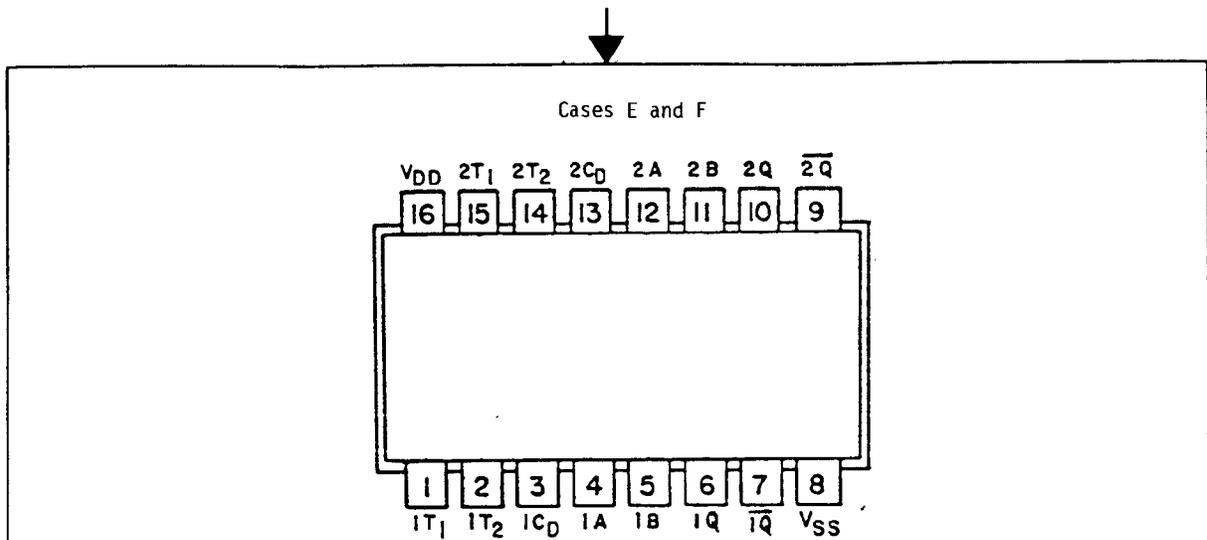


FIGURE 1. Terminal connections (top view).

INPUTS			OUTPUTS	
C _D	A	B	Q	\overline{Q}
L	X	X	L	H
X	H	X	L	H
X	X	L	L	H
H	↑	H		
H	L	↓		

H = High level (steady state)

L = Low level (steady state)

↑ = Transition, low-to-high

↓ = Transition, high-to-low

X = Irrelevant (Inc. transitions)

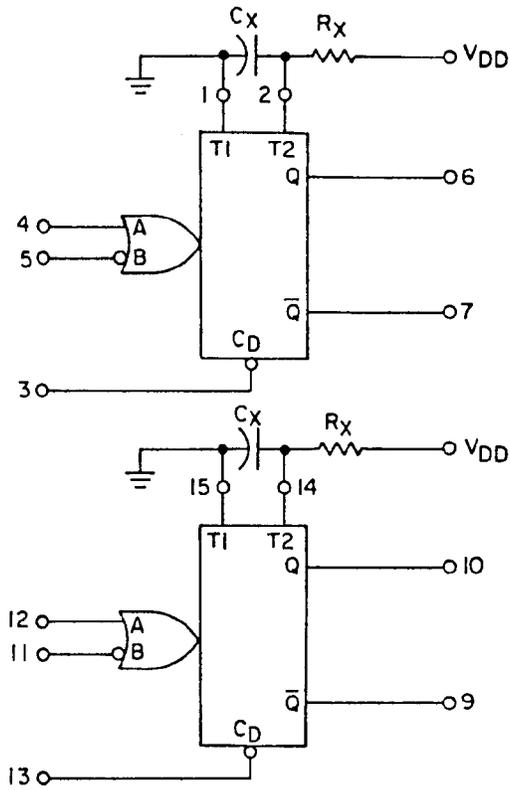
= One high-level pulse

= One low-level pulse

FIGURE 2. Truth table.

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BLOCK DIAGRAM



R_X AND C_X ARE EXTERNAL COMPONENTS

V_{DD} = PIN 16

V_{SS} = PIN 8

NOTE: Terminals 1 and 15 are internally connected to V_{SS} .

FIGURE 3. Logic diagram.

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3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

- a. Burn-in test (method 1015 of MIL-STD-883).
 - (1) Test condition A, B, C, D, or E using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^\circ\text{C}$, minimum.
- b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

- a. Tests shall be as specified in table II herein.
- b. Subgroups 5, 6, and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.
- c. Subgroup 4 (C_{IN} measurement) shall be measured only for the initial test and after process or design changes which may affect input capacitance.
- d. Subgroup 7 tests sufficient to verify the truth table.

4.3.2 Groups C and D inspections.

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test (method 1005 of MIL-STD-883) conditions:
 - (1) Test condition A, B, C, D, or E using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^\circ\text{C}$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by appendix B of MIL-M-38510 and method 1005 of MIL-STD-883.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*, 2, 3, 9
Group A test requirements (method 5005)	1, 2, 3, 4, 7, 9
Groups C and D end-point electrical parameters (method 5005)	1, 2, 3
Additional electrical subgroups for group C periodic inspections	10, 11

*PDA applies to subgroup 1.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

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6.4 Approved sources of supply. Approved sources of supply are listed herein. Additional sources will be added as they become available. The vendors listed herein have agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

Military drawing part number	Vendor CAGE number	Vendor similar part number <u>1/</u>	Replacement military specification part number
7704501EX	31019	883/4528BC	
7704502EX	27014	MM14528MJ/883	
7704501FX	31019	883/4528BF	

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number

31019

27014

Vendor name and address

Sprague Electric Company
3900 Welsh Road
Willow Grove, PA 19090

National Semiconductor
2900 Semiconductor Drive
P.O. Box 58090
Santa Clara, CA 95052-8090

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