

REVISIONS

LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Added radiation hardness assurance requirements. -sld	12-06-04	Charles F. Saffle
B	Figure 1: Corrected the dimension "e" min limit under the millimeters from "7.62" to ".76 mm". Editorial changes throughout. -sld	16-04-11	Charles F. Saffle



REV																				
SHEET																				
REV	B																			
SHEET	15																			
REV STATUS OF SHEETS	REV	B	B	B	B	B	B	B	B	B	B	B	B	B	B	B	B	B	B	B
	SHEET	1	2	3	4	5	6	7	8	9	10	11	12	13	14					

PMIC N/A	PREPARED BY Steve Duncan	<p align="center">DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990 http://www.landandmaritime.dla.mil/</p>		
<p align="center">STANDARD MICROCIRCUIT DRAWING</p> <p align="center">THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE</p> <p align="center">AMSC N/A</p>	CHECKED BY Greg Cecil			
	APPROVED BY Charles F. Saffle	<p align="center">MICROCIRCUIT, HYBRID, VOLTAGE REGULATOR, POSTIVE AND NEGATIVE, ADJUSTABLE</p>		
	DRAWING APPROVAL DATE 09-12-08			
	REVISION LEVEL B	SIZE A	CAGE CODE 67268	5962-09206
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1.2.4 Case outline(s). The case outline(s) are as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
X	See figure 1	3	Bottom terminal chip carrier, ceramic

1.2.5 Lead finish. The lead finish is as specified in MIL-PRF-38534.

1.3 Absolute maximum ratings. 1/

Input-Output differential voltage:	
Positive regulator	
Device type 01	40 V
Negative regulator:	
Device type 02	-30 V
Operating junction temperature range	-55°C to +150°C
Junction temperature (T _J)	+150°C
Thermal resistance, junction-to-case (θ _{JC}) each regulator	3°C/W
Lead temperature (soldering, 10 seconds)	300°C
Storage temperature range	-65°C to +150°C

1.4 Recommended operating conditions.

Output voltage range:	
Positive voltage regulator	
Device type 01	+1.25 V to +37 V dc
Negative voltage regulator:	
Device type 02	-1.25 V to -27 V dc
Case operating temperature range (T _C)	-55°C to +125°C

1.5 Radiation features. 2/

Maximum total dose available (dose rate = 50 - 300 rads(Si)/s)	100 krad(Si) 3/
Enhanced Low Dose Rate Sensitivity (ELDRS): (dose rate ≤ 10 mrad(Si)/s)	50 krad(Si) 4/

2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATIONS

MIL-PRF-38534 - Hybrid Microcircuits, General Specification for.

- 1/ Stresses above the absolute maximum ratings may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.
- 2/ Bipolar device types may degrade from displacement damage from radiation which could affect RHA levels. These device types have not been characterized for displacement damage.
- 3/ The active elements that make up the devices on this drawing have been tested for Total Ionizing Dose (TID) in accordance with MIL-STD-883 test method 1019 condition A. RHA testing of the active elements covered on this SMD are tested in alternate packages (TO3) and (TO39), not the packages as specified in paragraph 1.2.4.
- 4/ The active elements that make up the devices on this drawing have been tested for Enhanced Low Dose Rate Sensitivity (ELDRS) in accordance with MIL-STD-883, Method 1019 condition D for initial qualification. No ELDRS effect was observed. The devices will be re-tested after design or process changes that can affect RHA response of these devices. RHA testing of the active elements covered on this SMD were done in alternate packages (TO3) and (TO39), not the packages as specified in paragraph 1.2.4.

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DEPARTMENT OF DEFENSE STANDARDS

- MIL-STD-883 - Test Method Standard Microcircuits.
- MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

DEPARTMENT OF DEFENSE HANDBOOKS

- MIL-HDBK-103 - List of Standard Microcircuit Drawings.
- MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at <http://quicksearch.dla.mil> or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Item requirements. The individual item performance requirements for device classes D, E, G, H, and K shall be in accordance with MIL-PRF-38534. Compliance with MIL-PRF-38534 shall include the performance of all tests herein or as designated in the device manufacturer's Quality Management (QM) plan or as designated for the applicable device class. The manufacturer may eliminate, modify or optimize the tests and inspections herein, however the performance requirements as defined in MIL-PRF-38534 shall be met for the applicable device class. In addition, the modification in the QM plan shall not affect the form, fit, or function of the device for the applicable device class.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38534 and herein.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.4 herein and figure 1.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 2.

3.2.3 Block diagram. The block diagram shall be as specified on figure 3.

3.2.4 Maximum power dissipation verses case temperature chart. The maximum power dissipation verses case temperature is specified on figure 4.

3.2.5 Radiation exposure circuits. The radiation exposure circuits shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing and acquiring activity upon request.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full specified operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are defined in table I.

3.5 Marking of device(s). Marking of device(s) shall be in accordance with MIL-PRF-38534. The device shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's vendor similar PIN may also be marked.

3.6 Data. In addition to the general performance requirements of MIL-PRF-38534, the manufacturer of the device described herein shall maintain the electrical test data (variables format) from the initial quality conformance inspection group A lot sample, for each device type listed herein. Also, the data should include a summary of all parameters manually tested, and for those which, if any, are guaranteed. This data shall be maintained under document revision level control by the manufacturer and be made available to the preparing activity (DLA Land and Maritime -VA) upon request.

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3.7 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to supply to this drawing. The certificate of compliance (original copy) submitted to DLA Land and Maritime -VA shall affirm that the manufacturer's product meets the performance requirements of MIL-PRF-38534 and herein.

3.8 Certificate of conformance. A certificate of conformance as required in MIL-PRF-38534 shall be provided with each lot of microcircuits delivered to this drawing.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T _C ≤ +125°C V _{IN} - V _{OUT} = 5 V, I _{OUT} = 0.5 A, P ≤ P _{MAX} unless otherwise specified	Group A subgroups	Device types	Limits		Unit
					Min	Max	
POSITIVE REGULATOR							
Reference voltage <u>1/</u>	V _{REF}	3 V ≤ (V _{IN} - V _{OUT}) ≤ V _{DIFF} Max, 10 mA ≤ I _{OUT} ≤ I _{MAX}	1,2,3	01	1.20	1.30	V
Line regulation <u>1/ 2/</u>	$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$	3 V ≤ (V _{IN} - V _{OUT}) ≤ V _{DIFF} Max, I _{OUT} = 10 mA	1,2,3	01		0.06	%/V
Load regulation <u>1/ 2/</u>	$\frac{\Delta V_{OUT}}{\Delta I_{OUT}}$	10 mA ≤ I _{OUT} ≤ I _{MAX} , V _{OUT} ≤ 5 V	1,2,3	01		60	mV
		10 mA ≤ I _{OUT} ≤ I _{MAX} , V _{OUT} ≥ 5 V				1.2	%
Thermal regulation <u>3/</u>		I _{OUT} = 1.5 A, (V _{IN} - V _{OUT}) = 13.3 V, 20 ms pulse, 20 W, T _C = +25°C	1	01		0.07	%/W
Ripple rejection <u>3/</u>		V _{OUT} = 10 V, f = 120 Hz, C _{ADJ} = 10 μf	1,2,3	01	66		dB
Adjustment pin current <u>1/</u>	I _{ADJ}		1,2,3	01		100	μA
Adjustment pin current change <u>1/</u>	ΔI _{ADJ}	10 mA ≤ I _{OUT} ≤ I _{MAX} 3.0 V ≤ (V _{IN} - V _{OUT}) ≤ 40 V	1,2,3	01		5	μA
Minimum load current <u>1/ 3/</u>	I _{MIN}	(V _{IN} - V _{OUT}) = 40 V	1,2,3	01		5	mA
Current limit <u>1/</u>	I _{MAX}	(V _{IN} - V _{OUT}) ≤ 15 V, T _C = +25°C	1	01		1.5	A
		(V _{IN} - V _{OUT}) = 40 V, T _C = +25°C	1			0.30	
Long term stability <u>3/</u>	$\frac{\Delta V_{OUT}}{\Delta TIME}$	T _A = +125°C	2	01		1	%
Thermal resistance, <u>3/</u> junction-to-case	θ _{JC}		1,2,3	01		3	°C/W

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C ≤ T _C ≤ +125°C V _{IN} - V _{OUT} = 5 V, I _{OUT} = 0.5 A, P ≤ P _{MAX} unless otherwise specified	Group A subgroups	Device types	Limits		Unit
					Min	Max	
NEGATIVE REGULATOR							
Reference voltage <u>1/</u>	V _{REF}	-3 V ≤ (V _{IN} - V _{OUT}) ≤ V _{DIFF} Max, 10 mA ≤ I _{OUT} ≤ I _{MAX}	1,2,3	02	-1.200	-1.300	V
Line regulation <u>1/ 2/</u>	$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$	-3 V ≤ (V _{IN} - V _{OUT}) ≤ -30 V, I _{OUT} = 10 mA	1,2,3	02		0.05	%/V
Load regulation <u>1/ 2/</u>	$\frac{\Delta V_{OUT}}{\Delta I_{OUT}}$	10 mA ≤ I _{OUT} ≤ I _{MAX} , V _{OUT} ≤ 5 V	1,2,3	02		50	mV
		10 mA ≤ I _{OUT} ≤ I _{MAX} , V _{OUT} ≥ 5 V		02		1.0	%
Thermal regulation <u>3/</u>		I _{OUT} = 1.5 A, (V _{IN} - V _{OUT}) = -13.3 V, 20 ms pulse, 20 W, T _C = +25°C	1	02		0.02	%/W
Ripple rejection <u>3/</u>		V _{OUT} = -10 V, f = 120 Hz, C _{ADJ} = 10 μf	1,2,3	02	66		dB
Adjustment pin current <u>1/</u>	I _{ADJ}		1,2,3	02		100	μA
Adjustment pin current change <u>2/</u>	ΔI _{ADJ}	10 mA ≤ I _{OUT} ≤ I _{MAX} , -3 V ≤ (V _{IN} - V _{OUT}) ≤ -30 V	1,2,3	02		5	μA
Minimum load current <u>3/</u>	I _{MIN}	(V _{IN} - V _{OUT}) = -30 V	1,2,3	02		5	mA
		(V _{IN} - V _{OUT}) ≤ -10 V		02		3	
Current limit <u>1/</u>	I _{MAX}	(V _{IN} - V _{OUT}) ≤ -15 V,	1,2,3	02	1.5		A
		(V _{IN} - V _{OUT}) = -30 V, T _C = +25°C		1	02	0.24	
Long term stability <u>3/</u>	$\frac{\Delta V_{OUT}}{\Delta TIME}$	T _A = +125°C	2	02		1	%
Thermal resistance, <u>3/</u> junction-to-case	θ _{JC}		1,2,3	02		3	°C/W

See footnotes at top of next page.

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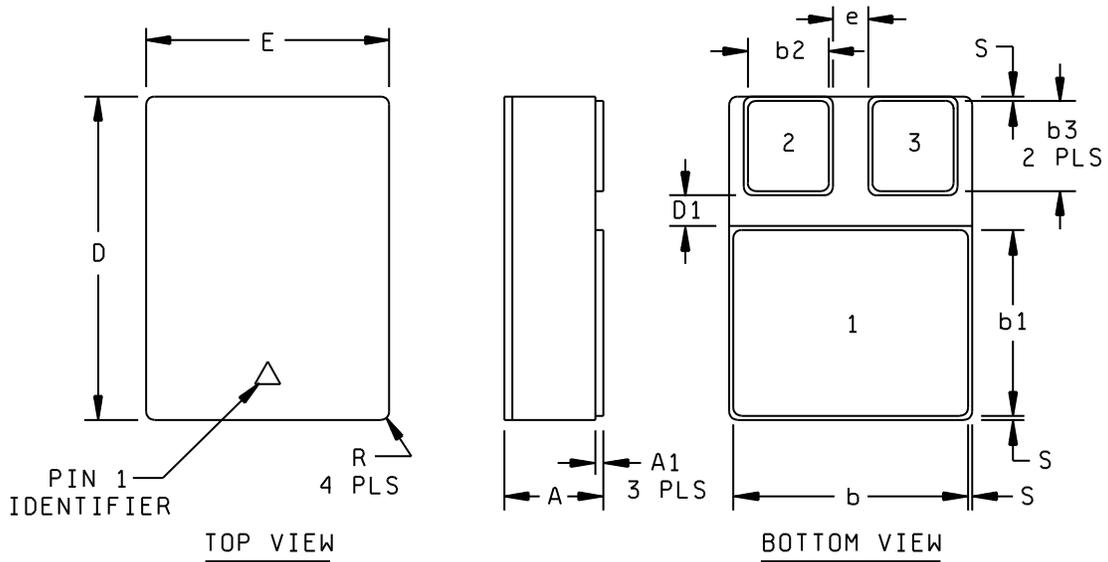
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TABLE I. Electrical performance characteristics - Continued.

- 1/ The active elements that make up these devices have been tested to the requirements of RHA designator level "R" (100 krads(Si)) of Method 1019, condition A of MIL-STD-883 and low dose rate tested to the requirements of Method 1019, condition D of MIL-STD-883 to 50 krads(Si) at +25°C for these parameters. No ELDRS effect was observed. The devices will be re-tested after design or process changes that can affect RHA response of these devices. RHA testing of the active elements covered on this SMD were done in alternate packages (TO3) and (TO39), not the packages as specified in paragraph 1.2.4.
- 2/ Regulation is measured at a constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specification for thermal regulation. Measurements taken at the output lead must be adjusted for lead resistance.
- 3/ Parameter shall be tested at initial device characterization and after design or process changes. Parameter shall be guaranteed to the limits specified in table I for all lots not specifically tested.

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Case outline X



Symbol	Inches		Millimeters	
	Min	Max	Min	Max
A		.127		3.23
A1	.010	.020	.25	.51
b	.281	.291	7.14	7.39
b1	.220	.230	5.59	5.84
b2	.090	.101	2.29	2.56
b3	.115	.125	2.92	3.18
D		.405		10.29
D1	.030		.76	
E		.301		7.65
e	.030		.76	
R	.015	.025	.38	.64
S		.010		.25

NOTES:

1. The U.S. preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. The package and lid are electrically isolated.

FIGURE 1. Case outline(s) - Continued.

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Device types	01	02
Case outline	X	
Terminal number	Terminal symbol	
1	Positive V_{OUT}	Negative V_{IN}
2	Positive V_{IN}	Negative V_{OUT}
3	Positive ADJ	Negative ADJ

FIGURE 2. Terminal connections.

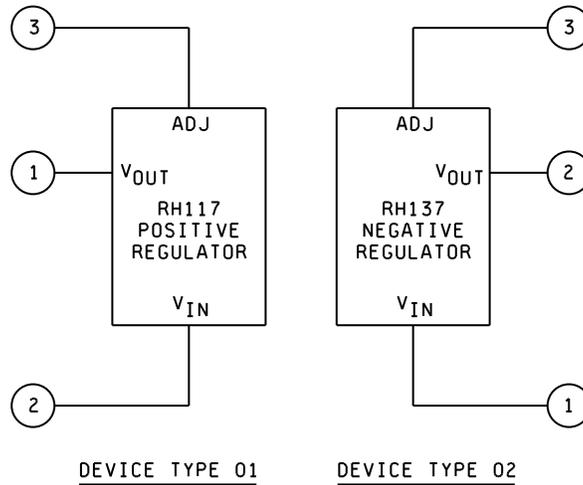


FIGURE 3. Block diagram.

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Case Temperature (°C)	Maximum power dissipation (Watts)
0	50.00
5	48.33
10	46.67
15	45.00
20	43.33
25	41.67
30	40.00
35	38.33
40	36.67
45	35.00
50	33.33
55	31.67
60	30.00
65	28.33
70	26.67
75	25.00
80	23.33
85	21.67
90	20.00
95	18.33
100	16.67
105	15.00
110	13.33
115	11.67
120	10.00
125	8.33
130	6.67
135	5.00
140	3.33
145	1.67
150	0.00

FIGURE 4. Maximum power dissipation verses case temperature chart.

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TABLE II. Electrical test requirements.

MIL-PRF-38534 test requirements	Subgroups (in accordance with MIL-PRF-38534, group A test table)
Interim electrical parameters	
Final electrical parameters	1*,2,3
Group A test requirements	1,2,3
Group C end-point electrical parameters	1,2,3
End-point electrical parameters for Radiation Hardness Assurance (RHA) devices	1

* PDA applies to subgroup 1.

4. VERIFICATION

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38534 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

4.2 Screening. Screening shall be in accordance with MIL-PRF-38534. The following additional criteria shall apply:

- a. Burn-in test, method 1015 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DLA Land and Maritime -VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.
 - (2) T_A as specified in accordance with table I of method 1015 of MIL-STD-883.
- b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Conformance and periodic inspections. Conformance inspection (CI) and periodic inspection (PI) shall be in accordance with MIL-PRF-38534 and as specified herein.

4.3.1 Group A inspection (CI). Group A inspection shall be in accordance with MIL-PRF-38534 and as follows:

- a. Tests shall be as specified in table II herein.
- b. Subgroups 4, 5, 6, 7, 8A, 8B, 9, 10, and 11 shall be omitted.

4.3.2 Group B inspection (PI). Group B inspection shall be in accordance with MIL-PRF-38534.

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4.3.3 Group C inspection (PI). Group C inspection shall be in accordance with MIL-PRF-38534 and as follows:

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test, method 1005 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DLA Land and Maritime -VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.
 - (2) T_A as specified in accordance with table I of method 1005 of MIL-STD-883.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

4.3.4 Group D inspection (PI). Group D inspection shall be in accordance with MIL-PRF-38534.

4.3.5. Radiation hardness assurance (RHA). RHA qualification is required only for those devices with the RHA designator as specified herein. See table IIIA and table IIIB.

Table IIIA. Radiation Hardness Assurance Method Table.

RHA method Employed	Testing at 2X rated total dose		Worst Case Analysis Performed No				End points after dose is achieved includes minimum maximum, and room temperatures	
	Element Level	Hybrid Device Level	Includes temperature effects	Combines temperature and radiation effects	Combines total dose and displacement effects	End-of-life	Element Level	Hybrid device level
	Yes	N/A	N/A	N/A	N/A	N/A	No	No

Table IIIB. Hybrid level and element level test table.

	Radiation Test								
	Total Dose			Heavy Ion		Proton		Neutron	
	Low Dose Rate	High Dose Rate (HDR)	ELDRS	SEU (upset)	SEL (latch-up)	Low Energy	High Energy	SEE (upset)	Displacement Damage (DD)
Hybrid	N/A	N/A	N/A	(N)	(N)	(N)	(N)	(N)	(N)
Element									
Bipolar, linear or mixed signal > 90 nm	X (50 krad)	X (100 krad)	X (50 krad)	(N)	(N)	(N)	(N)	(N)	(N)

NOTES:

- X = Radiation testing done (Level)
- G = Guaranteed by design or process
- (N) = Not yet tested
- N/A = Not applicable for this SMD

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4.3.5.1 Radiation Hardness Assurance (RHA) inspection. RHA qualification is required for those devices with the RHA designator as specified herein. End-point electrical parameters for radiation hardness assurance (RHA) devices shall be specified in table II. Radiation testing will be in accordance with the qualifying activity (DLA Land and Maritime -VQ) approved plan and with MIL-PRF-38534, Appendix G.

- a. The hybrid device manufacturer shall establish procedures controlling component radiation testing, and shall establish radiation test plans used to implement component lot qualification during procurement. Test plans and test reports shall be filed and controlled in accordance with the manufacturer's configuration management system.
- b. The hybrid device manufacturer shall designate a RHA program manager to oversee component lot qualification, and to monitor design changes for continued compliance to RHA requirements.

4.3.5.1.1 Hybrid level qualification.

4.3.5.1.1.2 Total ionizing dose irradiation testing. Hybrid level and component level testing are the same for the devices on this SMD since the active elements are independent of each other and accessible to the device leads for test. The qualification was performed on the active components, independent of the hybrid.

4.3.5.1.2 Component level qualification.

4.3.5.1.2.1 Total Ionizing dose irradiation testing. A minimum of twenty samples of each element is tested at initial qualification and after any design or process changes which may affect the RHA response of the device type. Five biased and five unbiased are tested at High Dose Rate (HDR) in accordance with condition A of method 1019 of MIL-STD-883 to 100 krad(Si). Another ten devices are tested at Low Dose Rate (LDR) in accordance with condition D of method 1019 of MIL-STD-883 to 50 krad(Si). The resulting data is evaluated in accordance with Condition D, ELDRS characterization. 0.9000/90% statistics are applied to the device parameter designations which are compared against established limits for acceptance.

4.3.5.2 RHA Lot Acceptance. Each lot of active elements shall be evaluated for acceptance in accordance with MIL-PRF-38534 and herein.

4.3.5.2.1 Total Ionizing Dose. Samples from every wafer lot will be assembled into a representative device type and tested for wafer lot acceptance RLAT (Radiation Lot Acceptance Testing). Four biased and four unbiased devices are tested in accordance with condition A, of method 1019 of MIL-STD-883 to 100 krad(Si). 0.9000/90% statistics are applied to the device parameter designations which are compared against established limits for acceptance.

4.3.5.2.2 Technologies not tested. All active components in these devices are RHA tested.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38534.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

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6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated as specified in MIL-PRF-38534.

6.4 Record of users. Military and industrial users should inform DLA Land and Maritime when a system application requires configuration control and the applicable SMD. DLA Land and Maritime will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DLA Land and Maritime-VA, telephone (614) 692-0544.

6.5 Comments. Comments on this drawing should be directed to DLA Land and Maritime-VA, Columbus, Ohio 43218-3990, or telephone (614) 692-1081.

6.6 Sources of supply. Sources of supply are listed in MIL-HDBK-103 and QML-38534. The vendors listed in MIL-HDBK-103 and QML-38534 have submitted a certificate of compliance (see 3.7 herein) to DLA Land and Maritime-VA and have agreed to this drawing.

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STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 16-04-11

Approved sources of supply for SMD 5962-09206 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38534 during the next revisions. MIL-HDBK-103 and QML-38534 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DLA Land and Maritime-VA. This information bulletin is superseded by the next dated revisions of MIL-HDBK-103 and QML-38534. DLA Land and Maritime maintains an online database of all current sources of supply at <http://www.landandmaritime.dla.mil/Programs/Smcr/>.

Standard microcircuit drawing PIN <u>1/</u>	Vendor CAGE number	Vendor similar PIN <u>2/</u>
5962-0920601KXA	88379	VRG8660-201-2S
5962R0920601KXA	88379	VRG8660-901-2S
5962-0920601KXC	88379	VRG8660-201-1S
5962R0920601KXC	88379	VRG8660-901-1S
5962-0920602KXA	88379	VRG8661-201-2S
5962R0920602KXA	88379	VRG8661-901-2S
5962-0920602KXC	88379	VRG8661-201-1S
5962R0920602KXC	88379	VRG8661-901-1S

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the Vendor to determine its availability.
- 2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number

88379

Vendor name and address

Aeroflex Plainview Incorporated,
 (Aeroflex Microelectronic Solutions)
 35 South Service Road
 Plainview, NY 11803-4193

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