<table>
<thead>
<tr>
<th>LTR</th>
<th>DESCRIPTION</th>
<th>DATE (YR-MO-DA)</th>
<th>APPROVED</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>Update drawing to the latest requirements. -sid</td>
<td>07-11-26</td>
<td>Robert M. Heber</td>
</tr>
<tr>
<td>B</td>
<td>Table II, add note to Group C end-point test parameters. Update boilerplate paragraphs. -gz</td>
<td>10-01-22</td>
<td>Charles F. Saffle</td>
</tr>
<tr>
<td>C</td>
<td>Corrected dimensions for symbols D, D2, and L1 for case outline U. Corrected dimensions for symbols D2, L, and L1 for case outline X. Corrected dimensions for symbols D, L, and L1 for case outline Y. Corrected dimensions for symbols D, D2, and L1 for case outline Z. Updated drawing paragraphs. -sid</td>
<td>13-03-05</td>
<td>Charles F. Saffle</td>
</tr>
</tbody>
</table>
1. SCOPE

1.1 Scope. This drawing documents five product assurance classes as defined in paragraph 1.2.3 and MIL-PRF-38534. A choice of case outlines and lead finishes which are available and are reflected in the Part or Identifying Number (PIN). When available, a choice of radiation hardness assurance levels are reflected in the PIN.

1.2 PIN. The PIN shall be as shown in the following example:

```
5962 - 02565 01 H X X
```

Federal stock class RHA Device designator (see 1.2.1) RHA designator (see 1.2.2) Device type (see 1.2.3)

Risk assurance Level designator (see 1.2.4) Device class designator (see 1.2.5) Case outline (see 1.2.6) Lead finish

1.2.1 Radiation hardness assurance (RHA) designator. RHA marked devices shall meet the MIL-PRF-38534 specified RHA levels and shall be marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.

1.2.2 Device type(s). The device type(s) identify the circuit function as follows:

<table>
<thead>
<tr>
<th>Device type</th>
<th>Generic number</th>
<th>Circuit function</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>AFL5015D/CH</td>
<td>DC/DC converter, 100 W, ±15 V outputs</td>
</tr>
</tbody>
</table>

1.2.3 Device class designator. This device class designator shall be a single letter identifying the product assurance level. All levels are defined by the requirements of MIL-PRF-38534 and require QML Certification as well as qualification (Class H, K, and E) or QML Listing (Class G and D). The product assurance levels are as follows:

<table>
<thead>
<tr>
<th>Device class</th>
<th>Device performance documentation</th>
</tr>
</thead>
<tbody>
<tr>
<td>K</td>
<td>Highest reliability class available. This level is intended for use in space applications.</td>
</tr>
<tr>
<td>H</td>
<td>Standard military quality class level. This level is intended for use in applications where non-space high reliability devices are required.</td>
</tr>
<tr>
<td>G</td>
<td>Reduced testing version of the standard military quality class. This level uses the Class H screening and In-Process Inspections with a possible limited temperature range, manufacturer specified incoming flow, and the manufacturer guarantees (but may not test) periodic and conformance inspections (Group A, B, C, and D).</td>
</tr>
<tr>
<td>E</td>
<td>Designates devices which are based upon one of the other classes (K, H, or G) with exception(s) taken to the requirements of that class. These exception(s) must be specified in the device acquisition document; therefore the acquisition document should be reviewed to ensure that the exception(s) taken will not adversely affect system performance.</td>
</tr>
<tr>
<td>D</td>
<td>Manufacturer specified quality class. Quality level is defined by the manufacturers internal, QML certified flow. This product may have a limited temperature range.</td>
</tr>
</tbody>
</table>

1.2.4 Case outlines. The case outlines are as designated in MIL-STD-1835 and as follows:

<table>
<thead>
<tr>
<th>Outline letter</th>
<th>Descriptive designator</th>
<th>Terminals</th>
<th>Package style</th>
</tr>
</thead>
<tbody>
<tr>
<td>U</td>
<td>See figure 1</td>
<td>12</td>
<td>Y case configuration with leads bent upwards</td>
</tr>
<tr>
<td>X</td>
<td>See figure 1</td>
<td>12</td>
<td>Straight leads with end mounting through holes</td>
</tr>
<tr>
<td>Y</td>
<td>See figure 1</td>
<td>12</td>
<td>Straight leads with side mounting through holes</td>
</tr>
<tr>
<td>Z</td>
<td>See figure 1</td>
<td>12</td>
<td>Y case configuration with leads bent downwards</td>
</tr>
</tbody>
</table>

1.2.5 Lead finish. The lead finish shall be as specified in MIL-PRF-38534.
1.3 Absolute maximum ratings. 1/

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Voltage/Temp</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input voltage range</td>
<td>-0.5 V dc to +100 V dc</td>
</tr>
<tr>
<td>Lead temperature (soldering, 10 seconds)</td>
<td>+300°C</td>
</tr>
<tr>
<td>Storage temperature</td>
<td>-65°C to +135°C</td>
</tr>
</tbody>
</table>

1.4 Recommended operating conditions.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Voltage/Temp</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input voltage range</td>
<td>+30 V dc to +80 V dc</td>
</tr>
<tr>
<td>Output power 2/ 3/</td>
<td>≤ 100 W</td>
</tr>
<tr>
<td>Case operating temperature range ((T_c))</td>
<td>-55°C to +125°C</td>
</tr>
</tbody>
</table>

2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATIONS


DEPARTMENT OF DEFENSE STANDARDS


DEPARTMENT OF DEFENSE HANDBOOKS

MIL-HDBK-103 - List of Standard Microcircuit Drawings.
MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at https://assist.dla.mil/quicksearch/ or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Item requirements. The individual item performance requirements for device classes D, E, G, H, and K shall be in accordance with MIL-PRF-38534. Compliance with MIL-PRF-38534 shall include the performance of all tests herein or as designated in the device manufacturer's Quality Management (QM) plan or as designated for the applicable device class. The manufacturer may eliminate, modify or optimize the tests and inspections herein, however the performance requirements as defined in MIL-PRF-38534 shall be met for the applicable device class. In addition, the modification in the QM plan shall not affect the form, fit, or function of the device for the applicable device class.

1/ Stresses above the absolute maximum ratings may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.
2/ Derate output power linearly above case temperature +125°C to 0 at +135°C.
3/ Output load must be distributed so that a minimum of 20% of the total output power is being provided by one of the outputs.
3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38534 and herein.

3.2.1 Case outlines. The case outlines shall be in accordance with 1.2.4 herein and figure 1.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 2.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full specified operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are defined in table I.

3.5 Marking of devices. Marking of devices shall be in accordance with MIL-PRF-38534. The device shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's vendor similar PIN may also be marked.

3.6 Data. In addition to the general performance requirements of MIL-PRF-38534, the manufacturer of the device described herein shall maintain the electrical test data (variables format) from the initial quality conformance inspection group A lot sample, for each device type listed herein. Also, the data should include a summary of all parameters manually tested, and for those which, if any, are guaranteed. This data shall be maintained under document revision level control by the manufacturer and be made available to the preparing activity (DLA Land and Maritime - VA) upon request.

3.7 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to supply to this drawing. The certificate of compliance (original copy) submitted to DLA Land and Maritime - VA shall affirm that the manufacturer's product meets the performance requirements of MIL-PRF-38534 and herein.

3.8 Certificate of conformance. A certificate of conformance as required in MIL-PRF-38534 shall be provided with each lot of microcircuits delivered to this drawing.

4. VERIFICATION

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38534 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

4.2 Screening. Screening shall be in accordance with MIL-PRF-38534. The following additional criteria shall apply:


      (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DLA Land and Maritime - VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.

      (2) \( T_A \) as specified in accordance with table I of method 1015 of MIL-STD-883.

   b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.
### TABLE I. Electrical performance characteristics.

<table>
<thead>
<tr>
<th>Test</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Group A subgroups</th>
<th>Device type</th>
<th>Limits</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Output voltage</td>
<td>V\text{OUT}</td>
<td>I\text{OUT} = 3.33 A, each output</td>
<td>1, 2, 3</td>
<td>01</td>
<td>±14.85</td>
<td>±15.15 V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>±14.70</td>
<td></td>
<td></td>
<td>±15.30</td>
<td></td>
</tr>
<tr>
<td>Output current 1/ 2/</td>
<td>I\text{OUT}</td>
<td>V\text{IN} = 30, 50, and 80 V dc, either output</td>
<td>1, 2, 3</td>
<td>01</td>
<td>5.3</td>
<td>A</td>
</tr>
<tr>
<td>Output ripple voltage</td>
<td>V\text{RIP}</td>
<td>V\text{IN} = 30, 50, and 80 V dc, B.W. = 10 MHz, I\text{OUT} = 3.33 A, both outputs</td>
<td>1, 2, 3</td>
<td>01</td>
<td>60</td>
<td>mV p-p</td>
</tr>
<tr>
<td>Line regulation 3/</td>
<td>V\text{RLINE}</td>
<td>V\text{IN} = 30, 50, and 80 V dc, I\text{OUT} = 0, 1.67, and 3.33 A, both outputs</td>
<td>1, 2, 3</td>
<td>01</td>
<td>±75</td>
<td>mV</td>
</tr>
<tr>
<td>Load regulation 3/</td>
<td>V\text{RLoad}</td>
<td>V\text{IN} = 30, 50, and 80 V dc, I\text{OUT} = 0, 1.67, and 3.33 A, both outputs</td>
<td>1, 2, 3</td>
<td>01</td>
<td>±150</td>
<td>mV</td>
</tr>
<tr>
<td>Cross regulation 4/</td>
<td>V\text{RCROSS}</td>
<td>V\text{IN} = 30, 50, and 80 V dc Positive output</td>
<td>1, 2, 3</td>
<td>01</td>
<td>±150</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Negative output</td>
<td></td>
<td></td>
<td>±750</td>
<td></td>
</tr>
<tr>
<td>Input current</td>
<td>I\text{IN}</td>
<td>I\text{OUT} = no load</td>
<td>1, 2, 3</td>
<td>01</td>
<td>50</td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>60</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>Enable 1, (pin 4) shorted to Input return (pin 2)</td>
<td>1, 2, 3</td>
<td>01</td>
<td>5</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>Enable 2, (pin 12) shorted to Output return (pin 8)</td>
<td>1, 2, 3</td>
<td>01</td>
<td>5</td>
<td></td>
</tr>
<tr>
<td>Input ripple current</td>
<td>I\text{RIP}</td>
<td>I\text{OUT} = 3.33 A, B.W. = 10 MHz</td>
<td>1, 2, 3</td>
<td>01</td>
<td>60</td>
<td>mA p-p</td>
</tr>
<tr>
<td>Efficiency</td>
<td>E\text{FF}</td>
<td>I\text{OUT} = 3.33 A</td>
<td>1, 2, 3</td>
<td>01</td>
<td>81</td>
<td>%</td>
</tr>
<tr>
<td>Isolation</td>
<td>ISO</td>
<td>Input to output or any pin to case (except pin 3) at 500 V dc, T\text{C} = +25°C</td>
<td>1</td>
<td>01</td>
<td>100</td>
<td>MΩ</td>
</tr>
<tr>
<td>Maximum capacitive load 5/</td>
<td>C\text{L}</td>
<td>No effect on dc performance, each output, T\text{C} = +25°C</td>
<td>4</td>
<td>01</td>
<td>10,000</td>
<td>µF</td>
</tr>
<tr>
<td>Power dissipation load fault</td>
<td>P\text{D}</td>
<td>Overload</td>
<td>1, 2, 3</td>
<td>01</td>
<td>32</td>
<td>W</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Short circuit</td>
<td></td>
<td></td>
<td>32</td>
<td></td>
</tr>
</tbody>
</table>

See footnotes at end of table.
## TABLE I. Electrical performance characteristics - Continued.

<table>
<thead>
<tr>
<th>Test</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Group A subgroup(s)</th>
<th>Device type</th>
<th>Limits Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Current limit point 6/</td>
<td>I&lt;sub&gt;CL&lt;/sub&gt;</td>
<td>V&lt;sub&gt;OUT&lt;/sub&gt; = 90% V&lt;sub&gt;NOM&lt;/sub&gt;, each output</td>
<td>1, 2, 3</td>
<td>01</td>
<td>3.83 - 4.16 A</td>
</tr>
<tr>
<td>Switching frequency</td>
<td>F&lt;sub&gt;S&lt;/sub&gt;</td>
<td>I&lt;sub&gt;OUT&lt;/sub&gt; = 3.33 A</td>
<td>1, 2, 3</td>
<td>01</td>
<td>500 - 600 kHz</td>
</tr>
<tr>
<td>Sync frequency range</td>
<td>F&lt;sub&gt;SYNC&lt;/sub&gt;</td>
<td>I&lt;sub&gt;OUT&lt;/sub&gt; = 3.33 A</td>
<td>1, 2, 3</td>
<td>01</td>
<td>500 - 700 kHz</td>
</tr>
<tr>
<td>Output response to step transient load changes 7/ 8/</td>
<td>V&lt;sub&gt;OLOAD&lt;/sub&gt;</td>
<td>50% to/from 100%</td>
<td>4, 5, 6</td>
<td>01</td>
<td>-750 - 750 mV pk</td>
</tr>
<tr>
<td>Recovery time, step transient load changes 7/ 8/</td>
<td>T&lt;sub&gt;TLOAD&lt;/sub&gt;</td>
<td>50% to/from 100%</td>
<td>4, 5, 6</td>
<td>01</td>
<td>200 - 400 µs</td>
</tr>
<tr>
<td>Output response to transient step line changes 5/ 7/ 9/</td>
<td>V&lt;sub&gt;OTLINE&lt;/sub&gt;</td>
<td>Input step, 30 V to/from 80 V dc, I&lt;sub&gt;OUT&lt;/sub&gt; = 3.33 A</td>
<td>4, 5, 6</td>
<td>01</td>
<td>-500 - 500 mV pk</td>
</tr>
<tr>
<td>Recovery time transient step line changes 5/ 7/ 9/</td>
<td>T&lt;sub&gt;TLINE&lt;/sub&gt;</td>
<td>Input step, 30 V to/from 80 V dc, I&lt;sub&gt;OUT&lt;/sub&gt; = 3.33 A</td>
<td>4, 5, 6</td>
<td>01</td>
<td>500 - 600 µs</td>
</tr>
<tr>
<td>Turn on overshoot 10/</td>
<td>V&lt;sub&gt;TonOS&lt;/sub&gt;</td>
<td>I&lt;sub&gt;OUT&lt;/sub&gt; = 0 and 3.33 A</td>
<td>4, 5, 6</td>
<td>01</td>
<td>250 - 300 mV pk</td>
</tr>
<tr>
<td>Turn on delay 10/</td>
<td>T&lt;sub&gt;TonD&lt;/sub&gt;</td>
<td>I&lt;sub&gt;OUT&lt;/sub&gt; = 0 and 3.33 A</td>
<td>4, 5, 6</td>
<td>01</td>
<td>120 - 200 ms</td>
</tr>
<tr>
<td>Load fault recovery 10/</td>
<td>T&lt;sub&gt;LF&lt;/sub&gt;</td>
<td></td>
<td>4, 5, 6</td>
<td>01</td>
<td>120 - 200 ms</td>
</tr>
</tbody>
</table>

1/ Parameter verified as part of another test.
2/ Output load must be distributed so that a minimum of 20% of the total output power is being provided by one of the outputs.
3/ Load current split equally between +V<sub>OUT</sub> and -V<sub>OUT</sub>.
4/ Cross regulation measured with load on tested output at 20% while changing the load on other output from 20% to 80%.
5/ Parameter not 100% tested but are guaranteed to the limits specified in the table.
6/ Current limit point is that condition of excess load causing output voltage to drop to 90% of nominal.
7/ Recovery time is measured from the initiation of the transient to where V<sub>OUT</sub> has returned to within ±1% of V<sub>OUT</sub> at 50% load.
8/ Load transient transition time ≥ 10 µs.
9/ Line transient transition time ≥ 100 µs.
10/ Turn-on delay is measured with an input voltage rise time of between 100 and 500 volts per millisecond.
### TABLE 1. Case outlines (inches)

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Min</th>
<th>Max</th>
<th>Min</th>
<th>Max</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>9.65</td>
<td>.380</td>
<td></td>
<td></td>
</tr>
<tr>
<td>D</td>
<td>70.35</td>
<td>71.96</td>
<td>2.770</td>
<td>2.833</td>
</tr>
<tr>
<td>D1</td>
<td>63.37</td>
<td>63.63</td>
<td>2.495</td>
<td>2.505</td>
</tr>
<tr>
<td>D2</td>
<td>13.08</td>
<td>13.59</td>
<td>.515</td>
<td>.535</td>
</tr>
<tr>
<td>E</td>
<td>37.97</td>
<td>38.23</td>
<td>1.495</td>
<td>1.505</td>
</tr>
<tr>
<td>e1</td>
<td>50.6</td>
<td>51.1</td>
<td>1.99</td>
<td>2.01</td>
</tr>
<tr>
<td>e2</td>
<td>25.27</td>
<td>25.53</td>
<td>.995</td>
<td>1.005</td>
</tr>
<tr>
<td>F</td>
<td>1.14</td>
<td>1.40</td>
<td>.045</td>
<td>.055</td>
</tr>
<tr>
<td>L</td>
<td>10.4</td>
<td>10.9</td>
<td>.41</td>
<td>.43</td>
</tr>
<tr>
<td>L1</td>
<td>5.33</td>
<td>5.84</td>
<td>.210</td>
<td>.230</td>
</tr>
<tr>
<td>q</td>
<td>44.32</td>
<td>44.58</td>
<td>1.745</td>
<td>1.755</td>
</tr>
<tr>
<td>q1</td>
<td>29.08</td>
<td>29.34</td>
<td>1.145</td>
<td>1.155</td>
</tr>
<tr>
<td>q2</td>
<td>44.32</td>
<td>44.58</td>
<td>1.745</td>
<td>1.755</td>
</tr>
<tr>
<td>q3</td>
<td>6.1</td>
<td>6.6</td>
<td>.24</td>
<td>.26</td>
</tr>
<tr>
<td>q4</td>
<td>7.49</td>
<td>7.75</td>
<td>.295</td>
<td>.305</td>
</tr>
<tr>
<td>q5</td>
<td>9.40</td>
<td>9.65</td>
<td>.370</td>
<td>.380</td>
</tr>
<tr>
<td>S</td>
<td>6.22</td>
<td>6.48</td>
<td>.245</td>
<td>.255</td>
</tr>
<tr>
<td>∅b</td>
<td>0.89</td>
<td>1.14</td>
<td>.035</td>
<td>.045</td>
</tr>
<tr>
<td>∅p</td>
<td>3.43</td>
<td>3.68</td>
<td>.135</td>
<td>.145</td>
</tr>
</tbody>
</table>

### NOTES:
1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. Lead identification for reference only.
3. Case outline weight: 92 grams maximum.

**FIGURE 1.** Case outlines.
I

Di----_

Li--l

Case outline X.

[Diagram of case outline]

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Min (Millimeters)</th>
<th>Max (Millimeters)</th>
<th>Min (Inches)</th>
<th>Max (Inches)</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>9.65</td>
<td>.380</td>
<td>.95</td>
<td>.380</td>
</tr>
<tr>
<td>D</td>
<td>76.07</td>
<td>76.33</td>
<td>3.005</td>
<td>3.005</td>
</tr>
<tr>
<td>D1</td>
<td>63.37</td>
<td>63.63</td>
<td>2.505</td>
<td>2.505</td>
</tr>
<tr>
<td>D2</td>
<td></td>
<td>76.23</td>
<td>3.001</td>
<td></td>
</tr>
<tr>
<td>E</td>
<td>37.97</td>
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</tbody>
</table>

NOTES:
1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. Lead identification for reference only.
3. Case outline weight: 92 grams maximum.

FIGURE 1. Case outlines - Continued.
Case outline Y.

NOTES:
1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. Lead identification for reference only.
3. Case outline weight: 92 grams maximum.

FIGURE 1. Case outlines - Continued.
NOTES:
1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. Lead identification for reference only.
3. Case outline weight: 92 grams maximum.

FIGURE 1. Case outlines - Continued.
<table>
<thead>
<tr>
<th>Device type</th>
<th>01</th>
</tr>
</thead>
<tbody>
<tr>
<td>Case outlines</td>
<td>U, X, Y, and Z</td>
</tr>
<tr>
<td>Terminal number</td>
<td>Terminal symbol</td>
</tr>
<tr>
<td>1</td>
<td>Positive input</td>
</tr>
<tr>
<td>2</td>
<td>Input return</td>
</tr>
<tr>
<td>3</td>
<td>Case</td>
</tr>
<tr>
<td>4</td>
<td>Enable 1</td>
</tr>
<tr>
<td>5</td>
<td>Sync output</td>
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<tr>
<td>6</td>
<td>Sync input</td>
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<td>7</td>
<td>Positive output</td>
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<td>8</td>
<td>Output return</td>
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<tr>
<td>9</td>
<td>Negative output</td>
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<tr>
<td>10</td>
<td>Output voltage trim</td>
</tr>
<tr>
<td>11</td>
<td>Share</td>
</tr>
<tr>
<td>12</td>
<td>Enable 2</td>
</tr>
</tbody>
</table>

**FIGURE 2. Terminal connections.**
TABLE II. Electrical test requirements.

<table>
<thead>
<tr>
<th>MIL-PRF-38534 test requirements</th>
<th>Subgroups (in accordance with MIL-PRF-38534, group A test table)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Interim electrical parameters</td>
<td>---</td>
</tr>
<tr>
<td>Final electrical parameters</td>
<td>1*, 2, 3, 4, 5, 6</td>
</tr>
<tr>
<td>Group A test requirements</td>
<td>1, 2, 3, 4, 5, 6</td>
</tr>
<tr>
<td>Group C end-point electrical parameters</td>
<td>1/</td>
</tr>
<tr>
<td>End-point electrical parameters for radiation hardness assurance (RHA) devices</td>
<td>Not applicable</td>
</tr>
</tbody>
</table>

1/ As a minimum, for all Group C testing performed after January 22, 2010 manufacturers shall perform subgroups 1, 2, and 3 from the Group A electrical test table (Table C-Xa of MIL-PRF-38534).

* PDA applies to subgroup 1.

4.3 Conformance and periodic inspections. Conformance inspection (CI) and periodic inspection (PI) shall be in accordance with MIL-PRF-38534 and as specified herein.

4.3.1 Group A inspection (CI). Group A inspection shall be in accordance with MIL-PRF-38534 and as follows:
   a. Tests shall be as specified in table II herein.
   b. Subgroups 7, 8, 9, 10, and 11 shall be omitted.

4.3.2 Group B inspection (PI). Group B inspection shall be in accordance with MIL-PRF-38534.

4.3.3 Group C inspection (PI). Group C inspection shall be in accordance with MIL-PRF-38534 and as follows:
   a. End-point electrical parameters shall be as specified in table II herein.
      (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DLA Land and Maritime -VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.
      (2) $T_A$ as specified in accordance with table I of method 1005 of MIL-STD-883.
      (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

4.3.4 Group D inspection (PI). Group D inspection shall be in accordance with MIL-PRF-38534.

4.3.5 Radiation Hardness Assurance (RHA) inspection. RHA inspection is not currently applicable to this drawing.
5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38534.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated as specified in MIL-PRF-38534.

6.4 Record of users. Military and industrial users shall inform DLA Land and Maritime when a system application requires configuration control and the applicable SMD to that system. DLA Land and Maritime will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DLA Land and Maritime-VA, telephone (614) 692-8108.

6.5 Comments. Comments on this drawing should be directed to DLA Land and Maritime-VA, Columbus, Ohio 43218-3990, or telephone (614) 692-1081.

6.6 Sources of supply. Sources of supply are listed in MIL-HDBK-103 and QML-38534. The vendors, listed in MIL-HDBK-103 and QML-38534, have submitted a certificate of compliance (see 3.7 herein) to DLA Land and Maritime-VA and have agreed to this drawing.
Approved sources of supply for SMD 5962-02565 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38534 during the next revisions. MIL-HDBK-103 and QML-38534 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DLA Land and Maritime -VA. This information bulletin is superseded by the next dated revisions of MIL-HDBK-103 and QML-38534. DLA Land and Maritime maintains an online database of all current sources of supply at [http://www.landandmaritime.dla.mil/Programs/Smcr/](http://www.landandmaritime.dla.mil/Programs/Smcr/).

<table>
<thead>
<tr>
<th>Standard microcircuit drawing PIN 1/</th>
<th>Vendor CAGE number</th>
<th>Vendor similar PIN 2/</th>
</tr>
</thead>
<tbody>
<tr>
<td>5962-0256501HUA 5962-0256501HUC</td>
<td>52467</td>
<td>AFL5015DW/CH</td>
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<tr>
<td>5962-0256501HXA 5962-0256501HXC</td>
<td>52467</td>
<td>AFL5015DX/CH</td>
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<tr>
<td>5962-0256501HYA 5962-0256501HYC</td>
<td>52467</td>
<td>AFL5015DY/CH</td>
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<tr>
<td>5962-0256501HZA 5962-0256501HZC</td>
<td>52467</td>
<td>AFL5015DZ/CH</td>
</tr>
</tbody>
</table>

1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the Vendor to determine its availability.

2/ **Caution.** Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

<table>
<thead>
<tr>
<th>Vendor CAGE number</th>
<th>Vendor name and address</th>
</tr>
</thead>
</table>
| 52467              | International Rectifier Corporation  
2520 Junction Ave.  
San Jose, CA 95134 |

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