

1.3 Absolute maximum ratings. 1/ 2/

Supply voltage range (V_{DD})	6 V
Differential input voltage (V_{ID})	$V_{DD} - 0.2$ V to $V_{DD} + 0.2$ V
Input current, (I_{IN}) (any input)	± 200 mA
Output current, (I_{OUT})	± 175 mA
Total input current, (I_I) (into $+V_{DD}$)	175 mA
Total output current, (I_O) (out of GND)	175 mA
Continuous total power dissipation (P_D):	
Cases C and 2	1375 mW
Case H	675 mW
Case P	1050 mW
Operating free-air temperature range (T_A)	-55°C to $+125^\circ\text{C}$
Storage temperature range	-65°C to $+150^\circ\text{C}$
Lead temperature 1.6 mm (1/16 inch) from case for 10 seconds	$+260^\circ\text{C}$
Maximum junction temperature (T_J)	$+150^\circ\text{C}$
Thermal resistance, junction-to-case (θ_{JC}):	
Case C	12.9°C/W
Case H	11.3°C/W
Case P	12.9°C/W
Case 2	12.1°C/W
Thermal resistance, junction-to-ambient (θ_{JA}):	
Case C	83°C/W
Case H	197°C/W
Case P	122°C/W
Case 2	83°C/W

1.4 Recommended operating conditions.

Single supply voltage range (V_{DD})	$+2.7$ V to $+6$ V
Split supply voltage range ($\pm V_{DD}$)	± 1.35 V to ± 3 V
Common-mode input voltage (V_{ICR})	GND to $+V_{DD}$
Ambient operating temperature (T_A)	-55°C to $+125^\circ\text{C}$

1/ Stresses above the absolute maximum rating may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.
 2/ All voltage values, except differential voltages, are with respect to GND.

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2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATION

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard Microcircuits.
MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

DEPARTMENT OF DEFENSE HANDBOOKS

MIL-HDBK-103 - List of Standard Microcircuit Drawings.
MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at <https://assist.dla.mil/quicksearch/> or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements for device classes Q and V shall be in accordance with MIL-PRF-38535 as specified herein, or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein for device classes Q and V.

3.2.1 Case outlines. The case outlines shall be in accordance with 1.2.4 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.3 Electrical performance characteristics and postirradiation parameter limits. Unless otherwise specified herein, the electrical performance characteristics and postirradiation parameter limits are as specified in table I and shall apply over the full ambient operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are defined in table I.

3.5 Marking. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked. For packages where marking of the entire SMD PIN number is not feasible due to space limitations, the manufacturer has the option of not marking the "5962-" on the device. For RHA product using this option, the RHA designator shall still be marked. Marking for device classes Q and V shall be in accordance with MIL-PRF-38535.

3.5.1 Certification/compliance mark. The certification mark for device classes Q and V shall be a "QML" or "Q" as required in MIL-PRF-38535.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits 1/		Unit
					Min	Max	
Input offset voltage	V _{IO}	V _{DD} = ±1.5 V, V _{IC} = 0 V, V _{OUT} = 0 V, R _S = 50 Ω	1	01,03,		2000	μV
			2,3	05,07		2200	
			1	02,04,		1500	
			2,3	06,08		1700	
			1	01,03,		2000	
			2,3	05,07		2200	
			1	02,04,		1500	
			2,3	06,08		1700	
		V _{DD} = ±2.5 V, V _{IC} = 0 V, V _{OUT} = 0 V, R _S = 50 Ω	1	01,03,		2000	
			2,3	05,07		2200	
			1	02,04,		1500	
			2,3	06,08		1700	
			1	01,03,		2000	
			2,3	05,07		2200	
			1	02,04,		1500	
			2,3	06,08		1700	
V _{DD} = 3 V, Gnd, V _{IC} = 0 V, V _{OUT} = 1.5 V, R _S = 50 Ω	1	01,03,		2000			
	2,3	05,07		2200			
	1	02,04,		1500			
	2,3	06,08		1700			
	1	01,03,		2000			
	2,3	05,07		2200			
	1	02,04,		1500			
	2,3	06,08		1700			
V _{DD} = 5 V, Gnd, V _{IC} = 0 V, V _{OUT} = 2.5 V, R _S = 50 Ω	1	01,03,		2000			
	2,3	05,07		2200			
	1	02,04,		1500			
	2,3	06,08		1700			
	1	01,03,		2000			
	2,3	05,07		2200			
	1	02,04,		1500			
	2,3	06,08		1700			
Input offset current	I _{IO}	V _{DD} = ±1.5 V, R _S = 50 Ω, V _{IC} = 0 V, V _{OUT} = 0 V,	1	All		7	nA
			2,3			75	
		V _{DD} = ±2.5 V, R _S = 50 Ω, V _{IC} = 0 V, V _{OUT} = 0 V,	1			7	
			2,3			60	
Input bias current	I _{IB}	V _{DD} = ±1.5 V, R _S = 50 Ω, V _{IC} = 0 V, V _{OUT} = 0 V,	1	All		14	nA
			2,3			75	
		V _{DD} = ±2.5 V, R _S = 50 Ω, V _{IC} = 0 V, V _{OUT} = 0 V,	1			14	
			2,3			60	

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits 1/		Unit
					Min	Max	
Common-mode input voltage range	V _{ICR}	CMRR > 66 dB, R _S = 50 Ω, V _{DD} = 3 V	1	All	-0.2 to 3.2		V
		CMRR > 60 dB, R _S = 50 Ω, V _{DD} = 3 V	2,3		0 to 3.0		
		CMRR > 71 dB, R _S = 50 Ω, V _{DD} = 5 V	1		-0.2 to 5.2		
		CMRR > 60 dB, R _S = 50 Ω, V _{DD} = 5 V	2,3		0 to 5		
High-level output voltage	V _{OH}	I _{OH} = -2.5 mA, V _{DD} = 3 V	1,2,3	All	2.8		V
		I _{OH} = -10 mA, V _{DD} = 3 V			2.5		
		I _{OH} = -2.5 mA, V _{DD} = 5 V			4.8		
		I _{OH} = -10 mA, V _{DD} = 5 V			4.7		
Low output voltage	V _{OL}	I _{OL} = 2.5 mA, V _{IC} = 1.5 V	1,2,3	All		0.2	V
		I _{OL} = 10 mA, V _{IC} = 1.5 V				0.5	
		I _{OL} = 2.5 mA, V _{IC} = 2.5 V				0.2	
		I _{OL} = 10 mA, V _{IC} = 2.5 V				0.3	
Short-circuit output current	I _{OS}	Sourcing, V _{DD} = ±1.5 V	1,2,3	All	20		mA
		Sinking, V _{DD} = ±1.5 V			20		
		Sourcing, V _{DD} = ±2.5 V			60		
		Sinking, V _{DD} = ±2.5 V			60		
Large signal differential voltage amplification	A _{VD}	V _{DD} = ±1.5 V, R _L = 10 kΩ	1	All	90		dB
			2,3		89		
		V _{DD} = ±2.5 V, R _L = 10 kΩ	1		92		
			2,3		90		

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits ^{1/}		Unit
					Min	Max	
Common-mode rejection ratio	CMRR	V _{ICR} = 0 V to 3 V, R _S = 50 Ω, V _{DD} = 3 V	1	All	66		dB
			2,3		60		
		V _{ICR} = 0 V to 5 V, R _S = 50 Ω, V _{DD} = 5 V	1		71		
			2,3		60		
Supply voltage rejection ratio (ΔV _{DD} / ΔV _{IO})	k _{SVR}	V _{IC} = V _{DD} / 2, no load, V _{DD} = 2.7 V to 6.0 V	1	All	80		dB
			2,3		75		
		V _{IC} = V _{DD} / 2, no load, V _{DD} = 3.0 V to 5.0 V	1		85		
			2,3		80		
Supply current per channel	I _{DD}	V _{OUT} = 1.5 V, no load, V _{DD} = 3 V	1	All		0.575	mA
			2,3			0.9	
		V _{OUT} = 2.5 V, no load, V _{DD} = 5 V	1			0.65	
			2,3			1	
Supply current per channel in shutdown (For devices with shutdown feature)	I _{DD} (SHDN)	SHDN < 0.8 V, V _{DD} = 3 V	1,2,3	01,02, 07,08		2.5	μA
		SHDN < 1.3 V, V _{DD} = 5 V				3	
Slew rate	SR	V _{OUT} (PP) = 2 V, C _L = 160 pF, R _L = 10 kΩ	4	All	1		V/μs
			5, 6		0.8		

^{1/} All characteristics are measured with zero common-mode input voltages unless otherwise noted.

3.6 Certificate of compliance. For device classes Q and V, a certificate of compliance shall be required from a QML-38535 listed manufacturer in order to supply to the requirements of this drawing (see 6.6.1 herein). The certificate of compliance submitted to DLA Land and Maritime-VA prior to listing as an approved source of supply for this drawing shall affirm that the manufacturer's product meets, for device classes Q and V, the requirements of MIL-PRF-38535 and herein.

3.7 Certificate of conformance. A certificate of conformance as required for device classes Q and V in MIL-PRF-38535 shall be provided with each lot of microcircuits delivered to this drawing.

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Device types	01 and 02 (with shutdown)		
Case outlines	H	P	2
Terminal number	Terminal symbol		
1	NC	NC	NC
2	NC	-INPUT 1	NC
3	-INPUT 1	+INPUT 1	NC
4	+INPUT 1	-V _{DD} / GND	NC
5	-V _{DD} / GND	NC	-INPUT 1
6	NC	OUTPUT	NC
7	OUTPUT	+V _{DD}	+INPUT 1
8	+V _{DD}	SHDN	NC
9	SHDN	---	NC
10	NC	---	-V _{DD} / GND
11	---	---	NC
12	---	---	NC
13	---	---	NC
14	---	---	NC
15	---	---	OUTPUT
16	---	---	NC
17	---	---	+V _{DD}
18	---	---	NC
19	---	---	NC
20	---	---	SHDN

NC = No connect

FIGURE 1. Terminal connections.

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Device types	03 and 04 (without shutdown)		
Case outlines	H	P	2
Terminal number	Terminal symbol		
1	NC	NC	NC
2	NC	-INPUT 1	NC
3	-INPUT 1	+INPUT 1	NC
4	+INPUT 1	-V _{DD} / GND	NC
5	-V _{DD} / GND	NC	-INPUT 1
6	NC	OUTPUT	NC
7	OUTPUT	+V _{DD}	+INPUT 1
8	+V _{DD}	NC	NC
9	NC	---	NC
10	NC	---	-V _{DD} / GND
11	---	---	NC
12	---	---	NC
13	---	---	NC
14	---	---	NC
15	---	---	OUTPUT
16	---	---	NC
17	---	---	+V _{DD}
18	---	---	NC
19	---	---	NC
20	---	---	NC

NC = No connect

FIGURE 1. Terminal connections – Continued.

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Device types	05 and 06 (without shutdown)		
Case outlines	H	P	2
Terminal number	Terminal symbol		
1	NC	OUTPUT 1	NC
2	OUTPUT 1	-INPUT 1	OUTPUT 1
3	-INPUT 1	+INPUT 1	-INPUT 1
4	+INPUT 1	-V _{DD} / GND	+INPUT 1
5	-V _{DD} / GND	+INPUT 2	NC
6	+INPUT 2	-INPUT 2	-V _{DD} / GND
7	-INPUT 2	OUTPUT 2	NC
8	OUTPUT 2	+V _{DD}	NC
9	+V _{DD}	---	NC
10	NC	---	NC
11	---	---	NC
12	---	---	NC
13	---	---	NC
14	---	---	NC
15	---	---	NC
16	---	---	+INPUT 2
17	---	---	NC
18	---	---	-INPUT 2
19	---	---	OUTPUT 2
20	---	---	+V _{DD}

NC = No connect

FIGURE 1. Terminal connections – Continued.

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Device types	07 and 08 (with shutdown)	
Case outlines	C	2
Terminal number	Terminal symbol	
1	OUTPUT 1	NC
2	-INPUT 1	OUTPUT 1
3	+INPUT 1	-INPUT 1
4	-V _{DD} / GND	+INPUT 1
5	NC	NC
6	SHDN 1	-V _{DD} / GND
7	NC	NC
8	NC	NC
9	SHDN 2	SHDN 1
10	NC	NC
11	+INPUT 2	NC
12	-INPUT 2	NC
13	OUTPUT 2	SHDN 2
14	+V _{DD}	NC
15	---	NC
16	---	+INPUT 2
17	---	NC
18	---	-INPUT 2
19	---	OUTPUT 2
20	---	+V _{DD}

NC = No connect

FIGURE 1. Terminal connections – Continued.

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4. VERIFICATION

4.1 Sampling and inspection. For device classes Q and V, sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

4.2 Screening. For device classes Q and V, screening shall be in accordance with MIL-PRF-38535, and shall be conducted on all devices prior to qualification and technology conformance inspection.

4.2.1 Additional criteria for device classes Q and V.

- a. The burn-in test duration, test condition and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document revision level control of the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.
- b. Interim and final electrical test parameters shall be as specified in table II herein.
- c. Additional screening for device class V beyond the requirements of device class Q shall be as specified in MIL-PRF-38535, appendix B.

4.3 Qualification inspection for device classes Q and V. Qualification inspection for device classes Q and V shall be in accordance with MIL-PRF-38535. Inspections to be performed shall be those specified in MIL-PRF-38535 and herein for groups A, B, C, D, and E inspections (see 4.4.1 through 4.4.4).

4.4 Conformance inspection. Technology conformance inspection for classes Q and V shall be in accordance with MIL-PRF-38535 including groups A, B, C, D, and E inspections, and as specified herein.

4.4.1 Group A inspection.

- a. Tests shall be as specified in table II herein.
- b. Subgroups 7, 8, 9, 10, and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.

4.4.2 Group C inspection. The group C inspection end-point electrical parameters shall be as specified in table II herein.

4.4.2.1 Additional criteria for device classes Q and V. The steady-state life test duration, test condition and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The test circuit shall be maintained under document revision level control by the device manufacturer's TRB in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.

4.4.3 Group D inspection. The group D inspection end-point electrical parameters shall be as specified in table II herein.

4.4.4 Group E inspection. Group E inspection is required only for parts intended to be marked as radiation hardness assured (see 3.5 herein).

- a. End-point electrical parameters shall be as specified in table II herein.
- b. For device classes Q and V, the devices or test vehicle shall be subjected to radiation hardness assured tests as specified in MIL-PRF-38535 for the RHA level being tested. All device classes must meet the postirradiation end-point electrical parameter limits as defined in table I at $T_A = +25^{\circ}\text{C} \pm 5^{\circ}\text{C}$, after exposure, to the subgroups specified in table II herein.

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TABLE II. Electrical test requirements.

Test requirements	Subgroups (in accordance with MIL-PRF-38535, table III)	
	Device class Q	Device class V
Interim electrical parameters (see 4.2)	---	---
Final electrical parameters (see 4.2)	1,2,3,4,5,6 <u>1/</u>	1,2,3,4,5,6 <u>1/</u>
Group A test requirements (see 4.4)	1,2,3,4,5,6	1,2,3,4,5,6
Group C end-point electrical parameters (see 4.4)	1	1
Group D end-point electrical parameters (see 4.4)	1	1
Group E end-point electrical parameters (see 4.4)	---	---

1/ PDA applies to subgroup 1.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38535 for device classes Q and V.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.1.1 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor prepared specification or drawing.

6.2 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished using DD Form 1692, Engineering Change Proposal.

6.3 Record of users. Military and industrial users should inform DLA Land and Maritime when a system application requires configuration control and which SMD's are applicable to that system. DLA Land and Maritime will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DLA Land and Maritime-VA, telephone (614) 692-8108.

6.4 Comments. Comments on this drawing should be directed to DLA Land and Maritime-VA, Columbus, Ohio 43218-3990, or telephone (614) 692-0540.

6.5 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535 and MIL-HDBK-1331.

6.6 Sources of supply.

6.6.1 Sources of supply for device classes Q and V. Sources of supply for device classes Q and V are listed in MIL-HDBK-103 and QML-38535. The vendors listed in MIL-HDBK-103 and QML-38535 have submitted a certificate of compliance (see 3.6 herein) to DLA Land and Maritime-VA and have agreed to this drawing.

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STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 13-03-06

Approved sources of supply for SMD 5962-00512 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38535 during the next revision. MIL-HDBK-103 and QML-38535 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DLA Land and Maritime -VA. This information bulletin is superseded by the next dated revision of MIL-HDBK-103 and QML-38535. DLA Land and Maritime maintains an online database of all current sources of supply at <http://www.landandmaritime.dla.mil/Programs/Smcr/>.

Standard microcircuit drawing PIN <u>1</u> /	Vendor CAGE number	Vendor similar PIN <u>2</u> /
5962-0051201QHA	01295	TLV2460MUB
5962-0051201QPA	01295	TLV2460MJGB
5962-0051201Q2A	01295	TLV2460MFKB
5962-0051202QHA	01295	TLV2460AMUB
5962-0051202QPA	01295	TLV2460AMJGB
5962-0051202Q2A	01295	TLV2460AMFKB
5962-0051203QHA	01295	TLV2461MUB
5962-0051203QPA	01295	TLV2461MJGB
5962-0051203Q2A	01295	TLV2461MFKB
5962-0051204QHA	01295	TLV2461AMUB
5962-0051204QPA	01295	TLV2461AMJGB
5962-0051204Q2A	01295	TLV2461AMFKB
5962-0051205QHA	01295	TLV2462MUB
5962-0051205QPA	01295	TLV2462MJGB
5962-0051205Q2A	01295	TLV2462MFKB
5962-0051206QHA	01295	TLV2462AMUB
5962-0051206QPA	01295	TLV2462AMJGB
5962-0051206Q2A	01295	TLV2462AMFKB

STANDARD MICROCIRCUIT DRAWING BULLETIN – CONTINUED.

DATE: 13-03-06

Standard microcircuit drawing PIN <u>1/</u>	Vendor CAGE number	Vendor similar PIN <u>2/</u>
5962-0051207QCA	01295	TLV2463MUB
5962-0051207Q2A	01295	TLV2463MFKB
5962-0051208QCA	01295	TLV2463AMUB
5962-0051208Q2A	01295	TLV2463AMFKB

1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.

2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number

Vendor name and address

01295

Texas Instruments Incorporated
 Semiconductor Group
 8505 Forest Lane
 P.O. Box 660199
 Dallas, TX 75243
 Point of contact: U.S. Highway 75 South
 P.O. Box 84, M/S 853
 Sherman, TX 75090-9493

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