REVISIONS

LTR	DESCRIPTION	DATE	APPROVED
A	Change figure 1 from a two-view drawing to a three-view drawing. Change the width and length dimensions. Editorial changes throughout.	23 NOV 1994	N. Christolear
B	Add vendor. Editor changes throughout.	10 SEP 1999	K. Cottongim
C	Modify PIN to include termination material. Add new paragraphs 3.4 and 6.3. Revised figure 1 to coincide with MIL-PRF-55342/7.	12 DEC 2002	K. Cottongim
D	Inactive for new design. New vendor plant address. Revise to present DoD policy requirements.	12 APR 2005	K. Cottongim
E	Revise to present DoD policy requirements. Change reference document from MIL-PRF-55342 to MIL-PRF-32159. Remove vendor.	30 MAY 2014	M. Radecki

CURRENT DESIGN ACTIVITY CAGE CODE 037Z3
HAS CHANGED NAMES TO:
DLA LAND AND MARITIME
COLUMBUS, OHIO 43218-3990

Notice of Inactivation for New Design

DLA Land and Maritime Drawing 94011 is inactive for new design and is no longer used, except for replacement purposes.
Use MIL-PRF-32159/7.

Prepared in accordance with ASME Y14.100

REV STATUS OF PAGES																		
REV	E	E	E	E	E													
PAGES	1	2	3	4	5													

PMIC N/A	**PREPARED BY** Andrew R. Ernst	**DESIGN ACTIVITY:** **DEFENSE ELECTRONIC SUPPLY CENTER** **DAYTON, OH 45444-5000**
Original date of drawing 4 March 1994	**CHECKED BY** Andrew R. Ernst	**TITLE** RESISTOR, CHIP, FIXED, FILM, ZERO-OHM, STYLE RM1206
	APPROVED BY David E. Moore	
	SIZE A / **CODE IDENT. NO.** 14933	**DWG NO.** 94011
	REV E	**PAGE** 1 **OF** 5

1. SCOPE

1.1 Scope. This drawing describes the requirements for a zero-ohm, chip resistor with wrap around termination, style 1206.

1.2 Part or Identifying Number (PIN). The complete PIN is as follows:

94011	-	B
Drawing number		Termination material (see 3.4 & 6.8)

2. APPLICABLE DOCUMENTS

2.1 Government documents.

2.1.1 Specifications, standards, and handbooks. The following specifications, standards, and handbooks form a part of this document to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATIONS

* MIL-PRF-32159 - Resistor, Chip, Fixed, Film, Zero Ohm, Industrial, High Reliability, Space Level, General Specification for.

DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-690 - Failure Rate Sampling Plans and Procedures.
MIL-STD-790 - Standard Practice for Established Reliability and High Reliability Qualified Products List (QPL) Systems for Electrical, Electronic, and Fiber Optics Parts Specifications.

MIL-STD-1285 - Marking of Electrical, and Electronic Parts.

* (Copies of these documents are available online at http://quicksearch.dla.mil.)

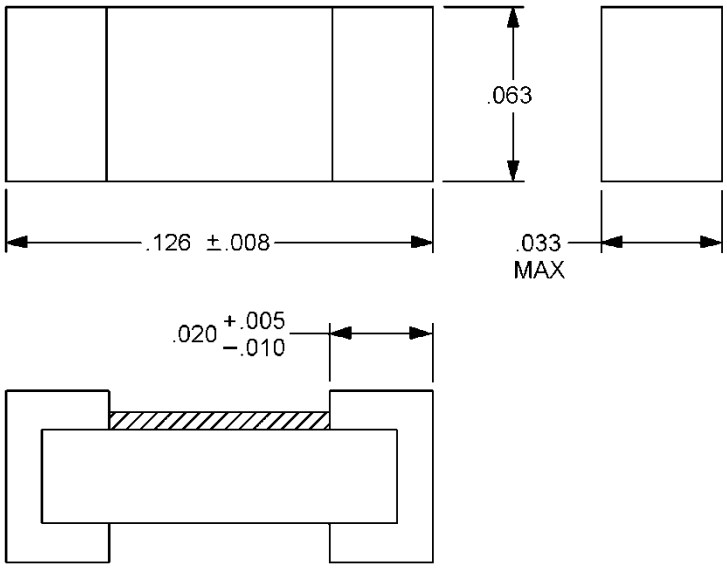
* 2.2 Order of precedence. Unless otherwise noted herein or in the contract, or in the event of a conflict between the text of this document and the references cited herein (except for related specification sheets), the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

* 3.1 Item requirements. The individual item requirements shall be in accordance with MIL-PRF-32159 and as specified herein.

* 3.2 Interface and physical dimensions. The resistor shall meet the interface and physical dimensions as specified in MIL-PRF-32159 and herein (see figure 1).

* 3.2.1 Pure tin. The use of pure tin, as an underplate or final finish is prohibited both internally and externally. Tin content of resistor components and solder shall not exceed 97 percent, by mass. Tin shall be alloyed with a minimum of 3 percent lead, by mass (see 6.4).



Inches	mm
.005	0.13
.008	0.20
.010	0.25
.020	0.51
.033	0.84
.063	1.60
.126	3.20

NOTES:
1. Dimensions are in inches.
2. Metric equivalents are given for general information only.
3. Unless otherwise specified, tolerance is ±.005 (0.13 mm).
4. The pictorial view of the style above is given as representative of the envelope of the item. Slight deviations from the outline shown, which are contained within the envelope, and do not alter the functional aspects of the devices are acceptable.

FIGURE 1. Zero-ohm chip resistor style RM1206.

3.3 Electrical characteristics.

3.3.1 Temperature range. The temperature range shall be -55°C to +125°C.

3.3.2 Resistance. The resistance shall not exceed 0.02 ohm.

* 3.4 Termination. Termination material shall be identified by a single letter as specified in MIL-PRF-32159; code letters B, G, U, and C (see 6.8).

3.5 Marking. Marking of the individual chip resistors is not required; however, each unit package shall be marked in accordance with MIL-STD-1285 and include the PIN as specified herein (see 1.2), the manufacturer's name or Commercial and Government Entity (CAGE) code, date, and lot codes.

* 3.6 Recycled, recovered, environmentally preferable, or biobased materials. Recycled, recovered, environmentally preferable, or biobased materials should be used to the maximum extent possible provided that the material meets or exceeds the operational and maintenance requirements, and promotes economically advantageous life cycle costs.

* 3.7 Manufacturer eligibility. To be eligible for listing as an approved source of supply, a manufacturer shall be listed on the MIL-PRF-32159 Qualified Products List for at least one part, or perform the group A and group B inspections specified herein on a sample agreed upon by the manufacturer and DLA Land and Maritime-VAT.

* 3.7.1 Certificate of compliance. A certificate of compliance shall be required from manufacturers requesting to be listed as an approved source of supply.

3.8 Workmanship. Resistors shall be uniform in quality and free from defects that will affect life, serviceability, or appearance.

4. VERIFICATION

* 4.1 Product assurance program. The product assurance program specified in MIL-PRF-32159 and maintained in accordance with MIL-STD-790 is not applicable to this document.

4.2 Qualification inspection. Qualification inspection is not applicable to this document.

* 4.3 Product level qualification. The product level qualification specified in MIL-PRF-32159 and MIL-STD-690 is not applicable to this document.

4.4 Conformance inspection.

* 4.4.1 Inspection of product for delivery. Inspection of product for delivery shall consist of group A inspection (established reliability level only and the parts per million reporting is not applicable) of MIL-PRF-32159.

* 4.5 Visual and mechanical examination. Resistors shall be examined to verify that the materials, design, construction, physical dimensions, marking, and workmanship are in accordance with the applicable requirements of MIL-PRF-32159.

5. PACKAGING

5.1 Packaging. For acquisition purposes, the packaging requirements shall be as specified in the contract or order (see 6.2). When packaging of materiel is to be performed by DoD or in-house contractor personnel, these personnel need to contact the responsible packaging activity to ascertain requisite requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activities within the Military Service or Defense Agency, or within the Military Services System Command. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

6. NOTES

(This section contains information of a general or explanatory nature that may be helpful, but is not mandatory.)

6.1 Intended use. Chip resistors are intended for use in thick or thin film circuits where microcircuitry is intended, also in most surface mount application.

6.2 Ordering data. The contract or purchase order should specify the following:

a. Complete PIN (see 1.2).

b. Requirements for delivery: One copy of the conformance inspection data or certification of compliance that parts have passed conformance inspection with each shipment of parts by the manufacturer.

c. Requirements for packaging and packing.

6.3 PIN supersession. The PIN in the original 94011 and revision B has been superseded by a new PIN in revision C that includes a code for termination material. Table I illustrates a generic PIN substitution:

TABLE I. PIN supersession.

Revisions through B	Revision C
94011	94011-B

* 6.4 Tin whisker growth. The use of alloys with tin content greater than 97 percent, by mass, may exhibit tin whisker growth problems after manufacture. Tin whiskers may occur anytime from a day to years after manufacture and can develop under typical operating conditions, on products that use such materials. Conformal coatings applied over top of a whisker-prone surface will not prevent the formation of tin whiskers. Alloys of 3 percent lead, by mass, have shown to inhibit the growth of tin whiskers. For additional information on this matter, refer to ASTM-B545 (Standard Specification for Electrodeposited Coatings of Tin).

* 6.5 Electrostatic charge. Under several combinations of conditions, these resistors can be electrically damaged, by electrostatic charges, and drift from specified value. Users should consider this phenomena when ordering or shipping resistors. Direct shipment to the Government is controlled by MIL-DTL-39032, which specifies a preventive packaging procedure.

* 6.6 Pulse applications. Designers are CAUTIONED on using the above resistors in high power pulse applications. Since they have not been qualified nor tested for such applications, damage and premature failure are possible. These resistors only see a onetime pulse (Short-time overload) as part of the group B inspection of MIL-PRF-32159.

* 6.7 User of record. Coordination of this document for future revisions is coordinated only with the approved source of supply and the users of record of this document. Requests to be added as a recorded user of this drawing may be achieved online at resistor@dla.mil or in writing to: DLA Land and Maritime, Attn: VAT, Post Office Box 3990, Columbus, OH 43218-3990 or by telephone (614) 692-0552 or DSN 850-0552.

* 6.8 Approved source of supply. Approved source of supply is listed herein. Additional sources will be added as they become available. Assistance in the use of this drawing may be obtained online at resistor@dla.mil or contact DLA Land and Maritime, Attn: VAT, Post Office Box 3990, Columbus, OH 43218-3990 or by telephone (614) 692-0552 or DSN 850-0552.

DLA Land and Maritime drawing PIN 94011-*	Vendors similar designation or type number 1/	Vendor CAGE	Vendor's name and address
Termination B.	J1415-0R02Y	50316	Mini-Systems Incorporated Thick Film Division P.O. 69 North Attleboro, MA 02761-0069
Term. B, G, U, and C.	H1206CP*000(D94011)	56235	State of the Art, Incorporated 2470 Fox Hill Road State College, PA 16803-1797
Termination B.	RCWPM-1206-99	91637	Vishay Dale Electronics, Incorporated PO Box 609 Columbus, NE 68602-0609 PLANT: Vishay Israel, Ltd Emek-Sara "B" Industrial Park Beer Sheva, Israel 84874

1/ Parts must be purchased to the DLA Land and Maritime PIN to assure that all performance requirements and tests are met.