

REVISIONS			
LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Changes in accordance with NOR 5905-E640.	07-01-22	M. Radecki
B	Paragraph 3.3.5, corrected wattage rating.	08-02-06	M. Radecki
C	Paragraph 3.3.9, corrected ohmic adder. Editorial changes throughout.	09-05-05	M. Radecki
D	Update Hyperlinks. Editorial changes throughout.	18-07-13	M. Radecki
E	Change sections 3.4 and 4.9. Update to present DoD requirements.	24-03-29	M. Radecki

CURRENT DESIGN ACTIVITY CAGE CODE 037Z3
HAS CHANGED NAMES TO:
DLA LAND AND MARITIME
COLUMBUS, OHIO 43218-3990



Prepared in accordance with [ASME Y14.24](#)

Selected Item Drawing

Revision Status of Sheets												
REV												
SHEET												
REV	E	E	E	E	E	E						
SHEET	1	2	3	4	5	6						

PMIC N/A Original date of drawing 2006-30-10	PREPARED BY Andrew R. Ernst		DEFENSE SUPPLY CENTER, COLUMBUS COLUMBUS, OHIO 43218-3990	
	CHECKED BY Andrew R. Ernst		TITLE RESISTOR, FIXED, WIREWOUND, SURFACE MOUNT POWER TYPE (3 WATT)	
	APPROVED BY Michael A. Radecki			
	SIZE A	CAGE CODE 037Z3	DWG NO. 07002	
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1. SCOPE

1.1 Scope. This drawing describes the requirements for a 3 watt, fixed, surface mounted, wirewound, power type resistor.

1.2 Part or Identifying Number (PIN). The complete PIN is as follows:

<u>07002</u> Drawing number	<u>****</u> Resistance value (see 3.3.1 and 3.3.2)	<u>*</u> Tolerance (see 3.3.3)	<u>*</u> Encapsulation (see 3.3.4)
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2. APPLICABLE DOCUMENTS

2.1 Government documents.

2.1.1 Specifications and standards. The following specifications, standards and handbooks form a part of this document to the extent specified herein. Unless otherwise specified, the issues of these documents are those listed in the cited in the solicitation or contract (see 6.2).

DEPARTMENT OF DEFENSE SPECIFICATIONS

[MIL-PRF-39007](#) - Resistor, Fixed, Wirewound (Power Type), Nonestablished Reliability, Established Reliability, and Space Level, General Specification for

DEPARTMENT OF DEFENSE STANDARDS

[MIL-STD-202](#) - Test Methods for Electronic and Electrical Component Parts
[MIL-STD-202-107](#) - Test Method Standard Method 107, Thermal Shock
[MIL-STD-202-208](#) - Test Method Standard Method 208, Solderability
[MIL-STD-690](#) - Failure Rate Sampling Plans and Procedures
[MIL-STD-790](#) - Established Reliability and High Reliability Qualified Products List (QPL) Systems for Electrical, Electronic, and Fiber Optic Parts Specifications
[MIL-STD-1285](#) - Marking of Electrical and Electronic Parts

(Copies of these documents are available online at <https://quicksearch.dla.mil>.)

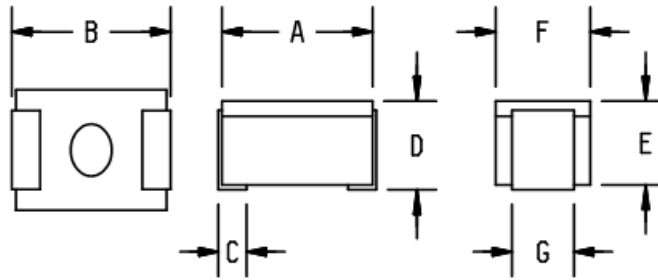
2.2 Order of precedence. Unless otherwise noted herein or in the contract, in the event of a conflict between the text of this document and the references cited herein, the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with [MIL-PRF-39007](#) and as specified herein.

3.2 Interface and physical dimensions. Resistors shall meet the interface and physical dimensions as specified in [MIL-PRF-39007](#) and herein (see [figure 1](#)).

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Dimensions						
A	B	C	D	E	F	G
0.678 ±0.005 (17.23 ±.127)	0.690 ±0.032 (17.53 ±.813)	0.100 ±0.010 (2.54 ±.254)	0.280 ±0.015 (7.11 ±.381)	0.156 ±0.005 (3.96 ±.127)	0.275 ±0.005 (6.98 ±.127)	0.215 ±0.005 (5.46 ±.127)

NOTES:

1. Dimensions are in inches.
2. Millimeter equivalents are given for general information only.

FIGURE 1. Resistor, fixed, wirewound, surface mount.

3.3 Electrical characteristics.

3.3.1 Resistance value designation. Resistance value designations shall be in accordance with [MIL-PRF-39007](#).

3.3.2 Resistance. The resistance range shall be 0.1 ohm to 8 kilohms.

3.3.3 Resistance tolerance. Resistance tolerances are available in the following values

- (D) ±0.5 percent (for values above 1 ohm)
- (F) ±1 percent,
- (H) ±3 percent
- (J) ±5 percent

3.3.4 Encapsulation. The encapsulation shall be in accordance with table I.

TABLE I. Encapsulation.

Style	Encapsulation
A	Thermoplastic

3.3.5 Power rating. The power rating shall be 3 watt at +70°C. For operation at temperatures greater than +70°C, derated in accordance with [figure 2](#).

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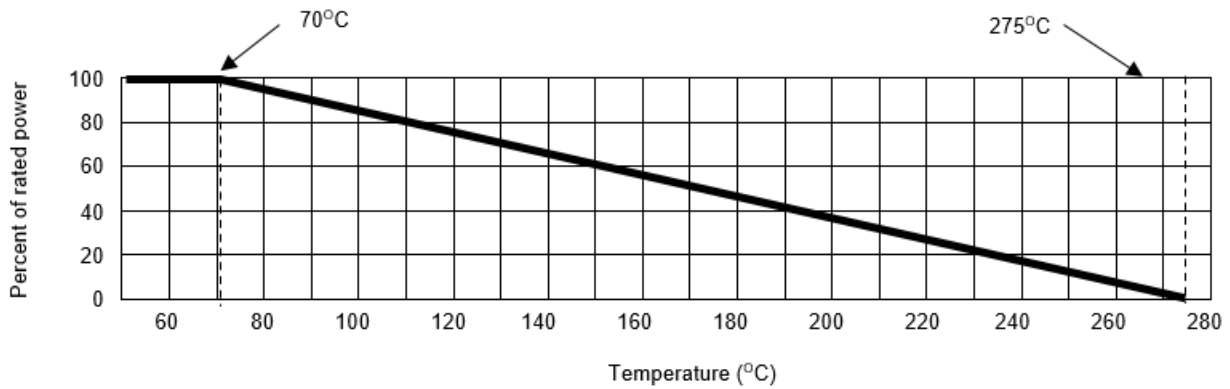


FIGURE 2. Derating curves for high ambient temperatures.

3.3.6 Resistance temperature coefficient. The resistance temperature coefficient shall be as follows in Table II.

TABLE II - Resistance Temperature Coefficient (RTC).

Ohms	RTC
0.1 to 0.3	±150 ppm/°C
0.31 to 0.99	±90 ppm/°C
1.0 to 9.9	±50 ppm/°C
10 and above	±20 ppm/°C

3.3.7 Thermal shock. When resistors are tested as specified in 4.6, there shall be no evidence of mechanical damage. The maximum change in resistance shall not exceed ± (0.2 percent + 0.05 ohm).

3.3.8 Load life. When resistors are tested as specified in 4.7, there shall be no evidence of mechanical damage. The maximum change in resistance shall not exceed ± (0.5 percent + 0.05 ohm).

3.3.9 Overload. When resistors are tested as specified in 4.8, there shall be no evidence of electrical or mechanical damage. The maximum change in resistance shall not exceed ± (0.2 percent + 0.05 ohm).

3.3.10 Solderability. The total surface area of the dipped part of the termination is at least 95 percent covered by a continuous new solder coating. Pin holes, voids, porosity, nonwetting, or dewetting shall not be concentrated in one area and shall not exceed 5 percent of the total termination area. The area of the surface to be tested shall include the total metallized areas of both terminations within .3125 inch (7.938 mm) of the bottom contact point of the "J" bend. In case of a dispute, the percentage of coverage with pinholes or voids shall be determined by actual measurement of these areas, as compared to the total area.

3.3.11 High temperature exposure. When resistors are tested as specified in 4.10, there shall be no evidence of mechanical damage. The maximum change in resistance shall not exceed ± (0.5 percent + 0.05 ohm).

3.4 Marking. Marking shall be as follows:

- 070021500FA - DLA Land and Maritime PIN per paragraph 1.2
- XXXXX - CAGE Code
- XXXX - Date Code YYWW

3.5 Pure tin. The use of pure tin, as an underplate or final finish is prohibited both internally and externally. Tin content of resistor components and solder shall not exceed 97 percent, by mass. Tin shall be alloyed with a minimum of 3 percent lead, by mass (see 6.3).

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3.6 Manufacturer eligibility. To be eligible to be added as an approved source of supply, a manufacturer shall be listed on the [MIL-PRF-39007 Qualified Products List](#) for at least one part, or perform the group A and group B inspections specified herein on a sample agreed upon by the manufacturer and DLA Land and Maritime - VAT.

3.6.1 Certificate of compliance. A certificate of compliance shall be required from manufacturers requesting to be an approved source of supply.

3.7 Recycled, recovered, environmentally preferable, or biobased materials. Recycled, recovered, environmentally preferable or biobased materials should be used to the maximum extent possible, provided that the material meets or exceeds the operational and maintenance requirements, and promotes economically advantageous life cycle costs.

3.8 Workmanship. The resistor shall be uniform in quality and free from any defects that will affect life, serviceability, or appearance.

4. VERIFICATION

4.1 Qualification inspection. Qualification inspection is not applicable to this document.

4.2 Reliability assurance program. The reliability assurance program specified in [MIL-PRF-39007](#) and maintained in accordance with [MIL-STD-790](#) is not applicable to this document.

4.3 Failure rate qualification. Failure rate qualification specified in [MIL-PRF-39007](#) and [MIL-STD-690](#) is not applicable to this document.

4.4 Conformance inspection. Conformance inspection shall consist of group A (ER level) and group B inspections of [MIL-PRF-39007](#). Group A, subgroup 1 conditioning will not be performed.

4.4.1 Certification. The activity, at its discretion, may accept a certificate of compliance with group B requirements in lieu of performing group B tests (see 6.2d).

4.5 Inspection of packaging. Inspection of packaging shall be in accordance with MIL-PRF-39007.

4.6 Thermal shock. Resistors shall be tested in accordance with [MIL-PRF-39007](#) and the following exception shall apply: Mounting of test samples shall be on surface mount test cards, and [MIL-STD-202-107](#), test condition B-3.

4.7 Load life. Resistors shall be tested in accordance with [MIL-PRF-39007](#) and the following exception shall apply: Mounting of test samples shall be on surface mount test cards, and 1,000 hours at maximum load at +25°C.

4.8 Overload. Resistors shall be tested in accordance with [MIL-PRF-39007](#) and the following exception shall apply: Mounting of test samples shall be on surface mount test cards, and Overload testing will be performed at 5 times rated power for 5 seconds.

4.9 Solderability. Solderability shall be in accordance with [MIL-STD-202-208](#) and the following details shall apply:

- a. The sample size shall be 8 pieces.
- b. Application of flux: Type R shall be used. Terminations shall be immersed in the flux, which is at room temperature, to the minimum depth necessary to cover the surface to be tested. The terminations shall be completely immersed by individually dipping each termination. The terminations to be tested shall be immersed in the flux for a period of 5 seconds to 10 seconds.
- c. Solder dip: The dross and burned flux shall be skimmed from the surface of the molten solder (may not require separate operation in wave or flow pot). The molten solder shall be maintained at a uniform temperature of +245°C ±5°C. The surface of the molten solder shall be skimmed again prior to immersing the terminations in the solder. The part shall be attached to a dipping device and the flux-covered terminations immersed in molten solder to the depth, and in the same manner specified in 4.9a. After the dipping process, the part shall be allowed to cool in air. Residue flux shall be removed from the terminations by rinsing in a suitable solvent. If necessary, a soft cloth or cotton swab moistened with clean 91 percent isopropyl alcohol shall remove all remaining flux.
- d. Examination of terminations: After each dip-coated termination has been thoroughly cleaned of flux, the terminations shall be examined under a magnification between 10X and 20X.

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4.10 High temperature exposure. Resistors shall be tested in accordance with [MIL-PRF-39007](#) and the following exception shall apply: Mounting of test samples shall be on surface mount test cards, and 250 hours at +125°C.

5. PACKAGING

5.1 Packaging. For acquisition purposes, the packaging requirements shall be as specified in the contract or order (see 6.2). When packaging of material is to be performed by DoD or in-house contractor personnel, these personnel need to contact the responsible packaging activity to ascertain packaging requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activities within the Military Service or Defense Agency, or within the military service's system commands. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

6. NOTES

(This section contains information of a general or explanatory nature that may be helpful, but is not mandatory.)

6.1 Intended use. Surface mount resistors are used in electronic circuits where space is a major concern.

6.2 Ordering data. The contract or purchase order should specify the following:

- a. Complete DLA Land and Maritime CAGE Code and PIN (see 1.2).
- b. Requirements for delivery: One copy of the conformance inspection data or certification of compliance that parts have passed conformance inspection with each shipment of parts by the manufacturer.
- c. Requirements for packaging and packing.
- d. Whether the manufacturer performs the group B tests or provides certification of compliance with group B requirements (see 4.4.1).

6.3 Tin whisker growth. The use of alloys with tin content greater than 97 percent, by mass, may exhibit tin whisker growth problems after manufacture. Tin whiskers may occur anytime from a day to years after manufacture and can develop under typical operating conditions, on products that use such materials. Conformal coatings applied over top of a whisker-prone surface will not prevent the formation of tin whiskers. Alloys of 3 percent lead, by mass, have shown to inhibit the growth of tin whiskers. For additional information on this matter, refer to [ASTM-B545](#) (Standard Specification for Electrodeposited Coatings of Tin).

6.4 User of record. Coordination of this document for future revisions is coordinated only with the approved sources of supply and the users of record of this document. Requests to be added as a recorded user of this drawing may be achieved on-line at resistor@dla.mil or in writing to: DLA Land and Maritime - VAT, P.O. Box 3990, Columbus, OH 43218-3990 or by telephone (614) 400-3997 or DSN 850-0552.

6.5 Approved source of supply. Approved sources of supply are listed herein. Additional sources will be added as they become available. Assistance in the use of this drawing may be obtained on on-line at resistor@dla.mil or contact DLA Land and Maritime - VAT, P.O. Box 3990, Columbus, OH 43218-3990, or by telephone (614) 400-3997 or DSN 850-0552.

DLA Land and Maritime drawing PIN (see 1.2)	Vendors similar designation or type number ^{1/}	Vendor CAGE	Vendor's name and address
07002*****	WSC6927	91637	Vishay Dale Electronics, Inc. P.O. Box 609 Columbus, NE 68601-0609 <u>Plant:</u> Electronica Dale de Mexico, S.A. de C.V. Insurgentes 606 PT. CD., Juarez, Chihuahua MEXICO

^{1/} Parts must be purchased to the DLA Land and Maritime CAGE Code and PIN to assure that all performance requirements and tests are met.

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