

INCH-POUND

MIL-PRF-83532/3C  
10 November 2015  
SUPERSEDING  
MIL-PRF-83532/3B  
7 June 2004

PERFORMANCE SPECIFICATION SHEET

DELAY LINES, 14-PIN DIP COMPATIBLE, 10 TAP

This specification is approved for use by all Departments and Agencies of the Department of Defense.

The requirements for acquiring the product described herein shall consist of this specification sheet and [MIL-PRF-83532](#).

REQUIREMENTS:

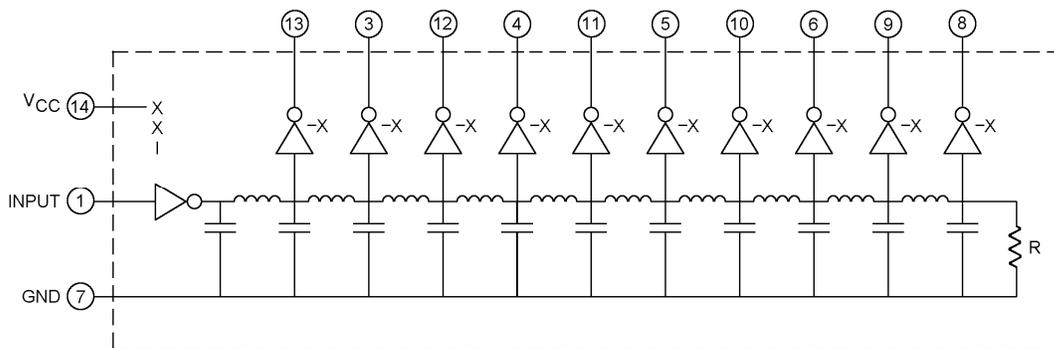
Design and construction: Parts shall be of the design, construction, and physical dimensions specified in the appendix of MIL-PRF-83532. Four case styles (A, B, C, and D) are available.

Input pulse: Delay lines must be capable of meeting applicable [table I](#) and [table II](#) requirements with an input pulse having the following characteristics:

- A: Leading edge of a positive-going pulse.
- B: Minimum pulse width of 50 percent of total delay time.
- C: Fixed pulse repetition rate equal to ten times the total delay time.
- D: Duty cycle not to exceed 50 percent.

Schematic: See [figure 1](#).

Delay times: Delay time from input to all taps shall be as specified in [table I](#) (+25°C) and [table II](#) (-55°C and +125°C).



NOTES:

1. Schematic is for general information only.
2. Pin 2 is omitted on level A delay lines.
3. Pin 2 is installed on level B delay lines.

FIGURE 1. [Schematic](#).



TABLE I. Dash numbers and delay characteristics at +25°C, Vcc = 5.00 ±.01 volts.

Dash number	Delay and tolerances (ns)									
	Tap 1 Pin 13	Tap 2 Pin 3	Tap 3 Pin 12	Tap 4 Pin 4	Tap 5 Pin 11	Tap 6 Pin 5	Tap 7 Pin 10	Tap 8 Pin 6	Tap 9 Pin 9	Tap 10 Pin 8
001	5 ±2 ns	10 ±2 ns	15 ±2 ns	20 ±2 ns	25 ±2 ns	30 ±2 ns	35 ±2 ns	40 ±2 ns	45 ±5%	50 ±5%
002	7.5 ±2 ns	15 ±2 ns	22.5 ±2 ns	30 ±2 ns	37.5 ±2 ns	45 ±5%	52.5 ±5%	60 ±5%	67.5 ±5%	75 ±5%
003	10 ±2 ns	20 ±2 ns	30 ±2 ns	40 ±2 ns	50 ±5%	60 ±5%	70 ±5%	80 ±5%	90 ±5%	100 ±5%
004	12.5 ±2 ns	25 ±2 ns	37.5 ±2 ns	50 ±5%	62.5 ±5%	75 ±5%	87.5 ±5%	100 ±5%	112.5 ±5%	125 ±5%
005	15 ±2 ns	30 ±2 ns	45 ±5%	60 ±5%	75 ±5%	90 ±5%	105 ±5%	120 ±5%	135 ±5%	150 ±5%
006	17.5 ±2 ns	35 ±2 ns	52.5 ±5%	70 ±5%	87.5 ±5%	105 ±5%	122.5 ±5%	140 ±5%	157.5 ±5%	175 ±5%
007	20 ±2 ns	40 ±2 ns	60 ±5%	80 ±5%	100 ±5%	120 ±5%	140 ±5%	160 ±5%	180 ±5%	200 ±5%
008	25 ±2 ns	50 ±5%	75 ±5%	100 ±5%	125 ±5%	150 ±5%	175 ±5%	200 ±5%	225 ±5%	250 ±5%
009	30 ±2 ns	60 ±5%	90 ±5%	120 ±5%	150 ±5%	180 ±5%	210 ±5%	240 ±5%	270 ±5%	300 ±5%
010	35 ±2 ns	70 ±5%	105 ±5%	140 ±5%	175 ±5%	210 ±5%	245 ±5%	280 ±5%	315 ±5%	350 ±5%
011	40 ±2 ns	80 ±5%	120 ±5%	160 ±5%	200 ±5%	240 ±5%	280 ±5%	320 ±5%	360 ±5%	400 ±5%
012	45 ±5%	90 ±5%	135 ±5%	180 ±5%	225 ±5%	270 ±5%	315 ±5%	360 ±5%	405 ±5%	450 ±5%
013	50 ±5%	100 ±5%	150 ±5%	200 ±5%	250 ±5%	300 ±5%	350 ±5%	400 ±5%	450 ±5%	500 ±5%
014	60 ±5%	120 ±5%	180 ±5%	240 ±5%	300 ±5%	360 ±5%	420 ±5%	480 ±5%	540 ±5%	600 ±5%
015	70 ±5%	140 ±5%	210 ±5%	280 ±5%	350 ±5%	420 ±5%	490 ±5%	560 ±5%	630 ±5%	700 ±5%
016	80 ±5%	160 ±5%	240 ±5%	320 ±5%	400 ±5%	480 ±5%	560 ±5%	640 ±5%	720 ±5%	800 ±5%
017	90 ±5%	180 ±5%	270 ±5%	360 ±5%	450 ±5%	540 ±5%	630 ±5%	720 ±5%	810 ±5%	900 ±5%
018	100 ±5%	200 ±5%	300 ±5%	400 ±5%	500 ±5%	600 ±5%	700 ±5%	800 ±5%	900 ±5%	1000 ±5%

TABLE II. Dash numbers and delay characteristics at -55°C and +125°C, Vcc = 5.00 ±.01 volts.

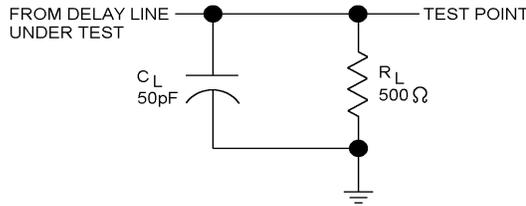
Dash number	Delay and tolerances (ns)									
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TABLE III. DC characteristics.

Test	Symbol	Conditions $-55^{\circ}\text{C} \leq T_C \leq +125^{\circ}\text{C}$	Limits		Unit
			Min	Max	
High level output voltage	$V_{OH}$	$V_{CC} = 4.5\text{ V}$ $V_{IH} = 2.0\text{ V}$ $I_{OH} = -1\text{ mA}$	2.5		V
Low level output voltage	$V_{OL}$	$V_{CC} = 4.5\text{ V}$ $V_{IL} = 0.8\text{ V}$ $I_{OL} = 20\text{ mA}$		0.5	V
Input clamp voltage	$V_{IC}$	$V_{CC} = 4.5\text{ V}$ $I_I = -18\text{ mA}$ $T_C = +25^{\circ}\text{C}$		-1.2	V
High level input current	$I_{IH1}$	$V_{CC} = 5.5\text{ V}, V_{IH} = 2.7\text{ V}$		100	$\mu\text{A}$
	$I_{IH2}$	$V_{CC} = 5.5\text{ V}, V_{IH} = 5.5\text{ V}$		2,000	$\mu\text{A}$
Low level input current	$I_{IL}$	$V_{CC} = 5.5\text{ V}, V_{IL} = 0.5$		-4.0	mA
Short circuit output current	$I_{OS}$	$V_{CC} = 5.5\text{ V}, V_{OS} = 0.0\text{ V}$ (not more than one output shorted at a time)	-40	-150	mA
Low level supply current	$I_{CCL}$	$V_{CC} = 5.5\text{ V}$ $V_I = 0.0\text{ V}$		130	mA

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Output rise time (applied to leading edge only): 5 ns maximum for dash numbers 001 through 015, and 6 ns maximum for 016 through 018. Measurement conditions ( $-55^{\circ}\text{C} \leq T_C \leq +125^{\circ}\text{C}$ ):  $V_{CC} = 5.0 \text{ V dc}$ ;  $TR_1 \leq 3 \text{ ns}$ . The load circuit shall be as follows ( $C_L$  includes probe and test fixture capacitance):



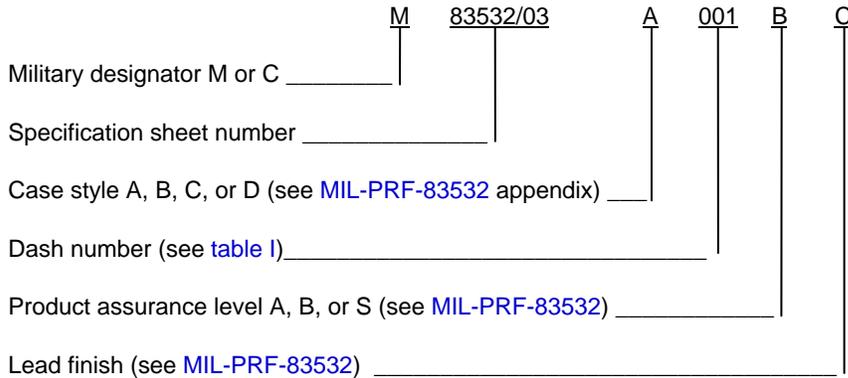
DC parameters (over operating temperature range): See [table III](#).

Rated maximum load (fan-out): Ten TTL Schottky loads per tap (no more than 20 TTL Schottky loads per unit).

Terminations: Part terminations shall be printed-circuit pin type in accordance with [MIL-PRF-38534](#).

Operating temperature: Operating temperature range shall be  $-55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ .

Part or Identifying Number (PIN): The PIN shall be in the following format:



VERIFICATION:

Extent of qualification: The extent of qualification shall be as specified in [MIL-PRF-83532](#), for example:

Qualification and testing of M83532/03A001A\* 1/ and M83532/03A018A\* 1/ shall be sufficient to grant qualification to all dash numbers with case style A contained in this specification sheet. Similarly, for case styles B, C, and D, qualification and testing of dash numbers 001 and 018 shall be sufficient to grant qualification to all dash numbers in this specification sheet having the same case style as the components tested.

1/ \* - Asterisk represents any of the applicable finishes.

Product assurance level: Product assurance level A shall not extend to level B. Level B shall not extend to level A. Level S shall extend to level B. Level B shall not extend to level S.

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Supersession data: MIL-PRF-83532/3 supersedes DESC drawing number 85014 when a qualified source is available.

Changes from previous issue. The margins of this specification are marked with vertical lines to indicate where changes from the previous issue were made. This was done as a convenience only and the Government assumes no liability whatsoever for any inaccuracies in these notations. Bidders and contractors are cautioned to evaluate the requirements of this document based on the entire content irrespective of the marginal notations and relationship to the previous issue.

Referenced documents. In addition to [MIL-PRF-83532](#), this document references the following:

[MIL-PRF-38534](#)

Custodians:

Army - CR

Navy - EC

Air Force - 85

DLA-CC

Preparing activity:

DLA - CC

(Project 5999-2015-017)

Review activities:

Navy - AS, CG, MC, SH

Air Force - 19, 99

NOTE: The activities listed above were interested in this document as of the date of this document. Since organizations and responsibilities can change, you should verify the currency of the information above using the ASSIST Online database at <https://assist.dla.mil/>.