PERFORMANCE SPECIFICATION SHEET

CAPACITORS, CHIP, MULTIPLE LAYER, FIXED, CERAMIC DIELECTRIC, ESTABLISHED RELIABILITY AND NON-ESTABLISHED RELIABILITY, STYLE CDR06

This specification is approved for use by all Departments and Agencies of the Department of Defense.

The requirements for acquiring the product described herein shall consist of this specification and MIL-PRF-55681.

<table>
<thead>
<tr>
<th>Dimensions</th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>L ± .020</td>
<td>W ± .020</td>
<td>T</td>
</tr>
<tr>
<td>Min</td>
<td>Max</td>
<td></td>
</tr>
<tr>
<td>.225</td>
<td>.250</td>
<td>.020</td>
</tr>
</tbody>
</table>

NOTES:
1. Dimensions are in inches.
2. Metric equivalents are given for information only.
3. Dimensions and tolerances are for bare chips. For solder coated terminations, add .025 inch (0.64 mm) to the positive length tolerance and .015 inch (0.38 mm) to the positive width and thickness tolerances.

FIGURE 1. Dimensions and configuration.
REQUIREMENTS:

Dimensions and configuration: See figure 1.

Capacitance value: See table I.

Capacitance tolerance: J = ±5 percent, K = ±10 percent, M = ±20 percent. See table I.

Rated voltage: See table I.

Product level designator: Non-established Reliability - C; Established Reliability - M, P, R, S.

Operating temperature range: -55° C to +125° C.

Marking: In accordance with MIL-PRF-55681.

TABLE I. Capacitor characteristics.

<table>
<thead>
<tr>
<th>Part or Identifying Number (PIN) 1/</th>
<th>Capacitance (pF)</th>
<th>Capacitance tolerance</th>
<th>Rated temperature and voltage temperature limits</th>
<th>Rated voltage (V dc)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CDR06BP682B - - -</td>
<td>6,800</td>
<td>J, K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR06BP822B - - -</td>
<td>8,200</td>
<td>J, K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR06BP103B - - -</td>
<td>10,000</td>
<td>J, K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR06BX394AK - -</td>
<td>390,000</td>
<td>K</td>
<td>BX</td>
<td>50</td>
</tr>
<tr>
<td>CDR06BX474A - - -</td>
<td>470,000</td>
<td>K, M</td>
<td>BX</td>
<td>50</td>
</tr>
</tbody>
</table>

1/ Complete PIN shall include additional symbols to indicate capacitance tolerance (where applicable), termination finish, and product level.

Changes from previous issue. The margins of this specification are marked with vertical lines to indicate where changes from the previous issue were made. This was done as a convenience only and the Government assumes no liability whatsoever for any inaccuracies in these notations. Bidders and contractors are cautioned to evaluate the requirements of this document based on the entire content irrespective of the marginal notations and relationship to the last previous issue.

Custodians: Preparing activity:
Army - CR                        Army - CR
Navy - EC                        Navy - AS, MC, OS, SH
Air Force - 11                   Air Force - 19, 99
DLA - CC                        Agent:

Review activities:
Navy - AS, MC, OS, SH (Project 5910-2006-049)
Air Force - 19, 99

NOTE: The activities listed above were interested in this document as of the date of this document. Since organizations and responsibilities can change, you should verify the currency of the information above using the ASSIST Online database at http://assist.daps.dla.mil.