

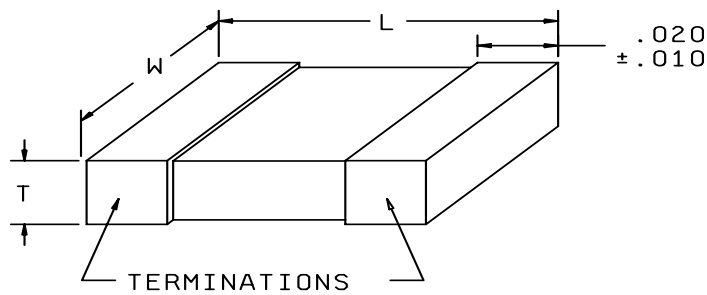
INCH-POUND  
 MIL-PRF-55681/2D  
 11 October 2006  
 SUPERSEDING  
 MIL-PRF-55681/2C  
 3 July 1997

PERFORMANCE SPECIFICATION SHEET

CAPACITORS, CHIP, MULTIPLE LAYER, FIXED, CERAMIC DIELECTRIC,  
 ESTABLISHED RELIABILITY AND NON-ESTABLISHED RELIABILITY,  
 STYLE CDR05

This specification is approved for use by all Departments and Agencies  
 of the Department of Defense.

The requirements for acquiring the product described herein  
 shall consist of this specification sheet and [MIL-PRF-55681](#).



Inches	mm
.010	0.25
.015	0.38
.020	0.51
.080	2.03
.180	4.57
.250	6.35

Dimensions			
L +.020 -.015	W +.020 -.015	T	
		Min	Max
.180	.250	.020	.080

NOTES:

1. Dimensions are in inches.
2. Metric equivalents are given for information only.
3. Dimensions and tolerances are for bare chips. For solder coated terminations, add .025 inch (0.64 mm) to the positive length tolerance and .015 inch (0.38 mm) to the positive width and thickness tolerances.

FIGURE 1. Dimensions and configuration.

MIL-PRF-55681/2D

REQUIREMENTS:

Dimensions and configuration: See [figure 1](#).

Capacitance value: See [table I](#).

Capacitance tolerance: J = ±5 percent; K = ±10 percent; M = ±20 percent. See [table I](#).

Rated voltage: See [table I](#).

Product level designator: Non-established Reliability - C; Established Reliability - M, P, R, and S.

Operating temperature range: -55°C to +125°C.

Marking: In accordance with [MIL-PRF-55681](#).

TABLE I. Capacitor characteristics.

Part or Identifying Number (PIN) <sup>1/</sup>	Capacitance (pF)	Capacitance tolerance	Rated temperature and voltage temperature limits	Rated voltage (V dc)
CDR05BP392B - - -	3,900	J, K	BP	100
CDR05BP472B - - -	4,700	J, K	BP	100
CDR05BP562B - - -	5,600	J, K	BP	100
CDR05BX683B - - -	68,000	K, M	BX	100
CDR05BX823BK - -	82,000	K	BX	100
CDR05BX104B - - -	100,000	K, M	BX	100
CDR05BX124BK - -	120,000	K	BX	100
CDR05BX154B - - -	150,000	K, M	BX	100
CDR05BX224A - - -	220,000	K, M	BX	50
CDR05BX274AK - -	270,000	K	BX	50
CDR05BX334A - - -	330,000	K, M	BX	50

<sup>1/</sup> Complete PIN shall include additional symbols to indicate capacitance tolerance (where applicable), termination finish, and product level.

CHANGES FROM PREVIOUS ISSUE: The margins of this specification sheet are marked with asterisks to indicate where changes from the previous issue were made. This was done as a convenience only and the Government assumes no liability whatsoever for any inaccuracies in these notations. Bidders and contractors are cautioned to evaluate the requirements of this document based on the entire content irrespective of the marginal notations and relationship to the last previous issue.

Custodians:

Army - CR  
Navy - EC  
Air Force - 11  
DLA - CC

Review activities:

Navy - AS, MC, OS, SH  
Air Force - 19, 99

Preparing activity:

Army - CR

Agent:

DLA - CC

(Project 5910-2006-048)

- \* NOTE: The activities listed above were interested in this document as of the date of this document. Since organizations and responsibilities can change, you should verify the currency of the information above using the ASSIST Online database at <http://assist.daps.dla.mil>.