

The documentation and process conversion measures necessary to comply with this revision shall be completed by 19 February 2014.

INCH-POUND

MIL-PRF-19500/722C  
 19 November 2013  
 SUPERSEDING  
 MIL-PRF-19500/722B  
 28 February 2011

PERFORMANCE SPECIFICATION SHEET

SEMICONDUCTOR DEVICE, DIODE, SILICON, SCHOTTKY RECTIFIER,  
 TYPES 1N6902UTK3, 1N6903UTK3, 1N6904UTK3, 1N6905UTK3, 1N6902UTK3CS,  
 1N6903UTK3CS, 1N6904UTK3CS, 1N6905UTK3CS, 1N6902UTK3AS,  
 1N6903UTK3AS, 1N6904UTK3AS AND 1N6905UTK3AS,  
 JANTX, JANTXV, AND JANS

This specification is approved for use by all Departments and Agencies of the Department of Defense.

The requirements for acquiring the product described herein shall consist of this specification sheet and [MIL-PRF-19500](#).

1. SCOPE

1.1 Scope. This specification covers the performance requirements for silicon, Schottky power surface mount rectifier diodes in a low profile package. Three levels of product assurance are provided for each device type as specified in [MIL-PRF-19500](#).

1.2 Physical dimensions. See [figure 1](#).

1.3 Maximum ratings. Unless otherwise specified  $T_C = +25^\circ\text{C}$ .

Column 1	Column 2	Column 3	Column 4	Column 5	Column 6	Column 7
Types	$V_{RWM}$	$I_O$ $T_C = +100^\circ\text{C}$	$I_{FSM}$ $t_p = 8.3 \text{ ms}$ $T_C = +25^\circ\text{C}$	$R_{\theta JC}$ (junction to cathode side)	$R_{\theta JC}$ (junction to anode side)	$T_{STG}$ and $T_J$
	V dc	A dc	A (pk)	$^\circ\text{C/W}$	$^\circ\text{C/W}$	$^\circ\text{C}$
1N6902UTK3, CS, AS	45	150	2000	.25	.35	-65 to +175
1N6903UTK3, CS, AS	60	150	2000	.25	.35	
1N6904UTK3, CS, AS	80	150	2000	.25	.35	
1N6905UTK3, CS, AS	100	150	2000	.25	.35	

\* Comments, suggestions, or questions on this document should be addressed to DLA Land and Maritime, ATTN: VAC, P.O. Box 3990, Columbus, OH 43218-3990, or emailed to [Semiconductor@dla.mil](mailto:Semiconductor@dla.mil). Since contact information can change, you may want to verify the currency of this address information using the ASSIST Online database at <https://assist.dla.mil/>.

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1.4 Primary electrical characteristics. Unless otherwise specified,  $T_C = +25^\circ\text{C}$ .

Column 1	Column 2		Column 3	Column 4		Column 5
Types	$V_F$ Maximum forward voltage at $T_C = +25^\circ\text{C}$ V dc at $I_F$		$V_F$ Maximum forward voltage $T_C = +125^\circ\text{C}$ V dc at $I_F$	$I_R$ Maximum reverse current (see column 2) mA at $V_{RWM}$		$C_J$ Maximum junction capacitance $f = 1\text{MHz}$ $V_R = 5\text{VDC}$
	75A	150A	150A	$T_J = +25^\circ\text{C}$	$T_J = +125^\circ\text{C}$	pF
1N6902UTK3, CS, AS	.65	.78	.73	5.0	15	6,300
1N6903UTK3, CS, AS	.71	.82	.74	5.0	35	5,500
1N6904UTK3, CS, AS	.79	.88	.76	5.0	35	5,000
1N6905UTK3, CS, AS	.87	.94	.78	5.0	35	4,500

2. APPLICABLE DOCUMENTS

2.1 General. The documents listed in this section are specified in sections 3, 4, or 5 of this specification. This section does not include documents cited in other sections of this specification or recommended for additional information or as examples. While every effort has been made to ensure the completeness of this list, document users are cautioned that they must meet all specified requirements of documents cited in sections 3, 4, or 5 of this specification, whether or not they are listed.

2.2 Government documents.

2.2.1 Specifications, standards, and handbooks. The following specifications, standards, and handbooks form a part of this document to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATIONS

[MIL-PRF-19500](#) - Semiconductor Devices, General Specification for.

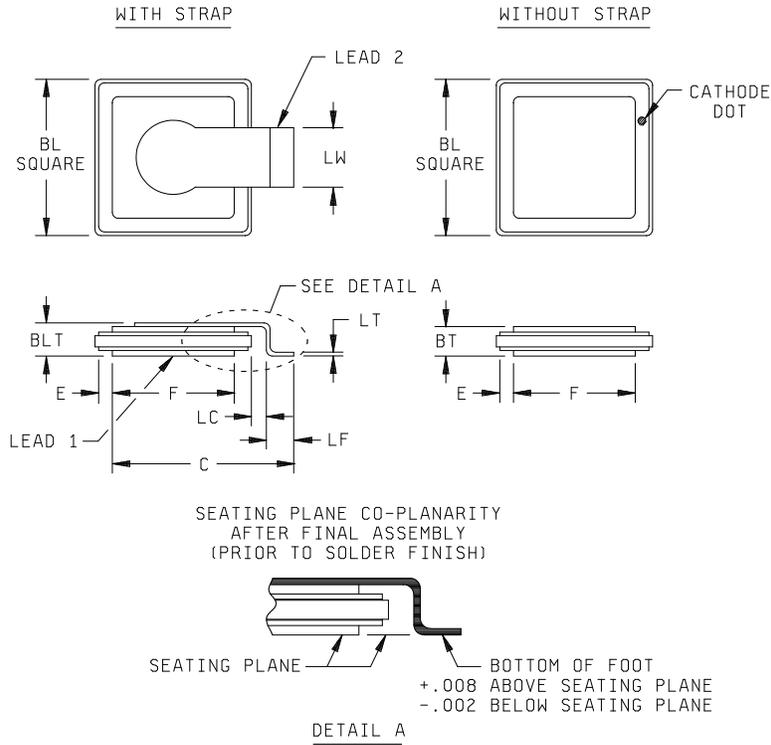
DEPARTMENT OF DEFENSE STANDARDS

[MIL-STD-750](#) - Test Methods for Semiconductor Devices.

\* (Copies of these documents are available online at <http://quicksearch.dla.mil/> or <https://assist.dla.mil/> or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.3 Order of precedence. Unless otherwise noted herein or in the contract, in the event of a conflict between the text of this document and the references cited herein, the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

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Ltr	Dimensions			
	Inches		Millimeters	
	Min	Max	Min	Max
BL	.420	.440	10.67	11.18
BLT		.125		3.18
BT		.115		2.92
C	.469	.509	11.91	12.93
E	.038 NOM		.97 NOM	
F	.331	.341	8.41	8.66
LC	.040 NOM		1.02 NOM	
LF	.055	.075	1.40	1.91
LT	.005	.015	.127	.381
LW	.185	.215	4.70	5.46

NOTES:

1. Dimensions are in inches.
2. Millimeters are given for general information only.
3. In accordance with ASME Y14.5M, diameters are equivalent to  $\phi$ x symbology.
4. For anode, cathode, and strap connections, see 3.4.1 and 3.4.3.

FIGURE 1. Dimensions and configuration.

### 3. REQUIREMENTS

3.1 General. The individual item requirements shall be as specified in [MIL-PRF-19500](#) and as modified herein.

3.2 Qualification. Devices furnished under this specification shall be products that are manufactured by a manufacturer authorized by the qualifying activity for listing on the applicable qualified manufacturer's list (QML) before contract award (see [4.2](#) and [6.3](#)).

3.3 Abbreviations, symbols, and definitions. Abbreviations, symbols, and definitions used herein shall be as specified in [MIL-PRF-19500](#).

3.4 Interface and physical dimensions. The interface and physical dimensions shall be as specified in [MIL-PRF-19500](#), and on [figure 1](#) herein.

3.4.1 Diode construction. These devices shall be constructed utilizing double plug construction with eutectic bonding between both sides of the silicon die and terminal pins (see [MIL-PRF-19500](#)). Metallurgical bond shall be in accordance with the requirements of category II in [MIL-PRF-19500](#). The diode body is ceramic. All seals are eutectic solder. Strap material is a copper alloy or copper sandwich. The 1N6902UTK3, 1N6903UTK3, 1N6904UTK3, and 1N6905UTK3 have no strap. The strap connects to the cathode on 1N6902UTK3CS, 1N6903UTK3CS, 1N6904UTK3CS, and 1N6905UTK3CS and to the anode on 1N6902UTK3AS, 1N6903UTK3AS, 1N6904UTK3AS, and 1N6905UTK3AS.

3.4.2 Lead formation and finish. Unless otherwise specified, lead finish (pads, bottom pad and strap foot) shall be solderable in accordance with [MIL-PRF-19500](#), [MIL-STD-750](#) and herein. Where a choice of lead finish or formation is desired, it shall be specified in the acquisition document (see [6.2](#)).

3.4.3 Polarity. Polarity shall be marked with the appropriate diode symbol on the strap or with a dot on the cathode side of the seal ring on "no strap" devices (see [figure 1](#)).

3.5 Marking. Marking shall be in accordance with [MIL-PRF-19500](#) and herein. All marking may be omitted from the device except for the polarity marking. When present, part number may be abbreviated (ex: JS6902 for JANS1N6902TK3). All marking that is omitted from the body of the device shall appear on the label of the initial container.

3.6 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in [1.3](#), [1.4](#) and [table I](#).

3.7 Electrical test requirements. The electrical test requirements shall be the subgroups specified in [table I](#) herein.

3.8 Workmanship. Semiconductor devices shall be processed in such a manner as to be uniform in quality and shall be free from other defects that will affect life, serviceability, or appearance.

### 4. VERIFICATION

4.1 Classification of inspections. The inspection requirements specified herein are classified as follows:

- a. Qualification inspection (see [4.2](#)).
- b. Screening (see [4.3](#)).
- c. Conformance inspection (see [4.4](#) and [tables I, II, and III](#)).

4.2 Qualification inspection. Qualification inspection shall be in accordance with [MIL-PRF-19500](#) and as specified herein.

4.2.1 Construction verification. Cross sectional photos from three devices shall be submitted in the qualification report.

4.2.2 Group E qualification. Group E inspection shall be performed for qualification or re-qualification only. In case qualification was awarded to a prior revision of the specification sheet that did not request the performance of [table II](#) tests, the tests specified in [table II](#) herein that were not performed in the prior revision shall be performed on the first inspection lot of this revision to maintain qualification.

4.3 Screening. Screening shall be in accordance with table IV of [MIL-PRF-19500](#), and as specified herein. The following measurements shall be made in accordance with [table I](#) herein. Devices that exceed the limits of [table I](#) herein shall not be acceptable.

Screen (see table IV of <a href="#">MIL-PRF-19500</a> )	Measurement	
	JANS level	JANTX and JANTXV levels
(1) 3b	Condition A, one pulse, $I_O = 0$ , $V_{RWM} = 0$ , $I_{FSM} =$ see <a href="#">1.3</a> , column 4 herein.	Condition A, one pulse, $I_O = 0$ , $V_{RWM} = 0$ , $I_{FSM} =$ see <a href="#">1.3</a> , column 4 herein.
3c	Thermal impedance (see <a href="#">4.3.1</a> )	Thermal impedance (see <a href="#">4.3.1</a> )
4	Not applicable	Not applicable
5	Not applicable	Not applicable
7b	Optional	Optional
8	Serialization required.	Not applicable
9	Not applicable	Not applicable
10	Not applicable	Not applicable
11	$V_{F1}$ and $I_{R1}$	$V_{F1}$ and $I_{R1}$
12	Method 1038 of <a href="#">MIL-STD-750</a> , test condition A; $T_C = 150^\circ\text{C}$ , $t = 240$ hours, $V_{RM} = 80$ percent of rated $V_{RWM} =$ see <a href="#">1.3</a> , column 2 herein $V$ (pk), $I_O = 0$ , $f = 60$ Hz; alternate test: $V_{RM} = 80$ percent of rated $V_{RWM}$ (see <a href="#">1.3</a> , column 2 herein for $V_{RWM}$ ), $T_C = 150^\circ\text{C}$	Method 1038 of <a href="#">MIL-STD-750</a> , test condition A; $T_C = 150^\circ\text{C}$ , $t = 48$ hours, $V_{RM} = 80$ percent of rated $V_{RWM} =$ see <a href="#">1.3</a> , column 2 herein $V$ (pk), $I_O = 0$ , $f = 60$ Hz; alternate test: $V_{RM} = 80$ percent of rated $V_{RWM}$ (see <a href="#">1.3</a> , column 2 herein for $V_{RWM}$ ), $T_C = 150^\circ\text{C}$
13	Subgroup 2, of <a href="#">table I</a> herein, and subgroup 3 of <a href="#">table I</a> herein, $V_{F1}$ and $I_{R1}$ ; $\Delta V_{F1} = \pm 50$ mV (pk); $\Delta I_{R1} = \pm 1$ mA dc or 100 percent from the initial value, whichever is greater. Scope display evaluation (see <a href="#">table I</a> , subgroup 7)	Subgroup 2, of <a href="#">table I</a> herein excluding thermal impedance; $V_{F1}$ and $I_{R1}$ ; $\Delta V_{F1} = \pm 50$ mV (pk); $\Delta I_{R1} = \pm 1$ mA dc or 100 percent from the initial value, whichever is greater. Scope display evaluation (see <a href="#">table I</a> , subgroup 7)
14b	Required	Required
15	Not applicable	Not applicable

(1) Surge shall precede thermal impedance. These tests shall be performed anytime after screen 3a and before screen 10.

4.3.1 Thermal impedance. The thermal impedance measurements shall be performed in accordance with method 3101 of [MIL-STD-750](#). See table E-IX, group E, subgroup 4 of [MIL-PRF-19500](#).

4.4 Conformance inspection. Conformance inspection shall be in accordance with [MIL-PRF-19500](#).

4.4.1 Group A inspection. Group A inspection shall be conducted in accordance with table E-V of [MIL-PRF-19500](#) and [table I](#) herein.

4.4.2 Group B inspection. Group B inspection shall be conducted in accordance with the conditions specified for subgroup testing in tables E-VIA (JANS) and E-VIB (JANTX and JANTXV) of [MIL-PRF-19500](#) and 4.4.2.1 and 4.4.2.2 herein. Electrical measurements (end-points) shall be in accordance with table I, subgroup 2 herein. Delta measurements shall be in accordance with table III herein.

4.4.2.1 Group B inspection, table VIA (JANS) of MIL-PRF-19500.

<u>Subgroup</u>	<u>Method</u>	<u>Condition</u>
B4	1037	$\Delta T_J = +85^\circ\text{C}$ minimum, $I_F = 2$ A minimum for 2,000 cycles.
B5	1038	Condition B, $I_F = 1$ A dc minimum, adjust $T_A$ and $I_F$ to achieve $T_J = +175^\circ\text{C}$ , $+0^\circ\text{C}$ , $-35^\circ\text{C}$ , $t = 240$ hours min; (heat sinking allowed).

4.4.2.2 Group B inspection, table VIB (JANTX and JANTXV) of MIL-PRF-19500.

<u>Subgroup</u>	<u>Method</u>	<u>Condition</u>
B3	1037	$\Delta T_J = +85^\circ\text{C}$ minimum, $I_F = 2$ A minimum for 2,000 cycles.

4.4.3 Group C inspection. Group C inspection shall be conducted in accordance with the conditions specified for subgroup testing in table VII of MIL-PRF-19500. Electrical measurements (end-points) shall be in accordance with table I, subgroup 2 herein. Delta measurements shall be in accordance with table III herein.

<u>Subgroup</u>	<u>Method</u>	<u>Condition</u>
C5	3101	$I_H = 20$ A to 50 A, $I_M = 10$ mA to 250 mA, $t_H =$ thermal equilibrium; $t_{MD} = 200$ $\mu\text{s}$ maximum.
C6	1037	$\Delta T_J = +85^\circ\text{C}$ minimum, $I_F = 2$ A minimum for 6,000 cycles.

4.4.4 Group E inspection. Group E inspection shall be conducted in accordance with the conditions specified for subgroup testing in table IX of MIL-PRF-19500 and table II herein. Electrical measurements (end-points) shall be in accordance with table I, subgroup 2 herein. Delta measurements shall be in accordance with table III herein.

4.5 Methods of inspection. Methods of inspection shall be as specified in the appropriate tables as follows.

4.5.1 Pulse measurements. Conditions for pulse measurement shall be as specified in section 4 of MIL-STD-750.

4.5.2. Avalanche energy test. The Schottky rectifier shall be capable of absorbing the reverse energy of 350 rectangular pulse,  $t_p = 1$   $\mu\text{sec}$ ,  $I_R = 2$  A at 1 kHz. This will be performed during wafer or die level prior to device assembly.

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TABLE I. Group A inspection.

Inspection <u>1/</u>	MIL-STD-750		Symbol	Limits		Unit
	Method	Conditions		Min	Max	
<u>Subgroup 1</u>						
Visual and mechanical examination	2071					
<u>Subgroup 2</u>						
Thermal impedance <u>2/</u>	3101	$I_M = 10 \text{ mA to } 250 \text{ mA}$ , $I_H = 50 \text{ A}$ , $t_H = 2 \text{ ms}$ , $t_{md} = 200 \text{ } \mu\text{s}$ maximum	$Z_{\theta JX}$			
1N6902UTK3, CS, AS				.12	°C/W	
1N6903UTK3, CS, AS				.12	°C/W	
1N6904UTK3, CS, AS				.12	°C/W	
1N6905UTK3, CS, AS				.12	°C/W	
Forward voltage	4011	Pulsed test (see 4.5.1) $t_p = 300 \text{ } \mu\text{s}$	$V_{F1}$			
1N6902UTK3, CS, AS		$I_F = 75 \text{ A (pk)}$		.65	V	
1N6903UTK3, CS, AS		$I_F = 75 \text{ A (pk)}$		.71	V	
1N6904UTK3, CS, AS		$I_F = 75 \text{ A (pk)}$		.79	V	
1N6905UTK3, CS, AS		$I_F = 75 \text{ A (pk)}$		.87	V	
Forward voltage	4011	Pulsed test (see 4.5.1) $t_p = 300 \text{ } \mu\text{s}$	$V_{F2}$			
1N6902UTK3, CS, AS		$I_F = 150 \text{ A (pk)}$		.78	V	
1N6903UTK3, CS, AS		$I_F = 150 \text{ A (pk)}$		.82	V	
1N6904UTK3, CS, AS		$I_F = 150 \text{ A (pk)}$		.88	V	
1N6905UTK3, CS, AS		$I_F = 150 \text{ A (pk)}$		.94	V	
Reverse current leakage	4016	DC method	$I_{R1}$			
1N6902UTK3, CS, AS		$V_R = 45 \text{ V}$		5.0	mA	
1N6903UTK3, CS, AS		$V_R = 60 \text{ V}$		5.0	mA	
1N6904UTK3, CS, AS		$V_R = 80 \text{ V}$		5.0	mA	
1N6905UTK3, CS, AS		$V_R = 100 \text{ V}$		5.0	mA	
Breakdown voltage	4021	Pulsed test (see 4.5.1) $t_p = 35 \text{ ms}$	$V_{BR1}$			
1N6902UTK3, CS, AS		$I_R = 5.0 \text{ mA (pk)}$		50	V	
1N6903UTK3, CS, AS		$I_R = 5.0 \text{ mA (pk)}$		66	V	
1N6904UTK3, CS, AS		$I_R = 10 \text{ mA (pk)}$		88	V	
1N6905UTK3, CS, AS		$I_R = 10 \text{ mA (pk)}$		110	V	

See footnotes at end of table.

TABLE I. Group A inspection – Continued.

Inspection <sup>1/</sup>	MIL-STD-750		Symbol	Limits		Unit
	Method	Conditions		Min	Max	
<u>Subgroup 3</u>						
High temperature operation:		$T_C = +125^\circ\text{C}$				
Forward voltage	4011	Pulsed test (see 4.5.1) $t_p = 300 \mu\text{s}$	$V_{F3}$			
1N6902UTK3, CS, AS		$I_F = 150 \text{ A (pk)}$			.73	V
1N6903UTK3, CS, AS		$I_F = 150 \text{ A (pk)}$			.74	V
1N6904UTK3, CS, AS		$I_F = 150 \text{ A (pk)}$			.76	V
1N6905UTK3, CS, AS		$I_F = 150 \text{ A (pk)}$			.78	V
Reverse current leakage	4016	DC method;				
1N6902UTK3, CS, AS		$V_R = 45 \text{ V}$			15	mA
1N6903UTK3, CS, AS		$V_R = 60 \text{ V}$			35	mA
1N6904UTK3, CS, AS		$V_R = 80 \text{ V}$			35	mA
1N6905UTK3, CS, AS		$V_R = 100 \text{ V}$	$I_{R2}$		35	mA
Low temperature operation:		$T_C = -55^\circ\text{C}$				
Breakdown voltage	4021	Pulsed test (see 4.5.1) $t_p = 35 \text{ ms}$	$V_{BR2}$			
1N6902UTK3, CS, AS		$I_R = 5.0 \text{ mA (pk)}$		45		V
1N6903UTK3, CS, AS		$I_R = 5.0 \text{ mA (pk)}$		60		V
1N6904UTK3, CS, AS		$I_R = 10 \text{ mA (pk)}$		80		V
1N6905UTK3, CS, AS		$I_R = 10 \text{ mA (pk)}$		100		V

See footnotes at end of table.

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TABLE I. Group A inspection – Continued.

Inspection 1/	MIL-STD-750		Symbol	Limits		Unit
	Method	Conditions		Min	Max	
<u>Subgroup 4</u> Junction capacitance  1N6902UTK3, CS, AS 1N6903UTK3, CS, AS 1N6904UTK3, CS, AS 1N6905UTK3, CS, AS	4001	$V_R = 5 \text{ V dc}$ , $f = 1 \text{ MHz}$ , $V_{SIG} = 50 \text{ mV (p-p)}$	$C_J$		6,300 5,500 5,000 4,500	pF pF pF pF
<u>Subgroup 5</u> Not applicable						
<u>Subgroup 6</u> Surge  Electrical measurements	4066	$I_{FSM}$ = see 1.3, column 4, ten surges of 8.3 ms each at 1 minute intervals. Condition A: $T_C = 25^\circ\text{C}$  See table I, subgroup 2 herein				
<u>Subgroup 7</u> Scope display evaluation  Electrical measurements	4023	$\underline{3/}$  See table I, subgroup 2 herein				

1/ For sampling plan, see MIL-PRF-19500.

2/ This test required for the following end-point measurements only:

Group B, subgroups 3, 4, and 5 (JANS).

Group B, subgroups 2 and 3 (JANTX and JANTXV).

Group C, subgroups 2 and 6.

Group E, subgroup 1.

3/ The reverse breakdown characteristics shall be viewed on an oscilloscope with display calibration factors of 2-mA/division and 10 to 20 V/division. Reverse current over the knee shall be at least 10 mA. Each device may exhibit a slightly rounded characteristic and any discontinuity or dynamic instability of the trace shall be cause for rejection.

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\* TABLE II. Group E inspection (all quality levels) – for qualification and requalification only.

Inspection	MIL-STD-750		Qualification
	Method	Conditions	
<u>Subgroup 1</u>	1056		n = 45, c = 0
Thermal shock (liquid to liquid)		Test condition A, 0°C to +100°C	
* Temperature cycling (air to air)	1051	Test condition G, 500 cycles	
Hermetic seal	1071		
Electrical measurements		See <a href="#">table I</a> , subgroup 2 and <a href="#">table III</a> herein	
<u>Subgroup 2</u>			n = 45, c = 0
Life test	1038	Condition A, t = 1,000 hours, V <sub>R</sub> = 80 percent V <sub>rated</sub> (see <a href="#">1.3</a> , column 2 herein)	
Electrical measurements		See <a href="#">table I</a> , subgroup 2 and <a href="#">table III</a> herein	
<u>Subgroup 4</u>			
Thermal impedance curves		See <a href="#">MIL-PRF-19500</a>	
<u>Subgroup 5</u>			
Not applicable			
<u>Subgroup 6</u>	1020		n = 11, c = 0
ESD			
<u>Subgroup 7</u>			n = 22, c = 0
Forward surge	4066	I <sub>FSM</sub> = see <a href="#">1.3</a> , 110 percent of column 4; ten surges of 8.3 ms each at 1 minute intervals, condition A: T <sub>C</sub> = 25°C	
Electrical measurements		See <a href="#">table I</a> , subgroup 2 except for thermal impedance	

TABLE III. Groups B, C, and E delta requirements. 1/ 2/ 3/ 4/ 5/

Step	Inspection 1/	MIL-STD-750		Symbol	Limit		Unit
		Method	Conditions		Min	Max	
1.	Forward voltage	4011		$\Delta V_{F1}$	$\pm 50$ mV		
2.	Reverse current leakage	4016		$\Delta I_{R1}$	$\pm 1$ mA or 100 percent of initial value whichever is greater.		

1/ The delta measurements for table VIA (JANS) of MIL-PRF-19500 are as follows: Subgroups 4 and 5, see table III herein, steps 1 and 2.

2/ The delta measurements for table VIB (JANTX and JANTXV) of MIL-PRF-19500 are as follows: Subgroup 3, see table III herein, steps 1 and 2.

3/ The delta measurements for table VII of MIL-PRF-19500 are as follows: Subgroup 6, see table III herein, steps 1 and 2.

4/ The delta measurements for table IX of MIL-PRF-19500 are as follows: Subgroups 1 and 2, see table III herein, steps 1 and 2.

5/ Devices which exceed the table I limits for this test shall not be accepted.

## 5. PACKAGING

5.1 Packaging. For acquisition purposes, the packaging requirements shall be as specified in the contract or order (see 6.2). When packaging of materiel is to be performed by DoD or in-house contractor personnel, these personnel need to contact the responsible packaging activity to ascertain packaging requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activities within the Military Service or Defense Agency, or within the Military Service's system commands. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

## 6. NOTES

(This section contains information of a general or explanatory nature that may be helpful, but is not mandatory. The notes specified in MIL-PRF-19500 are applicable to this specification.)

6.1 Intended use. Semiconductors conforming to this specification are intended for original equipment design applications and logistic support of existing equipment.

6.2 Acquisition requirements. Acquisition documents should specify the following:

- a. Title, number, and date of this specification.
- b. Packaging requirements (see 5.1).
- c. Lead formation and finish (see 3.4.2).
- d. Product assurance level and type designator.

\* 6.3 Qualification. With respect to products requiring qualification, awards will be made only for products which are, at the time of award of contract, qualified for inclusion in Qualified Manufacturers List (QML 19500) whether or not such products have actually been so listed by that date. The attention of the contractors is called to these requirements, and manufacturers are urged to arrange to have the products that they propose to offer to the Federal Government tested for qualification in order that they may be eligible to be awarded contracts or orders for the products covered by this specification. Information pertaining to qualification of products may be obtained from DLA Land and Maritime, ATTN: VQE, P.O. Box 3990, Columbus, OH 43218-3990 or e-mail [vqe.chief@dla.mil](mailto:vqe.chief@dla.mil). An online listing of products qualified to this specification may be found in the Qualified Products Database (QPD) at <https://assist.dla.mil>.

6.4 Changes from previous issue. The margins of this specification are marked with asterisks to indicate where changes from the previous issue were made. This was done as a convenience only and the Government assumes no liability whatsoever for any inaccuracies in these notations. Bidders and contractors are cautioned to evaluate the requirements of this document based on the entire content irrespective of the marginal notations and relationship to the last previous issue.

Custodians:  
Army - CR  
Navy - EC  
Air Force - 85  
NASA - NA  
DLA - CC

Preparing activity:  
DLA - CC  
  
(Project 5961-2013-097)

\* NOTE: The activities listed above were interested in this document as of the date of this document. Since organizations and responsibilities can change, you should verify the currency of the information above using the ASSIST Online database at <https://assist.dla.mil/>.