MILITARY SPECIFICATION
SEMICONDUCTOR DEVICE, TRANSISTOR, SILICON
TYPE 2N3904

This amendment forms a part of Military Specification
MIL-S-19500/529 (ER), dated 10 May 1978.

PAGE 1

1.3. Maximum Rating table, delete and substitute:

<table>
<thead>
<tr>
<th>P_T</th>
<th>R_0JC</th>
<th>V_CB</th>
<th>V_CE</th>
<th>V_EB</th>
<th>I_C</th>
<th>T_sub and T_STG</th>
</tr>
</thead>
<tbody>
<tr>
<td>350</td>
<td>125</td>
<td>Vdc</td>
<td>Vdc</td>
<td>Vdc</td>
<td>6Ac</td>
<td>-55 to +150°C</td>
</tr>
</tbody>
</table>

1/ Derate linearly, 8.0 mW/°C for T_A > 25°C.

1.4. Primary Electrical Characteristics table: Delete "MAX" and substitute "MIN"; also delete "MIN" and substitute "MAX".

PAGE 2

3.5a.: Delete "or" and substitute "and".

PAGE 12

Figure 1: Delete "C" and substitute "E"; also delete "E" and substitute "C".

Preparing activity:
Army - ER

Agent:
DLA - ES
(Project 5961-A759)