

MILITARY SPECIFICATION

SEMICONDUCTOR DEVICES, THYRISTOR (CONTROLLED RECTIFIER), SILICON
 TYPES 2N3091, 2N3093, 2N3095, 2N3097,
 2N3098, 2N3099, 2N3101, 2N3103
 2N3105, and 2N3106

This amendment forms a part of Military Specification MIL-S-19500/280A(EC), dated 6 January 1978, and is approved for use by the Naval Electronic Systems Command, Department of the Navy and is available for use by all Departments and Agencies of the Department of Defense.

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* 1.2, Characteristic, Holding current, Max column: Delete "40" and substitute "60".

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* TABLE 1, Holding current, Max limits column: Delete "40" and substitute "60".

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TABLE II, subgroup 1: After the "Forward blocking current" test, add the following new end points.

Examination or test	MIL-STD-750		LTPD	Symbol	Limits		Unit
	Method	Details			Min	Max	
Gate trigger voltage and gate trigger current	4221	V ₂ = 6.0 V dc R _L = 10 ohms R _F = 20 ohms		V _{GT}	---	3.6	V dc
				IGT	5	70.0	mA dc
On state voltage	4226	I _{TM} = 220 A dc pulse width = 8.5 ms duty cycle - 2 max		V _{TM}		1.7	V (pk)

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TABLE II, subgroup 4: Delete "Gate trigger voltage and gate trigger current" and "On state voltage" tests and all accompanying data.

NOTE: The margin of this amendment is marked with asterisks to indicate where changes (additions, modifications, corrections, deletions) from the previous amendment were made. This was done as a convenience only and the Government assumes no liability whatsoever for any inaccuracies in these notations. Bidders and contractors are cautioned to evaluate the requirements of this document based on the entire content irrespective of the marginal notations and relationship to the last previous amendment.

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