

INCH-POUND

MIL-M-38510/605B  
AMENDMENT 1  
16 August 1993

MILITARY SPECIFICATION

MICROCIRCUITS, DIGITAL, CMOS  
SEMICUSTOM (GATE ARRAY) DEVICES, MONOLITHIC SILICON

This amendment forms a part of MIL-M-38510/605B,  
dated 21 January 1989, and is approved for use by all  
Departments and Agencies of the Department of Defense.

PAGE 2

1.2.3, add the following new case outline:

"5            D-10 (28-pin, dual-in-line package)            B"

PAGE 9

TABLE I, input capacitance,  $C_{IN}$  maximum limits column: Delete "10" and substitute "8".

PAGE 10

TABLE I, add footnote 8/ as follows:

"8/ Due to the large number of die/package combinations and subsequent effect on input capacitance, the limit for this parameter shall be specified in the Altered Item Drawing (AID).

CONCLUDING MATERIAL

Custodians:

Army - ER  
Navy - EC  
Air Force - 17

Review activities:

Army - AR, MI  
Navy - OS, SH, TD  
Air Force - 19, 85, 99  
DLA - ES

User activities:

Army - SM  
Navy - AS, CG, MC

Preparing activity:

Air Force - 17

Agent:

DLA - ES

(Project 5962-1354)