

NOTICE OF
VALIDATION

INCH-POUND

MIL-M-38510/250
NOTICE 4
30 November 2010

DETAIL SPECIFICATION

**Microcircuits, Digital, CMOS, 512 x 9 Bit, First In - First Out
Dual Port Memory (FIFO), Monolithic Silicon**

MIL-M-38510/250, dated 06 February 2006, remains inactive for new design; however, the document is valid for use.

Custodians:

Army - CR
Navy - EC
Air Force - 85
DLA - CC

Preparing Activity:

DLA - CC

Reviewer Activities:

Army - AR, MI, SM
Navy - AS, CG, SH, TD
Air Force - 03, 19, 99

NOTE: The activities above were interested in this document as of the date of this document. Since organizations and responsibilities can change, you should verify the currency of the information above using the ASSIST Online database at <https://assist.daps.dla.mil>.