

INCH-POUND
MIL-M-38510/156C
28 MARCH 2005
SUPERSEDING
MIL-M-38510/156B
21 JULY 1986

MILITARY SPECIFICATION  
MICROCIRCUITS, DIGITAL, TTL, DATA ENCODERS, MONOLITHIC SILICON

Inactive for new design after 21 July 1986

This specification is approved for use by all Departments and Agencies of the Department of Defense.

The requirements for acquiring the product herein shall consist of this specification sheet and MIL-PRF-38535

1. SCOPE

1.1 Scope. This specification covers the detail requirements for monolithic silicon, TTL, data encoder, microcircuits. Two product assurance classes and a choice of case outlines and lead finishes are provided and are reflected in the complete part number. For this product, the requirements of MIL-M-38510 have been superseded by MIL-PRF-38535, (see 6.4)

1.2 Part or Identifying Number (PIN). The PIN is in accordance with MIL-PRF-38535, and as specified herein.

1.2.1 Device types. The device types should be as follows:

<u>Device type</u>	<u>Circuit</u>
01	Ten line to four line data encoder, without enable.
02	Eight line to three line data encoder, with enable.
03	Eight-input priority encoder.

1.2.2 Device class. The device class is the product assurance level as defined in MIL-PRF-38535.

1.2.3 Case outlines. The case outlines are as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
E	GDIP1-T16 or CDIP2-T16	16	Dual-in-line
F	GDFP2-F16 or CDFP3-F16	16	Flat pack

Comments, suggestions, or questions on this document should be addressed to: Commander, Defense Supply Center Columbus, ATTN: DSCC-VAS, 3990 East Broad St., Columbus, OH 43218-3990, or email <mailto:bipolar@dsc.dla.mil> . Since contact information can change, you may want to verify the currency of this address information using the ASSIST Online database at <http://assist.daps.dla.mil> .

1.3 Absolute maximum ratings.

Supply voltage range .....	-0.5 V dc to 7.0 V dc
Input voltage range .....	-1.5 V dc at -12 mA to 5.5 V dc
Storage temperature range .....	-65°C to 150°C
Maximum power dissipation per gate ( $P_D$ ) <u>1/</u> :	
Device type 01 .....	385 mW
Device type 02 .....	330 mW
Device type 03 .....	424 mW
Lead temperature (soldering, 10 seconds) .....	+300°C
Thermal resistance, junction-to-case ( $\theta_{JC}$ ):	
Cases E and F .....	See MIL-STD-1835
Junction temperature ( $T_J$ ) .....	+175°C <u>2/</u>

1.4 Recommended operating conditions.

Supply voltage ( $V_{CC}$ ) .....	4.5 V dc minimum to 5.5 V dc maximum
Minimum high level input voltage ( $V_{IH}$ ) .....	2.0 V dc
Maximum low level input voltage ( $V_{IL}$ ) .....	0.8 V dc
Case operating temperature range ( $T_C$ ) .....	-55°C to +125°C

## 2. APPLICABLE DOCUMENTS

2.1 General. The documents listed in this section are specified in sections 3, 4, or 5 of this specification. This section does not include documents cited in other sections of this specification or recommended for additional information or as examples. While every effort has been made to ensure the completeness of this list, document users are cautioned that they must meet all specified requirements of documents cited in sections 3, 4, or 5 of this specification, whether or not they are listed.

2.2 Government documents.

2.2.1 Specifications and Standards. The following specifications and standards form a part of this specification to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

## DEPARTMENT OF DEFENSE SPECIFICATIONS

MIL-PRF-38535 - Integrated Circuits (Microcircuits) Manufacturing, General Specification for.

## DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard for Microelectronics.  
MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

(Copies of these documents are available online at <http://assist.daps.dla.mil/quicksearch/> or <http://assist.daps.dla.mil> or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.

1/ Must withstand the added  $P_D$  due to short circuit test (e.g.  $I_{OS}$ ).

2/ Maximum junction temperature shall not be exceeded except for allowable short duration burn-in screening conditions per method 5004 of MIL-STD-883.

2.3 Order of precedence. In the event of a conflict between the text of this specification and the references cited herein the text of this document shall takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

### 3. REQUIREMENTS

3.1 Qualification. Microcircuits furnished under this specification shall be products that are manufactured by a manufacturer authorized by the qualifying activity for listing on the applicable qualified manufacturers list before contract award (see 4.3 and 6.3).

3.2 Item requirements. The individual item requirements shall be in accordance with MIL-PRF-38535 and as specified herein or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

3.3 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein.

3.3.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.3.2 Truth tables. The truth tables shall be as specified on figure 2.

3.3.3 Logic diagrams. The logic diagrams shall be as specified on figure 3.

3.3.4 Case outlines. The case outlines shall be as specified in 1.2.3.

3.4 Lead material and finish. The lead material and finish shall be in accordance with MIL-PRF-38535 (see 6.6).

3.5 Electrical performance characteristics. The electrical performance characteristics are as specified in table I, and apply over the full recommended case operating temperature range, unless otherwise specified.

3.6 Electrical test requirements. Electrical test requirements for each device class shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table III.

3.7 Marking. Marking shall be in accordance with MIL-PRF-38535.

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T <sub>C</sub> ≤ +125°C	Device type	Limit		Units
				Min	Max	
High level output voltage	V <sub>OH</sub>	V <sub>CC</sub> = 4.5 V, I <sub>OH</sub> = -8mA	All	2.4		V
Low level output voltage	V <sub>OL</sub>	V <sub>CC</sub> = 4.5 V, I <sub>OL</sub> = 16 mA	All		0.4	V
Input clamp voltage	V <sub>IC</sub>	V <sub>CC</sub> = 4.5 V, I <sub>IN</sub> = -12mA	All		-1.5	V
Low level input current	I <sub>IL</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0.4 V	01	-0.7	-1.6	mA
Low level input current at all inputs except 0	I <sub>IL1</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0.4 V	02	-1.2	-3.2	mA
Low level input current at 0	I <sub>IL2</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0.4 V	02	-0.7	-1.6	mA
Low level input current at $\bar{0}$ and $\bar{E}_i$	I <sub>IL1</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0.4 V	03	-0.7	-1.6	mA
Low level input current at all other inputs	I <sub>IL2</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0.4 V	03	-1.4	-3.2	mA
High level input current	I <sub>IH1</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 2.4 V	01		40	μA
	I <sub>IH2</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 5.5 V	01		100	μA
High level input current at all inputs except 0	I <sub>IH1</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 2.4 V	02		80	μA
	I <sub>IH2</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 5.5 V	02		200	μA
High level input current at 0	I <sub>IH3</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 2.4 V	02		40	μA
	I <sub>IH4</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 5.5 V	02		100	μA
High level input current at all inputs except $\bar{0}$	I <sub>IH1</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 2.4 V	03		80	μA
	I <sub>IH2</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 5.5 V	03		200	μA
High level input current at $\bar{0}$	I <sub>IH3</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 2.4 V	03		40	μA
	I <sub>IH4</sub>	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 5.5 V	03		100	μA
Short circuit output current	I <sub>OS</sub>	V <sub>CC</sub> = 5.5 V, V <sub>OUT</sub> = 0 V <u>1/</u>	01,02	-35	-85	mA
			03	-20	-80	mA
High level supply current	I <sub>CCH</sub>	V <sub>CC</sub> = 5.5 V	01	---	62	mA
			02	---	55	mA
Low level supply current	I <sub>CCL</sub>	V <sub>CC</sub> = 5.5 V	01	---	70	mA
			02	---	60	mA
Supply current	I <sub>CC</sub>	V <sub>CC</sub> = 5.5 V	03	---	77	mA
Propagation delay time, high-to-low level; any input to any output (in-phase output)	t <sub>PHL1</sub>	V <sub>CC</sub> = 5.0 V C <sub>L</sub> = 50 pF min R <sub>L</sub> = 390 Ω ± 5% (Figure 4)	01	3	21	ns
Propagation delay time, low- to-high level; any input to any output (in-phase output)	t <sub>PLH1</sub>		01	3	25	ns
Propagation delay time, high-to-low level; any input to any output (out-of-phase output)	t <sub>PHL2</sub>		01	3	26	ns
Propagation delay time, low- to-high level; any input to any output (out-of-phase)	t <sub>PLH2</sub>		01	3	31	ns

See footnote at end of table.

TABLE I. Electrical performance characteristics - Continued

Test	Symbol	Conditions -55°C ≤ T <sub>C</sub> ≤ +125°C	Device type	Limit		Units
				Min	Max	
Propagation delay time, high-to-low level output at GS from 0 thru 7 (in-phase output)	t <sub>PHL1</sub>	V <sub>CC</sub> = 5.0 V C <sub>L</sub> = 50 pF min R <sub>L</sub> = 390 Ω ± 5% (Figure 4)	02	3	30	ns
Propagation delay time, low- to-high level output at GS from 0 thru 7 (in-phase output)	t <sub>PLH1</sub>		02	3	34	ns
Propagation delay time, high-to-low level output at A <sub>0</sub> , A <sub>1</sub> , or A <sub>2</sub> from 0 thru 7 (in-phase output)	t <sub>PHL2</sub>		02	3	25	ns
Propagation delay time, low-to-high level output at A <sub>0</sub> , A <sub>1</sub> , or A <sub>2</sub> from 0 thru 7 (in-phase output)	t <sub>PLH2</sub>		02	3	26	ns
Propagation delay time, high-to-low level output at E <sub>0</sub> from E <sub>1</sub>	t <sub>PHL3</sub>		02	3	25	ns
Propagation delay time, low-to-high level output at E <sub>0</sub> from E <sub>1</sub>	t <sub>PLH3</sub>	V <sub>CC</sub> = 5.0 V C <sub>L</sub> = 50 pF min R <sub>L</sub> = 390 Ω ± 5% (Figure 5)	02	3	24	ns
Propagation delay time, high-to-low level output at E <sub>0</sub> from 0 thru 7	t <sub>PHL4</sub>		02	3	25	ns
Propagation delay time, low-to-high level output at E <sub>0</sub> from 0 thru 7	t <sub>PLH4</sub>		02	3	20	ns
Propagation delay time, high-to-low level output at A <sub>0</sub> , A <sub>1</sub> , or A <sub>2</sub> from 0 thru 7 (out-of-phase output)	t <sub>PHL5</sub>		02	3	26	ns
Propagation delay time, low-to-high level output at A <sub>0</sub> , A <sub>1</sub> , or A <sub>2</sub> from 0 thru 7 (out-of-phase output)	t <sub>PLH5</sub>		02	3	26	ns

See footnote at end of table.

TABLE I. Electrical performance characteristics – Continued

Test	Symbol	Conditions -55°C ≤ T <sub>C</sub> ≤ +125°C	Device type	Limit		Units
				Min	Max	
Propagation delay time, low-to-high level; data input to enable output	t <sub>PLH1</sub>	V <sub>CC</sub> = 5.0 V C <sub>L</sub> = 50 pF min R <sub>L</sub> = 390 Ω ± 5% (Figure 6)	03	6	22	ns
Propagation delay time, high-to-low level; data input to enable output	t <sub>PHL1</sub>		03	9	43	ns
Propagation delay time, low-to-high level; enable input to group signal	t <sub>PLH2</sub>		03	9	32	ns
Propagation delay time, high-to-low level; enable input to group signal	t <sub>PHL2</sub>		03	9	38	ns
Propagation delay time, low-to-high level; enable input to enable output	t <sub>PLH3</sub>		03	9	32	ns
Propagation delay time, high-to-low level; enable input to enable output	t <sub>PHL3</sub>		03	9	48	ns
Propagation delay time, low-to-high level; enable input to data output	t <sub>PLH4</sub>		03	9	37	ns
Propagation delay time, high-to-low level; enable input to data output	t <sub>PHL4</sub>		03	9	38	ns
Propagation delay time, low-to-high level; data input to group signal	t <sub>PLH5</sub>		03	15	56	ns
Propagation delay time, high-to-low level; data input to group signal	t <sub>PHL5</sub>		03	9	34	ns
Propagation delay time, low-to-high level; data input to data output	t <sub>PLH6</sub>		03	12	52	ns
Propagation delay time, high-to-low level; data input to data output	t <sub>PHL6</sub>		03	9	52	ns

1/ Not more than one output should be shorted at one time.

TABLE II. Electrical test requirements.

MIL-PRF-38535 test requirements	Subgroups (see table III)	
	Class S devices	Class B devices
Interim electrical parameters	1	1
Final electrical test parameters	1*, 2, 3, 7, 9, 10, 11	1*, 2, 3, 7, 9
Group A test requirements	1, 2, 3, 7, 8, 9, 10, 11	1, 2, 3, 7, 8, 9, 10, 11
Group B electrical test parameters when using the method 5005 QCI option	1,2,3,7,8,9, 10,11	N/A
Group C end-point electrical parameters	1,2,3,7,8,9, 10,11	1, 2, 3
Group D end-point electrical parameters	1, 2, 3	1, 2, 3

\*PDA applies to subgroup 1.

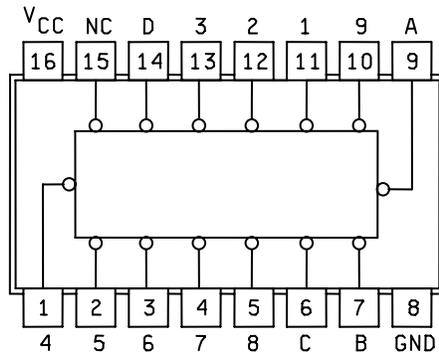
#### 4. VERIFICATION.

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not effect the form, fit, or function as described herein.

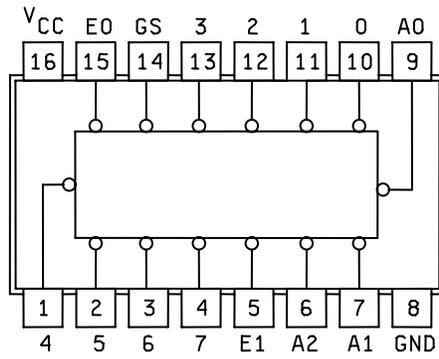
4.2 Screening. Screening shall be in accordance with MIL-PRF-38535, and shall be conducted on all devices prior to qualification and quality conformance inspection. The following additional criteria shall apply:

- a. The burn-in test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.
- b. Interim and final electrical test parameters shall be as specified in table II, except interim electrical parameters test prior to burn-in is optional at the discretion of the manufacturer.
- c. Additional screening for space level product shall be as specified in MIL-PRF-38535.

DEVICE TYPE 01



DEVICE TYPE 02



DEVICE TYPE 03

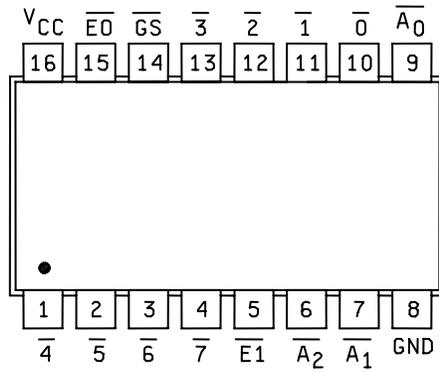


Figure 1. Terminal connections (top view).

Device type 01

Inputs									Outputs			
1	2	3	4	5	6	7	8	9	D	C	B	A
H	H	H	H	H	H	H	H	H	H	H	H	H
X	X	X	X	X	X	X	X	L	L	H	H	L
X	X	X	X	X	X	X	L	H	L	H	H	H
X	X	X	X	X	X	L	H	H	H	L	L	L
X	X	X	X	X	L	H	H	H	H	L	L	H
X	X	X	L	H	H	H	H	H	H	L	H	L
X	X	L	H	H	H	H	H	H	H	H	L	L
X	L	H	H	H	H	H	H	H	H	H	L	H
L	H	H	H	H	H	H	H	H	H	H	H	L

H = High logic level, L = Low logic level,  
X = Irrelevant

Device type 02

Inputs									Outputs				
E1	0	1	2	3	4	5	6	7	A2	A1	A0	GS	E0
H	X	X	X	X	X	X	X	X	H	H	H	H	H
L	H	H	H	H	H	H	H	H	H	H	H	H	L
L	X	X	X	X	X	X	X	L	L	L	L	L	H
L	X	X	X	X	X	X	L	H	L	L	H	L	H
L	X	X	X	X	L	H	H	H	L	H	H	L	H
L	X	X	X	L	H	H	H	H	H	L	L	L	H
L	X	X	L	H	H	H	H	H	H	L	H	L	H
L	X	L	H	H	H	H	H	H	H	H	L	L	H
L	L	H	H	H	H	H	H	H	H	H	H	L	H

H = High logic level, L = Low logic level,  
X = Irrelevant

Device type 03

Inputs									Outputs				
$\overline{E1}$	$\overline{0}$	$\overline{1}$	$\overline{2}$	$\overline{3}$	$\overline{4}$	$\overline{5}$	$\overline{6}$	$\overline{7}$	$\overline{GS}$	$\overline{A_0}$	$\overline{A_1}$	$\overline{A_2}$	$\overline{E0}$
H	X	X	X	X	X	X	X	X	H	H	H	H	H
L	H	H	H	H	H	H	H	H	H	H	H	H	L
L	X	X	X	X	X	X	X	L	L	L	L	L	H
L	X	X	X	X	X	X	L	H	L	H	L	L	H
L	X	X	X	X	X	L	H	H	L	L	H	L	H
L	X	X	X	L	H	H	H	H	L	H	H	L	H
L	X	X	L	H	H	H	H	H	L	H	L	H	H
L	X	L	H	H	H	H	H	H	L	L	H	H	H
L	L	H	H	H	H	H	H	H	L	H	H	H	H

H = High logic level, L = Low logic level,  
X = Irrelevant

Figure 2. Truth tables.

Device type 01

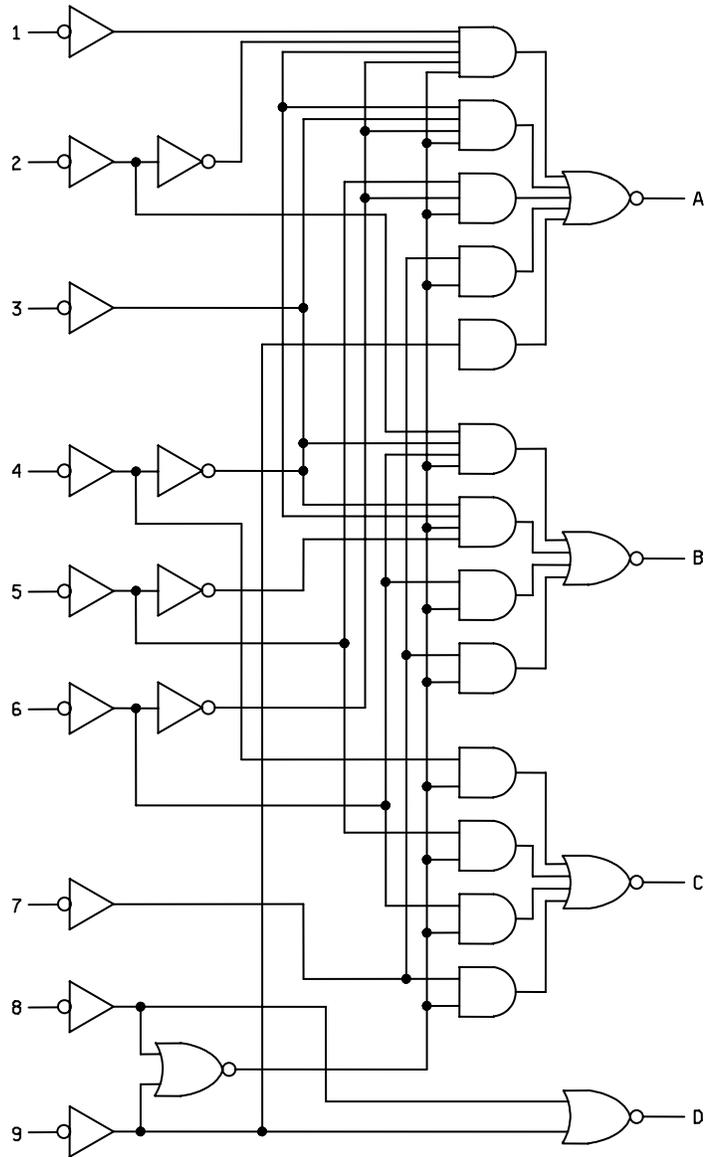


Figure 3. Logic diagrams.

DEVICE TYPE 02

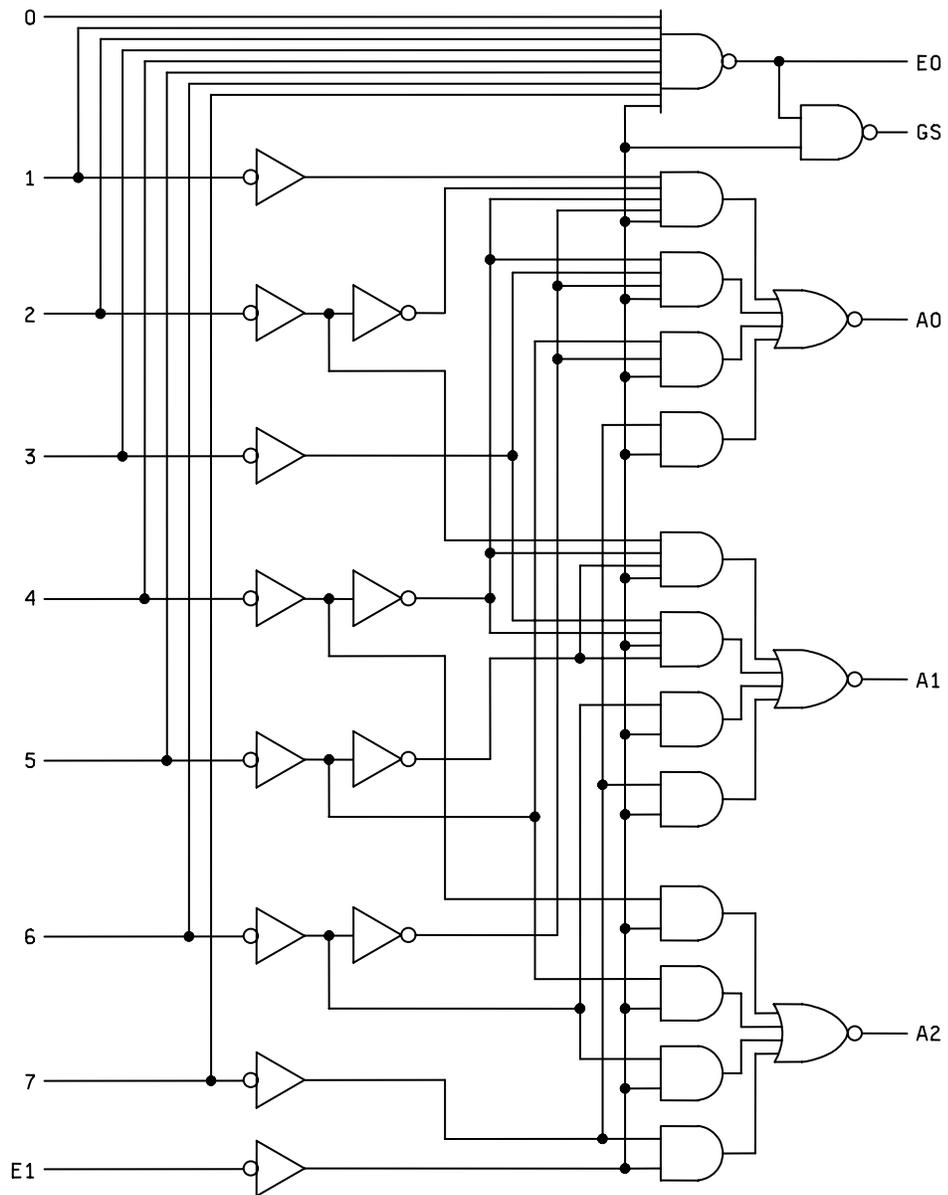


Figure 3. Logic diagrams – Continued.

DEVICE TYPE 03

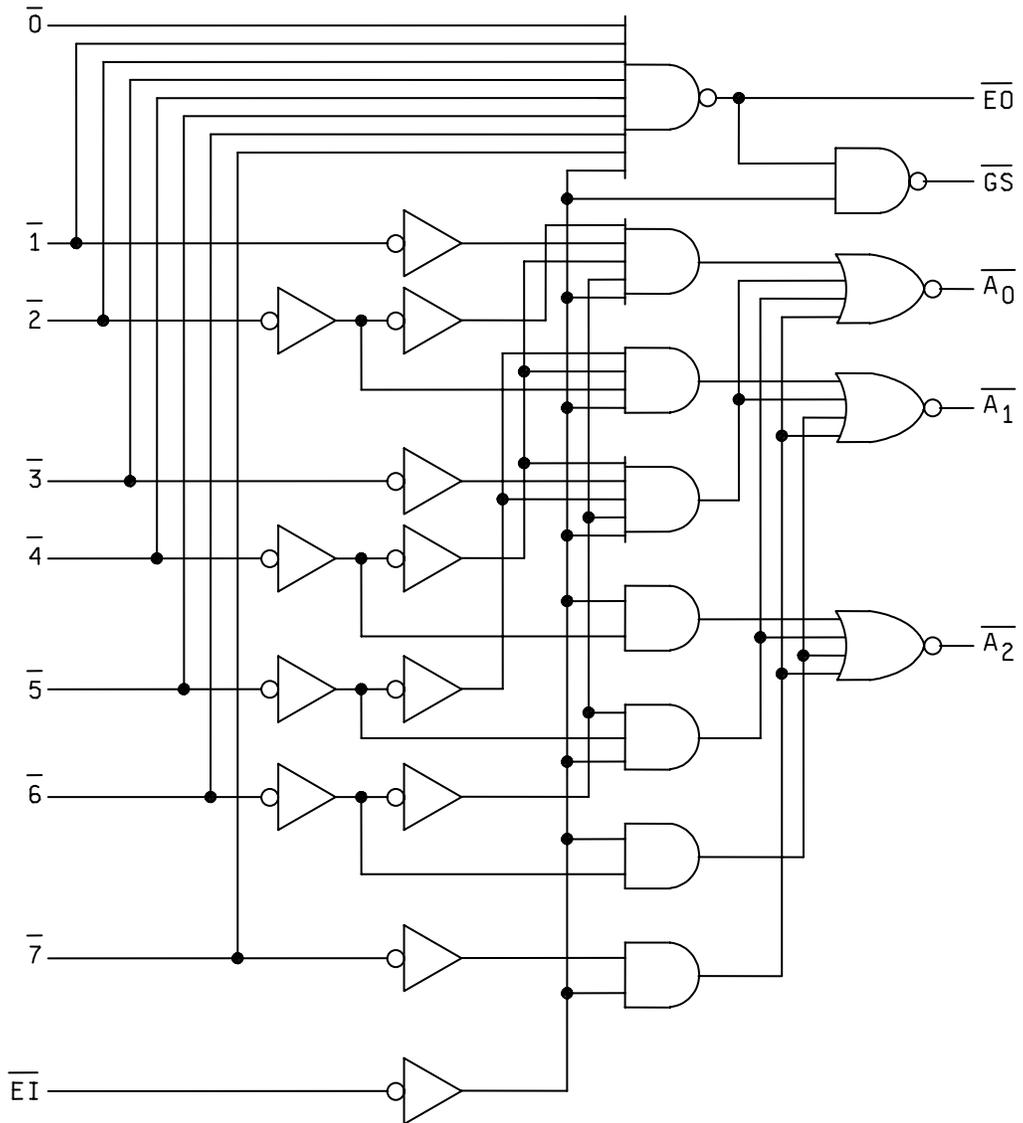
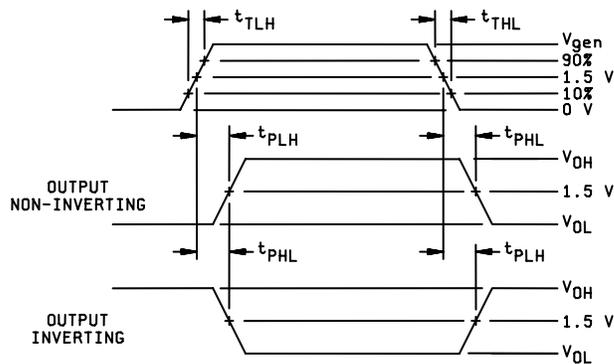
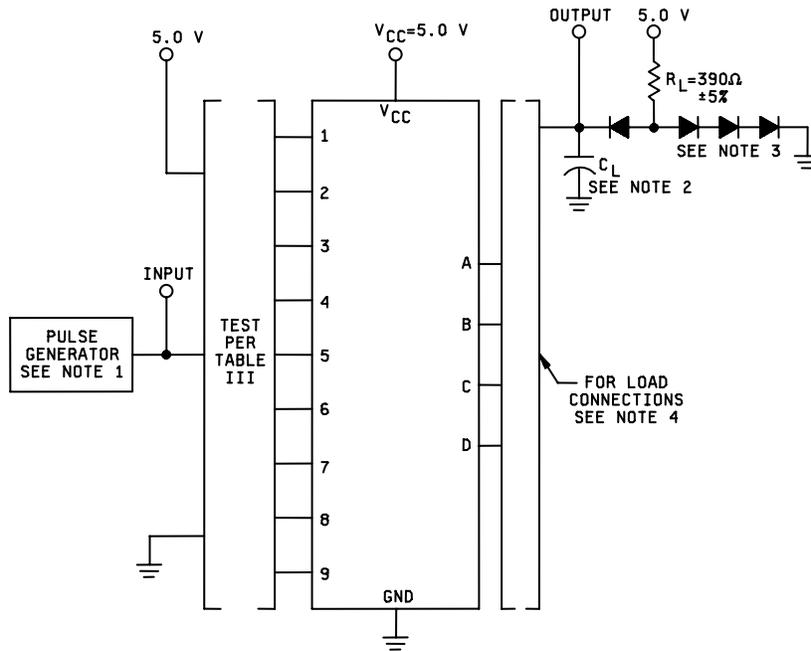


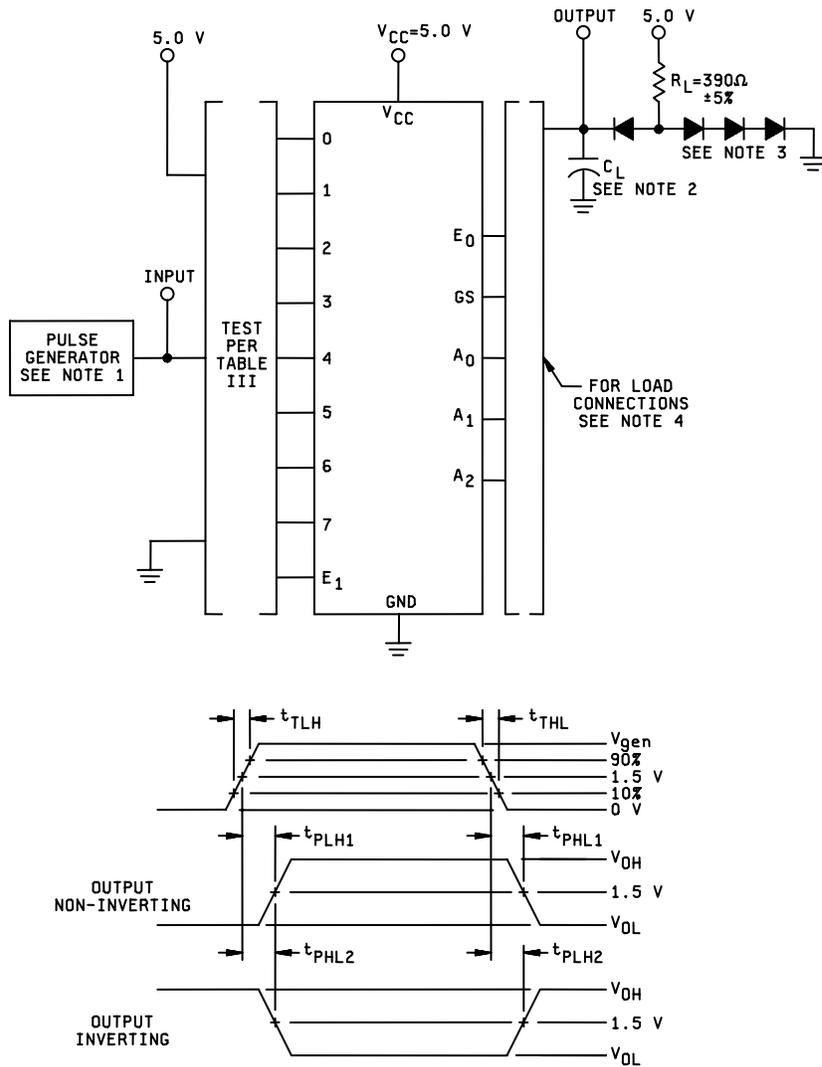
Figure 3. Logic diagrams – Continued.



NOTES:

1. The pulse generator has the following characteristics:  $V_{gen} = 3.0 \text{ V}$ ,  $PRR \leq 1 \text{ MHz}$ ,  $t_p = 500 \text{ ns}$ ,  $t_{THL} = t_{TLH} \leq 7 \text{ ns}$ , and  $Z_{OUT} \approx 50 \Omega$ .
2.  $C_L = 50 \text{ pF}$  minimum and includes probe and jig capacitance.
3. All diodes are 1N3064 or equivalent.
4. Load circuit is applied separately to each output pin under test. All other outputs may be open or loaded.

FIGURE 4. Switching time test circuit and waveforms for device type 01.



NOTES:

1. The pulse generator has the following characteristics:  $V_{gen} = 3.0\text{ V}$ ,  $PRR \leq 1\text{ MHz}$ ,  $t_p = 500\text{ ns}$ ,  $t_{THL} = t_{TLH} \leq 7\text{ ns}$ , and  $Z_{OUT} \approx 50\ \Omega$ .
2.  $C_L = 50\text{ pF}$  minimum and includes probe and jig capacitance.
3. All diodes are 1N3064 or equivalent.
4. Load circuit is applied separately to each output pin under test. All other outputs may be open or loaded.

FIGURE 5. Switching time test circuit and waveforms for device type 02.

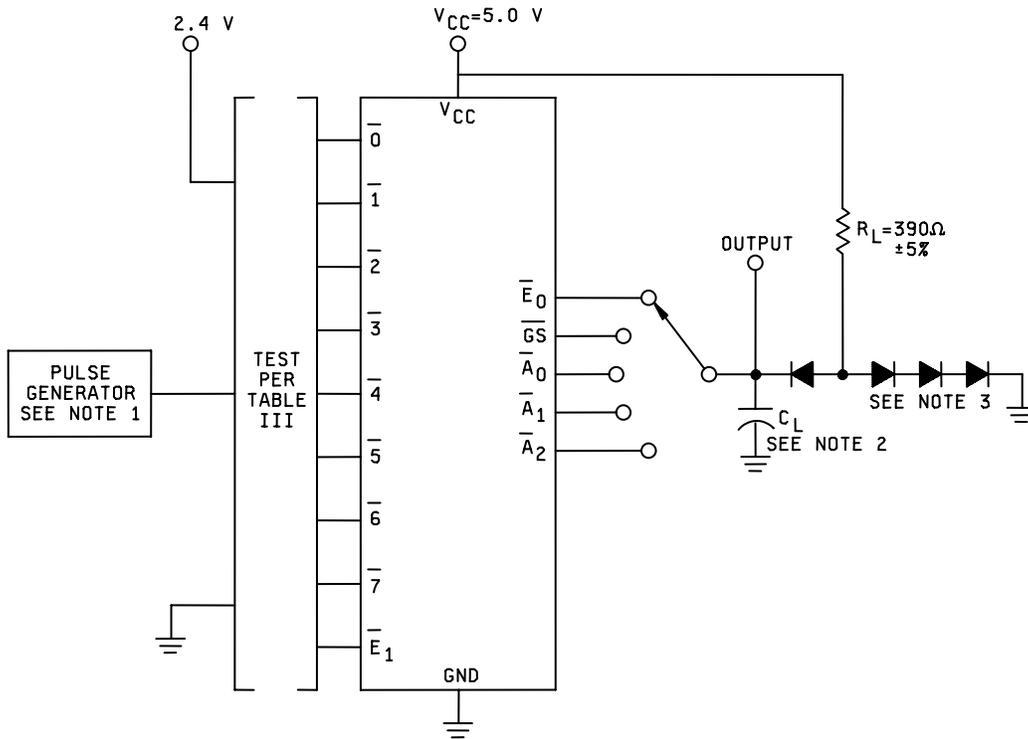
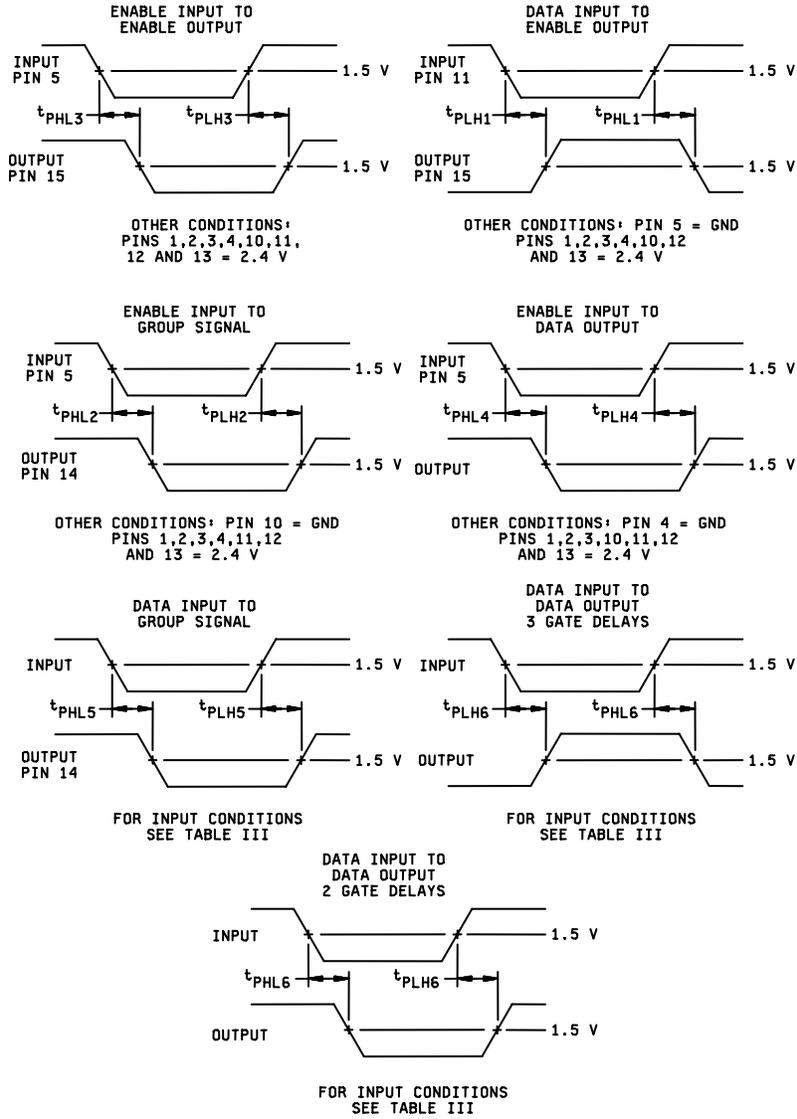


FIGURE 6. Switching test for device type 03.



NOTES:

1. The pulse generator has the following characteristics:  $V_{gen} = 3.0\text{ V}$ , rise and fall  $\leq 10\text{ ns}$ ,  $Z_{OUT} \approx 50\ \Omega$ ,  $PRR \leq 1\text{ MHz}$ ,  $PW = 100\text{ ns}$ .
2.  $C_L = 50\text{ pF}$  minimum and includes probe and jig capacitance.
3. All diodes are 1N3064 or equivalent.

FIGURE 6. Switching test for device type 03 – Continued.

TABLE III. Group A inspection for device type 01.  
Terminal conditions (pins not designated may be H ≥ 2.0 V; L ≤ 0.8 V; or open)

Subgroup	Symbol	MIL-STD-883 method	Cases E,F Test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Test limits		Unit	
				4	5	6	7	8	C	B	GND	A	9	1	2	3	D	NC	V <sub>CC</sub>		Min	Max		
1 T <sub>C</sub> =+25°C	V <sub>OH</sub>	3006	1	2.0V	2.0V	2.0V	2.0V	2.0V											4.5V	A	2.4		V <sub>dc</sub>	
		"	2	"	"	"	"	"											"	B	"		"	
		"	3	"	"	"	"	"												"	C	"		"
		"	4	"	"	"	"	"												"	D	"		"
	V <sub>OL</sub>	3007	5	"	"	"	"	"	"											"	A		0.4	"
		"	6	"	"	"	"	"	"											"	B		"	"
		"	7	0.8V	"	"	"	"	"	16mA	16mA									"	C		"	"
		"	8	2.0V	"	"	"	"	0.8V											"	D		"	"
	V <sub>IC</sub>		9																	"	1		-1.5	"
			10																	"	2		"	"
			11																	"	3		"	"
			12	-12mA																"	4		"	"
			13		-12mA															"	5		"	"
			14			-12mA														"	6		"	"
			15				-12mA													"	7		"	"
			16					-12mA												"	8		"	"
			17												-12mA					"	9		"	"
	I <sub>IL</sub>	3009	18												0.4V					5.5V	1	-0.7	-1.6	mA
		"	19													0.4V				"	2	"	"	"
		"	20														0.4V			"	3	"	"	"
		"	21	0.4V																"	4	"	"	"
		"	22		0.4V															"	5	"	"	"
		"	23			0.4V														"	6	"	"	"
"		24				0.4V													"	7	"	"	"	
"		25					0.4V												"	8	"	"	"	
"		26											0.4V						"	9	"	"	"	
I <sub>IH1</sub>	3010	27												2.4V					"	1		40	μA	
	"	28													2.4V				"	2		"	"	
	"	29														2.4V			"	3		"	"	
	"	30	2.4V																"	4		"	"	
	"	31		2.4V															"	5		"	"	
	"	32			2.4V														"	6		"	"	
	"	33				2.4V													"	7		"	"	
	"	34					2.4V												"	8		"	"	
	"	35						2.4V						2.4V					"	9		"	"	
I <sub>IH2</sub>	"	36																	"	1		100	"	
	"	37																	"	2		"	"	
	"	38																	"	3		"	"	
	"	39	5.5V																"	4		"	"	
	"	40		5.5V															"	5		"	"	
	"	41			5.5V														"	6		"	"	
"	42				5.5V													"	7		"	"		
"	43					5.5V												"	8		"	"		
"	44																	"	9		"	"		
I <sub>OS</sub>	3011	45	5.5V	5.5V	5.5V	5.5V	5.5V	5.5V											"	A	-35	-85	mA	
	"	46	"	"	"	"	"	"											"	B	"	"	"	
	"	47	"	"	"	"	"	"	GND	GND									"	C	"	"	"	
	"	48	"	"	"	"	"	"											"	D	"	"	"	
I <sub>CCL</sub>	3005	49				GND												"	V <sub>CC</sub>	"	70	mA		
I <sub>CCH</sub>	3005	50																"	V <sub>CC</sub>	"	62	mA		
2	Same tests, terminal conditions, and limits as for subgroup 1, except T <sub>C</sub> = +125°C, and V <sub>IC</sub> tests are omitted.																							
3	Same tests, terminal conditions, and limits as for subgroup 1, except T <sub>C</sub> = -55°C, and V <sub>IC</sub> tests are omitted.																							

See footnote at end of device type 01.

TABLE III. Group A inspection for device type 01 – Continued.  
Terminal conditions (pins not designated may be H ≥ 2.0 V; L ≤ 0.8 V; or open)

Subgroup	Symbol	MIL-STD-883 method	Cases E,F Test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Test limits		Unit																								
				4	5	6	7	8	C	B	GND	A	9	1	2	3	D	NC	V <sub>CC</sub>		Min	Max																									
7 1/ T <sub>C</sub> =+25°C		3014	51	A	A	A	A	A	H	H	GND	A	9	1	2	A	H	NC	V <sub>CC</sub>																												
			52	"	"	"	A	A	H	L	"	L	A	"	"	"	"	L	"				"																								
			53	"	"	"	"	B	H	"	"	"	"	"	"	"	"	L	"				"																								
			54	"	"	"	A	B	L	"	"	"	"	"	"	"	"	L	"				"																								
			55	"	"	"	B	A	"	"	"	"	"	"	"	"	"	"	"				"																								
			56	"	B	"	"	"	"	"	"	"	"	"	"	"	"	"	"				"																								
			57	B	A	"	"	"	"	"	"	"	"	"	"	"	"	"	"				"																								
			58	A	"	"	"	"	"	"	"	H	L	"	"	"	"	B	"				"																								
			59	"	"	"	"	"	"	"	"	"	L	"	"	B	A	"	"				"																								
			60	"	"	"	"	"	"	"	"	"	L	"	"	A	A	"	"				"																								
61	"	"	"	"	"	"	"	"	"	H	"	"	A	A	"	"	"																														
8	Repeat subgroup 7 at T <sub>C</sub> = +125°C and T <sub>C</sub> = -55°C.																																														
9 T <sub>C</sub> =+25°C	t <sub>PHL1</sub>	3003 (Fig. 4)	62	5.0V	5.0V	5.0V	5.0V	5.0V	"	OUT	GND	OUT	5.0V	IN	5.0V	IN	5.0V				"	5.0V	1 to A	3	16	ns																					
			63	"	"	"	"	"	"	"	OUT	"	OUT	"	5.0V	"	5.0V				"	"	"	2 to B	"	"	"																				
			64	"	"	"	"	"	"	"	"	"	"	"	"	5.0V	IN				"	"	"	3 to B	"	"	"																				
			65	IN	"	"	"	"	"	"	OUT	"	"	"	"	"	5.0V				IN	"	"	4 to C	"	"	"																				
			66	5.0V	IN	"	"	"	"	"	OUT	"	"	"	"	"	"				"	"	"	5 to A	"	"	"																				
			67	"	5.0V	IN	"	"	"	"	OUT	"	"	"	"	"	"				"	"	"	6 to C	"	"	"																				
			68	"	"	5.0V	IN	"	"	"	OUT	"	"	"	"	"	"				"	"	"	7 to C	"	"	"																				
			69	"	"	"	5.0V	IN	"	"	"	"	"	"	"	"	"				"	"	"	8 to D	"	"	"																				
			70	"	"	"	5.0V	5.0V	IN	"	"	"	"	OUT	IN	"	"	"	"	"	9 to A	"	"	"																							
			71	5.0V	5.0V	5.0V	5.0V	5.0V	"	"	"	OUT	"	OUT	5.0V	GND	IN	5.0V	"	"	"	2 to A	"	20	"																						
	72	IN	5.0V	"	"	"	"	"	"	OUT	"	OUT	"	"	"	"	"	"	"	4 to B	"	"	"																								
	73	5.0V	IN	"	"	"	"	"	"	OUT	"	OUT	"	"	"	"	"	"	"	5 to B	"	"	"																								
	74	"	GND	IN	"	"	"	"	"	"	"	OUT	5.0V	"	"	"	"	"	"	6 to A	"	"	"																								
	75	"	"	5.0V	IN	"	"	"	"	OUT	"	OUT	"	"	"	"	"	"	"	8 to B	"	"	"																								
	76	"	"	5.0V	GND	IN	"	"	"	"	"	OUT	IN	"	"	"	"	"	"	9 to C	"	"	"																								
	t <sub>PLH1</sub>			77	5.0V	5.0V	5.0V	5.0V	5.0V	"	OUT	"	OUT	5.0V	IN	5.0V	IN	5.0V	"	"	"	1 to A	"	19	"																						
				78	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	2 to B	"	"	"																						
				79	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	3 to B	"	"	"																						
				80	IN	"	"	"	"	"	"	OUT	"	"	"	"	"	"	"	"	"	4 to C	"	"	"																						
				81	5.0V	IN	"	"	"	"	"	OUT	"	"	"	"	"	"	"	"	"	"	5 to A	"	"	"																					
82				"	5.0V	IN	"	"	"	"	OUT	"	"	"	"	"	"	"	"	"	"	6 to C	"	"	"																						
83				"	"	5.0V	IN	"	"	"	OUT	"	"	"	"	"	"	"	"	"	"	7 to C	"	"	"																						
84				"	"	"	5.0V	IN	"	"	"	"	"	"	"	"	"	"	"	"	"	8 to D	"	"	"																						
85				"	"	"	5.0V	5.0V	IN	"	"	"	"	OUT	IN	"	"	"	"	"	"	9 to A	"	"	"																						
t <sub>PLH2</sub>						86	5.0V	5.0V	5.0V	5.0V	5.0V	"	OUT	"	OUT	5.0V	GND	IN	5.0V	"	"	"	"	2 to A	"	24	"																				
	87	IN	5.0V			"	"	"	"	"	OUT	"	OUT	"	"	GND	"	"	"	"	4 to B	"	"	"																							
	88	5.0V	IN			"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5 to B	"	"	"																							
	89	"	GND			IN	"	"	"	"	"	"	OUT	"	"	"	"	"	"	"	6 to A	"	"	"																							
	90	"	"			5.0V	IN	"	"	"	"	"	OUT	5.0V	"	"	"	"	"	"	"	8 to B	"	"	"																						
	91	"	"			5.0V	GND	IN	"	"	"	"	OUT	IN	"	"	"	"	"	"	"	9 to C	"	"	"																						
	92	5.0V	5.0V			5.0V	5.0V	5.0V	"	"	"	OUT	"	OUT	5.0V	IN	5.0V	IN	5.0V	"	"	"	1 to A	"	21	"																					
	93	"	"			"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	2 to B	"	"	"																						
	94	"	"			"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	3 to B	"	"	"																						
	95	IN	"			"	"	"	"	"	"	OUT	"	"	"	"	"	"	"	"	"	4 to C	"	"	"																						
96	5.0V	IN	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5 to A	"	"	"																									
97	"	5.0V	IN	"	"	"	"	"	OUT	"	"	"	"	"	"	"	"	"	6 to C	"	"	"																									
98	"	"	5.0V	IN	"	"	"	"	OUT	"	"	"	"	"	"	"	"	"	7 to C	"	"	"																									
99	"	"	"	5.0V	IN	"	"	"	"	"	"	"	"	"	"	"	"	"	8 to D	"	"	"																									
100	"	"	"	5.0V	5.0V	IN	"	"	"	"	OUT	IN	"	"	"	"	"	"	9 to A	"	"	"																									
t <sub>PHL2</sub>			101	5.0V	5.0V	5.0V	5.0V	5.0V	"	OUT	"	OUT	5.0V	GND	IN	5.0V	"	"	"	"	2 to A	"	26	"																							
			102	IN	5.0V	"	"	"	"	"	OUT	"	OUT	"	"	GND	"	"	"	"	4 to B	"	"	"																							
			103	5.0V	IN	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5 to B	"	"	"																							
			104	"	GND	IN	"	"	"	"	"	"	OUT	"	"	"	"	"	"	"	6 to A	"	"	"																							
			105	"	"	5.0V	IN	"	"	"	"	"	OUT	"	"	"	"	"	"	"	"	8 to B	"	"	"																						
			106	"	"	5.0V	GND	IN	"	"	"	"	OUT	"	"	"	"	"	"	"	"	9 to C	"	"	"																						

See footnote at end of device type 01.

TABLE III. Group A inspection for device type 01 – Continued.  
Terminal conditions (pins not designated may be  $H \geq 2.0$  V;  $L \leq 0.8$  V; or open)

Subgroup	Symbol	MIL-STD-883 method	Cases E,F Test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Test limits		Unit			
				4	5	6	7	8	C	B	GND	A	9	1	2	3	D	NC	V <sub>CC</sub>		Min	Max				
10 $T_c = +125^\circ\text{C}$	$t_{PLH1}$	3003 (Fig. 4)	107	5.0V	5.0V	5.0V	5.0V	5.0V				GND	OUT	5.0V	IN	5.0V	5.0V		5.0V	1 to A	3	25	ns			
			108	"	"	"	"	"	"				"	OUT	"	5.0V	IN	5.0V	"	"	2 to B	"	"	"		
			109	"	"	"	"	"	"				"	OUT	"	"	IN	5.0V	"	"	3 to B	"	"	"		
			110	IN	"	"	"	"	"		OUT		"	OUT	"	"	IN	5.0V	"	"	4 to C	"	"	"		
			111	5.0V	IN	"	"	"	"		OUT		"	OUT	"	"	IN	5.0V	"	"	5 to A	"	"	"		
			112	"	5.0V	IN	"	"	"		OUT		"	OUT	"	"	IN	5.0V	"	"	6 to C	"	"	"		
			113	"	"	5.0V	IN	"	"		OUT		"	OUT	"	"	IN	5.0V	"	"	7 to C	"	"	"		
			114	"	"	"	5.0V	IN	"		OUT		"	OUT	"	"	IN	5.0V	"	"	8 to D	"	"	"		
			115	"	"	"	5.0V	5.0V	5.0V		OUT		"	OUT	IN	"	"	"	"	"	"	9 to A	"	"	"	
						116	5.0V	5.0V	5.0V	5.0V	5.0V			"	OUT	5.0V	GND	IN	5.0V	"	"	"	2 to A	"	31	"
						117	IN	5.0V	"	"	"			"	OUT	"	"	GND	"	"	"	"	4 to B	"	"	"
						118	5.0V	IN	"	"	"			"	OUT	"	"	"	"	"	"	"	5 to B	"	"	"
						119	"	GND	IN	"	"			"	OUT	"	5.0V	"	"	"	"	"	6 to A	"	"	"
						120	"	"	5.0V	IN	"			"	OUT	"	"	"	"	"	"	"	8 to B	"	"	"
						121	"	"	5.0V	GND	5.0V		OUT	"	OUT	IN	"	"	"	"	"	"	9 to C	"	"	"
11	Same tests, terminal conditions, and limits as for subgroup 10, except $T_c = -55^\circ\text{C}$ .																									

1/ Input voltages shown are: A = 2.4 V minimum and B = 0.8 V maximum.

2/ Output voltages shall be either:  $H \geq 1.5$  V,  $L \leq 1.5$  V.

TABLE III. Group A inspection for device type 02 .  
Terminal conditions (pins not designated may be  $H \geq 2.0\text{ V}$ ;  $L \leq 0.8\text{ V}$ ; or open)

Subgroup	Symbol	MIL-STD-883 method	Cases E,F Test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Test limits		Unit			
				4	5	6	7	E <sub>1</sub>	A <sub>2</sub>	A <sub>1</sub>	GND	A <sub>0</sub>	0	1	2	3	GS	E <sub>0</sub>	V <sub>CC</sub>		Min	Max				
1 T <sub>C</sub> =+25°C	V <sub>OH</sub>	3006	1					2.0V											4.5V	A <sub>0</sub>	2.4		V dc			
		"	2					"											"	A <sub>1</sub>	"		"			
		"	3					"												"	A <sub>2</sub>	"		"		
		"	4					"												"	GS	"		"		
		"	5					"												"	E <sub>0</sub>	"		"		
	V <sub>OL</sub>	3007	6					0.8V	0.8V											"	A <sub>0</sub>		0.4	"		
		"	7					"	"											"	A <sub>1</sub>		"	"		
		"	8					"	"											"	A <sub>2</sub>		"	"		
		"	9					"	"	16mA	16mA									"	GS		"	"		
		"	10		2.0V	2.0V	2.0V	2.0V	"	"									16mA	16mA	E <sub>0</sub>		"	"		
	V <sub>IC</sub>			11																	"	0		-1.5	"	
				12																		"	1		"	"
				13																		"	2		"	"
				14																		"	3		"	"
				15																		"	4		"	"
				16																		"	5		"	"
				17																		"	6		"	"
				18																		"	7		"	"
				19																		"	E <sub>1</sub>		"	"
	I <sub>IL1</sub>	3009	20																		1		1/	1/	mA	
"		21																		"	2		"	"		
"		22																		"	3		"	"		
"		23																		"	4		"	"		
"		24																		"	5		"	"		
"		25																		"	6		"	"		
"		26																		"	7		"	"		
"		27																		"	E <sub>1</sub>		"	"		
I <sub>IL2</sub>		28																	"	0		-7	-1.6	mA		
I <sub>IH1</sub>	3010	29																		1		80		μA		
	"	30																		"	2		"	"		
	"	31																		"	3		"	"		
	"	32																		"	4		"	"		
	"	33																		"	5		"	"		
	"	34																		"	6		"	"		
	"	35																		"	7		"	"		
I <sub>IH2</sub>			37																	"	1		200	"		
			38																		"	2		"	"	
			39																		"	3		"	"	
			40																		"	4		"	"	
			41																		"	5		"	"	
I <sub>IH3</sub>			42																	"	6		"	"		
			43																		"	7		"	"	
			44																		"	E <sub>1</sub>		"	"	
			45																		"	0		40	"	
			46																		"	0		100	"	
I <sub>OS</sub>	3011	47																		"	-35	-85		mA		
	"	48																		"	A <sub>0</sub>		"	"		
	"	49																		"	A <sub>1</sub>		"	"		
	"	50																		"	A <sub>2</sub>		"	"		
	"	51																		"	GS		"	"		
I <sub>CCL</sub>	3005	52					GND	GND												"	V <sub>CC</sub>		60	"		
	3005	53																		"	V <sub>CC</sub>		55	"		

See footnote at end of device type 02.

TABLE III. Group A inspection for device type 02 - Continued.  
Terminal conditions (pins not designated may be H ≥ 2.0 V; L ≤ 0.8 V; or open)

Subgroup	Symbol	MIL-STD-883 method	Cases E,F Test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Test limits		Unit		
				4	5	6	7	E <sub>1</sub>	A <sub>2</sub>	A <sub>1</sub>	GND	A <sub>0</sub>	0	1	2	3	GS	E <sub>0</sub>	V <sub>CC</sub>		Min	Max			
2	Same tests, terminal conditions, and limits as for subgroup 1, except T <sub>C</sub> = +125°C, and V <sub>IC</sub> tests are omitted.																								
3	Same tests, terminal conditions, and limits as for subgroup 1, except T <sub>C</sub> = -55°C, and V <sub>IC</sub> tests are omitted.																								
7 2/ T <sub>C</sub> = +25°C		3014	54	A	A	A	A	A	H	H	GND	H	A	A	A	A	H	H	5.0V						
		"	55	"	"	"	A	B	L	L	"	L	"	"	"	"	"	L	L	"					
		"	56	"	"	"	B	"	"	"	"	"	"	"	"	"	"	"	"	"					
		"	57	"	"	B	A	"	"	"	"	"	"	"	"	"	"	"	"	"					
		"	58	"	"	B	A	"	"	"	"	"	"	"	"	"	"	"	"	"					
		"	59	"	B	A	"	"	"	"	"	"	"	"	"	"	"	"	"	"					
		"	60	"	A	"	"	"	"	"	H	"	"	"	"	"	"	B	"	"				3/	
		"	61	"	"	"	"	"	"	"	"	"	"	"	"	"	B	A	"	"					
		"	62	"	"	"	"	"	"	"	"	"	"	"	"	B	A	"	"	"					
		"	63	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"					
"	64	"	"	"	"	"	"	A	"	"	"	"	"	"	"	"	"	"							
8	Repeat subgroup 7 at T <sub>C</sub> = +125°C and T <sub>C</sub> = -55°C.																								
9 T <sub>C</sub> = +25°C	t <sub>PHL1</sub> t <sub>PHL2</sub> t <sub>PHL3</sub> t <sub>PHL4</sub> t <sub>PHL5</sub> t <sub>PHL6</sub> t <sub>PHL7</sub>	3003 (Fig. 5)	65	5.0V	5.0V	5.0V	5.0V	GND			GND	OUT	IN	5.0V IN	5.0V IN	5.0V IN	OUT		5.0V	0 to GS	3	23	ns		
		"	66	"	"	"	"	"	"	"	"	OUT	OUT	"	"	"	"	"	"	"	1 to A <sub>0</sub>	"	19	"	
		"	67	"	"	"	"	"	"	"	"	"	OUT	OUT	"	"	"	"	"	"	2 to A <sub>1</sub>	"	"	"	"
		"	68	"	"	"	"	"	"	"	"	"	OUT	OUT	"	"	"	"	"	"	3 to A <sub>1</sub>	"	"	"	"
		"	69	IN	"	"	"	"	"	"	OUT	"	"	"	"	"	"	"	"	"	4 to A <sub>2</sub>	"	"	"	"
		"	70	"	IN	"	"	"	"	"	"	"	OUT	"	"	"	"	"	"	"	5 to A <sub>0</sub>	"	"	"	"
		"	71	"	"	IN	"	"	"	"	OUT	"	"	"	"	"	"	"	"	"	6 to A <sub>2</sub>	"	"	"	"
		"	72	"	"	"	IN	"	"	"	OUT	"	"	"	"	"	"	"	"	"	7 to A <sub>2</sub>	"	"	"	"
		"	73	"	"	"	"	IN	"	"	OUT	"	"	"	"	"	"	"	"	"	E <sub>1</sub> to E <sub>0</sub>	"	"	"	"
		"	74	5.0V	5.0V	5.0V	5.0V	GND	"	"	"	"	OUT	IN	5.0V IN	5.0V IN	5.0V IN	"	"	"	"	0 to E <sub>0</sub>	"	"	"
		"	75	5.0V	"	"	"	"	"	"	"	"	OUT	"	"	"	"	"	"	"	"	2 to A <sub>0</sub>	"	20	"
		"	76	IN	"	"	"	"	"	"	"	"	OUT	"	"	"	"	"	"	"	"	4 to A <sub>1</sub>	"	"	"
		"	77	5.0V	IN	"	"	"	"	"	"	"	OUT	"	"	"	"	"	"	"	"	5 to A <sub>1</sub>	"	"	"
		"	78	GND	GND	IN	"	"	"	"	"	"	OUT	"	"	"	"	GND	"	"	"	6 to A <sub>0</sub>	"	"	"
		"	79	"	"	"	"	"	"	IN	"	"	"	"	"	"	"	GND	"	"	"	E <sub>1</sub> to GS	"	"	"
		"	80	"	"	"	"	"	"	"	"	"	OUT	"	"	"	"	"	"	"	"	E <sub>1</sub> to A <sub>0</sub>	"	"	"
		"	81	"	"	"	"	"	"	"	"	"	OUT	"	"	"	"	"	"	"	"	E <sub>1</sub> to A <sub>1</sub>	"	"	"
		"	82	"	"	"	"	"	"	"	OUT	"	"	"	"	"	"	"	"	"	"	E <sub>1</sub> to A <sub>2</sub>	"	"	"
		10 T <sub>C</sub> = +125°C	t <sub>PLH1</sub> t <sub>PLH2</sub> t <sub>PLH3</sub> t <sub>PLH4</sub> t <sub>PLH5</sub> t <sub>PLH6</sub> t <sub>PLH7</sub>	3003 (Fig. 5)	101	5.0V	5.0V	5.0V	5.0V	GND			"	OUT	IN	5.0V IN	5.0V IN	5.0V IN	OUT		"	0 to GS	"	30	"
				"	102	"	"	"	"	"	"	"	"	"	OUT	"	"	"	"	"	"	"	1 to A <sub>0</sub>	"	25
"	103			"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	2 to A <sub>1</sub>	"	"	"	
"	104			"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	3 to A <sub>1</sub>	"	"	"	
"	105			IN	"	"	"	"	"	"	OUT	"	"	"	"	"	"	"	"	"	"	4 to A <sub>2</sub>	"	"	"
"	106			"	IN	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5 to A <sub>0</sub>	"	"	"

See footnote at end of device type 02.

TABLE III. Group A inspection for device type 02 - Continued.  
Terminal conditions (pins not designated may be  $H \geq 2.0$  V;  $L \leq 0.8$  V; or open)

Subgroup	Symbol	MIL-STD-883 method	Cases E,F Test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Test limits		Unit		
				4	5	6	7	E <sub>1</sub>	A <sub>2</sub>	A <sub>1</sub>	GND	A <sub>0</sub>	0	1	2	3	GS	E <sub>0</sub>	V <sub>CC</sub>		Min	Max			
10 T <sub>C</sub> =+125°C	t <sub>PHL2</sub>	3003 (Fig. 5)	107			IN	5.0V IN	GND	OUT		GND								5.0V	6 to A <sub>2</sub>	3	25	ns		
			108					GND	OUT												7 to A <sub>2</sub>	"	"	"	
	t <sub>PHL3</sub>		109						GND												E <sub>1</sub> to E <sub>0</sub>	"	"	"	
	t <sub>PHL4</sub>		110	5.0V	5.0V	5.0V	5.0V		GND				IN	5.0V	5.0V	5.0V			OUT		0 to E <sub>0</sub>	"	"	"	
	t <sub>PHL5</sub>		111	5.0V	"	"	"	"	"	"			OUT	"	"	"	"				2 to A <sub>0</sub>	"	26	"	
			112	IN	"	"	"	"	"	"			OUT	"	"	"	"				4 to A <sub>1</sub>	"	"	"	
			113	5.0V	IN	"	"	"	"	"			OUT	"	"	"	GND				5 to A <sub>1</sub>	"	"	"	
			114	GND	GND	IN	"	"	"	"			OUT	"	"	"	GND				6 to A <sub>0</sub>	"	"	"	
	t <sub>PHL6</sub>		115	"	"	"	"	"	IN	"			"	"	"	"	"		OUT		E <sub>1</sub> to GS	"	28	"	
	t <sub>PHL7</sub>		116	"	"	"	"	"	"	"			OUT	"	"	"	"				E <sub>1</sub> to A <sub>0</sub>	"	"	"	
			117	"	"	"	"	"	"	"			OUT	"	"	"	"				E <sub>1</sub> to A <sub>1</sub>	"	"	"	
			118	"	"	"	"	"	"	"	OUT		"	"	"	"	"				E <sub>1</sub> to A <sub>2</sub>	"	"	"	
			t <sub>PLH1</sub>	119	5.0V	5.0V	5.0V	5.0V	GND	"			"	OUT	IN	5.0V	5.0V	5.0V	OUT		"	0 to GS	"	34	"
			t <sub>PLH2</sub>	120	"	"	"	"	"	"			"	OUT	"	IN	5.0V	"			"	1 to A <sub>0</sub>	"	26	"
		121	"	"	"	"	"	"			"	OUT	"	"	IN	"			"	2 to A <sub>1</sub>	"	"	"		
		122	"	"	"	"	"	"			"	OUT	"	"	"	"			"	3 to A <sub>1</sub>	"	"	"		
		123	IN	"	"	"	"	"	OUT		"	"	"	"	"	"			"	4 to A <sub>2</sub>	"	"	"		
		124	"	IN	"	"	"	"	"		"	OUT	"	"	"	"			"	5 to A <sub>0</sub>	"	"	"		
		125	"	"	"	"	"	"	OUT		"	"	"	"	"	"			"	6 to A <sub>2</sub>	"	"	"		
		126	"	"	"	IN	"	"	OUT		"	"	"	"	"	"			"	7 to A <sub>2</sub>	"	"	"		
	t <sub>PLH3</sub>	127	"	"	"	"	"	IN	"		"	"	"	"	"	"			"	E <sub>1</sub> to E <sub>0</sub>	"	24	"		
	t <sub>PLH4</sub>	128	5.0V	5.0V	5.0V	5.0V	GND	"			"	OUT	IN	5.0V	5.0V	5.0V	OUT		"	0 to E <sub>0</sub>	"	20	"		
	t <sub>PLH5</sub>	129	5.0V	"	"	"	"	"			"	OUT	"	GND	"	"			"	2 to A <sub>0</sub>	"	26	"		
		130	IN	"	"	"	"	"			"	OUT	"	"	"	"			"	4 to A <sub>1</sub>	"	"	"		
		131	5.0V	IN	"	"	"	"			"	OUT	"	"	"	GND			"	5 to A <sub>1</sub>	"	"	"		
		132	GND	GND	IN	"	"	"			"	OUT	"	"	"	GND			"	6 to A <sub>0</sub>	"	"	"		
	t <sub>PLH6</sub>	133	"	"	"	"	"	IN			"	"	"	"	"	"		OUT	"	E <sub>1</sub> to GS	"	24	"		
	t <sub>PLH7</sub>	134	"	"	"	"	"	"			"	OUT	"	"	"	"			"	E <sub>1</sub> to A <sub>0</sub>	"	28	"		
		135	"	"	"	"	"	"			"	"	"	"	"	"			"	E <sub>1</sub> to A <sub>1</sub>	"	"	"		
		136	"	"	"	"	"	"	OUT		"	"	"	"	"	"			"	E <sub>1</sub> to A <sub>2</sub>	"	"	"		
11	Same tests, terminal conditions, and limits as for subgroup 10, except T <sub>C</sub> = -55°C.																								

- 1/ I<sub>IL1</sub> limits for National are -1.2/-2.8 mA.
- 2/ Input voltages shown are: A = 2.4 V minimum and B = 0.8 V maximum.
- 3/ Output voltages shall be either: H ≥ 1.5 V, L ≤ 1.5 V.

TABLE III. Group A inspection for device type 03.  
Terminal conditions (pins not designated may be  $H \geq 2.0\text{ V}$ ;  $L \leq 0.8\text{ V}$ ; or open)

Subgroup	Symbol	MIL-STD-883 method	Cases E,F Test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Test limits		Unit			
				$\bar{4}$	$\bar{5}$	$\bar{6}$	$\bar{7}$	$\bar{E}_1$	$\bar{A}_2$	$\bar{A}_1$	GND	$\bar{A}_0$	$\bar{0}$	$\bar{1}$	$\bar{2}$	$\bar{3}$	$\bar{GS}$	$\bar{E}_0$	$V_{CC}$		Min	Max				
1 $T_C=+25^\circ\text{C}$	$V_{OH}$	3006	1	4.5V	4.5V	4.5V	4.5V	2.0V					4.5V	4.5V	4.5V	4.5V	$\bar{GS}$	$\bar{E}_0$	$V_{CC}$	$\bar{E}_1$	2.4		V dc			
		"	2	"	"	"	4.5V	"					GND	"	4.5V	4.5V	-8mA	"	"	"	$\bar{GS}$	"		"		
		"	3	"	"	"	GND	"					"	"	GND	GND	"	"	"	"	"	$\bar{A}_0$	"		"	
		"	4	"	"	"	"	"	"					4.5V	"	4.5V	GND	"	"	"	"	$\bar{A}_1$	"		"	
		"	5	GND	GND	GND	"	"	"	-8mA	-8mA	"	"	4.5V	2.0V	2.0V	2.0V	"	"	"	"	$\bar{A}_2$	"		"	
	$V_{OL}$	3007	6	2.0V	2.0V	2.0V	2.0V	0.8V						2.0V	2.0V	2.0V	2.0V	16mA	16mA	"	$\bar{E}_0$	0.4		"		
		"	7	"	"	"	"	"						0.8V	2.0V	2.0V	"	"	"	"	$\bar{GS}$	"		"		
		"	8	"	"	"	"	"						2.0V	0.8V	"	"	"	"	"	$\bar{A}_0$	"		"		
		"	9	"	"	"	"	"						"	2.0V	"	0.8V	"	"	"	$\bar{A}_0$	"		"		
		"	10	"	"	"	"	"						"	"	"	2.0V	"	"	"	$\bar{A}_0$	"		"		
		"	11	"	"	"	"	"						"	"	0.8V	"	"	"	"	$\bar{A}_1$	"		"		
		"	12	"	"	"	"	"						"	"	2.0V	"	"	"	"	$\bar{A}_1$	"		"		
		"	13	0.8V	"	0.8V	2.0V	"	"	16mA	16mA	"	"	"	"	"	"	"	"	"	"	$\bar{A}_2$	"		"	
		"	14	2.0V	0.8V	2.0V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	$\bar{A}_2$	"		"	
		$I_{IH1}$	3010	15	2.4V	GND	GND	GND	GND	4.5V					GND	GND	GND	GND			5.5V	$\bar{4}$	80		$\mu\text{A}$	
	"		16	GND	2.4V	GND	"	"	"					"	"	"	"			"	$\bar{5}$	"		"		
	"		17	"	GND	2.4V	"	"	"					"	"	"	"			"	$\bar{6}$	"		"		
	"		18	"	"	GND	2.4V	"	"					"	"	"	"			"	$\bar{7}$	"		"		
	"		19	"	"	"	"	2.4V	"					"	"	"	"			"	$\bar{7}$	"		"		
	"		20	"	"	"	"	GND	2.4V	"				"	"	"	"			"	$\bar{1}$	"		"		
	"		21	"	"	"	"	"	4.5V	"				"	"	"	"			"	$\bar{1}$	"		"		
	"		22	"	"	"	"	"	"	"				"	"	2.4V	"	"			"	$\bar{2}$	"		"	
	$I_{IH3}$		"	23	"	"	"	"	"	"				"	"	GND	2.4V	GND	2.4V	"	"	$\bar{2}$	"		"	
			"	24	5.5V	"	"	"	"	"				"	"	GND	"	"	2.4V	"	"	$\bar{3}$	"		"	
			$I_{IH2}$	"	25	GND	5.5V	"	"	"	"				"	"	"	"	"	"	"	"	$\bar{3}$	40		"
				"	26	"	GND	5.5V	"	"	"				"	"	"	"	"	"	"	"	$\bar{4}$	200		"
	$I_{IH4}$	"	27	"	"	5.5V	"	"	"				"	"	"	"	"	"	"	"	$\bar{4}$	"		"		
		"	28	"	"	GND	5.5V	"	"				"	"	"	"	"	"	"	"	$\bar{5}$	"		"		
		"	29	"	"	"	5.5V	"	5.5V				"	"	"	"	"	"	"	"	$\bar{5}$	"		"		
		"	30	"	"	"	"	"	4.5V				"	"	"	"	"	"	"	"	$\bar{1}$	"		"		
		"	31	"	"	"	"	"	"				"	"	"	5.5V	"	"	"	"	$\bar{1}$	"		"		
		"	32	"	"	"	"	"	"				"	"	"	GND	5.5V	"	"	"	$\bar{2}$	"		"		
$I_{IL1}$		3009	33	4.5V	4.5V	4.5V	4.5V	GND						0.4V	4.5V	4.5V	4.5V			"	$\bar{1}$	-7	-1.6	mA		
		"	34	"	"	"	"	0.4V	"					"	"	"	"			"	$\bar{1}$	-7	-1.6	"		
	$I_{IL2}$	"	35	0.4V	4.5V	4.5V	4.5V	"	"				"	"	"	"	"	"	"	"	$\bar{4}$	-1.4	-3.2	"		
		"	36	4.5V	0.4V	"	"	"	"				"	"	"	"	"	"	"	"	$\bar{4}$	"	"	"		
	"	37	"	4.5V	0.4V	"	"	"				"	"	"	"	"	"	"	"	$\bar{5}$	"	"	"			
	"	38	"	"	0.4V	"	"	"				"	"	"	"	"	"	"	"	$\bar{5}$	"	"	"			
	"	39	"	"	4.5V	"	"	"				"	"	"	"	"	"	"	"	$\bar{6}$	"	"	"			
	"	40	"	"	"	"	"	"				"	"	"	"	"	"	"	"	$\bar{6}$	"	"	"			
"	41	"	"	"	"	"	"				"	"	"	0.4V	0.4V	0.4V	"	"	$\bar{3}$	"	"	"				
$I_{OS}$	3011	42					4.5V										GND	GND	"	$\bar{GS}$	-20	-80	"			
	"	43					"										"	"	"	$\bar{GS}$	"	"	"			
	"	44					"										"	"	"	$\bar{A}_0$	"	"	"			
	"	45					"										"	"	"	$\bar{A}_1$	"	"	"			
	"	46					"		GND	GND	"						"	"	"	$\bar{A}_2$	"	"	"			
$V_{IC}$		47	-12mA																4.5V	$\bar{1}$			-1.5	V dc		
		48		-12mA															"	$\bar{1}$	"		"	"		
		49			-12mA														"	$\bar{1}$	"		"	"		
		50				-12mA													"	$\bar{1}$	"		"	"		
		51					-12mA												"	$\bar{1}$	"		"	"		
		52						-12mA											"	$\bar{1}$	"		"	"		
		53							-12mA										"	$\bar{1}$	"		"	"		
		54																	"	$\bar{1}$	"		"	"		
		55																	"	$\bar{1}$	"		"	"		
	$I_{CC}$	3005	56			GND	GND												5.5V	$V_{CC}$			70	mA		

See footnote at end of device type 03.



TABLE III. Group A inspection for device type 03 – Continued.  
Terminal conditions (pins not designated may be  $H \geq 2.0$  V;  $L \leq 0.8$  V; or open)

Subgroup	Symbol	MIL-STD-883 method	Cases E,F Test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Test limits		Unit	
				$\bar{4}$	$\bar{5}$	$\bar{6}$	$\bar{7}$	$\bar{E}_1$	$\bar{A}_2$	$\bar{A}_1$	GND	$\bar{A}_0$	$\bar{0}$	$\bar{1}$	$\bar{2}$	$\bar{3}$	$\bar{GS}$	$\bar{E}_0$	$V_{CC}$		Min	Max		
10 $T_C = +125^\circ\text{C}$	$t_{PLH1}$	3003 (Fig. 6)	111	2.4V	2.4V	2.4V	2.4V	GND					IN	2.4V	2.4V	2.4V		OUT	5.0V	IO - $\bar{E}_0$	6	22	ns	
	$t_{PHL1}$		112	"	"	"	"	GND					IN	"	"	"	"	OUT	"	IO - $\bar{E}_0$	9	43	"	
	$t_{PLH2}$		113	"	"	"	"	"					"	"	"	"	"	OUT	"	E1 - $\bar{GS}$	"	32	"	
	$t_{PHL2}$		114	"	"	"	"	"					"	"	"	"	"	OUT	"	E1 - $\bar{GS}$	"	38	"	
	$t_{PLH3}$		115	"	"	"	"	"					"	"	"	"	"	OUT	"	E1 - $\bar{E}_0$	"	32	"	
	$t_{PHL3}$		116	"	"	"	"	"					"	"	"	"	"	OUT	"	E1 - $\bar{E}_0$	"	48	"	
	$t_{PLH4}$		117	"	"	"	"	GND			OUT		"	"	"	"	"	"	"	"	E1 - $\bar{A}_0$	"	37	"
			118	"	"	"	"	"			OUT	OUT	"	"	"	"	"	"	"	"	E1 - $\bar{A}_1$	"	37	"
			119	"	"	"	"	"			OUT	OUT	"	"	"	"	"	"	"	"	E1 - $\bar{A}_2$	"	37	"
	$t_{PHL4}$		120	"	"	"	"	"			OUT	OUT	"	"	"	"	"	"	"	"	E1 - $\bar{A}_0$	"	38	"
			121	"	"	"	"	"			OUT	OUT	"	"	"	"	"	"	"	"	E1 - $\bar{A}_1$	"	38	"
			122	"	"	"	"	"			OUT	OUT	"	"	"	"	"	"	"	"	E1 - $\bar{A}_2$	"	38	"
	$t_{PLH5}$		123	"	"	"	"	2.4V	GND						IN	"	"	"	OUT	"	IO - $\bar{GS}$	15	56	"
	$t_{PHL5}$		124	"	"	"	"	"	"						IN	"	"	"	OUT	"	IO - $\bar{GS}$	9	34	"
			125 $\frac{3}{4}$	"	"	"	"	"	"						2.4V	"	"	"	"	"	IO - $\bar{A}_0$	"	52	"
			126 $\frac{4}{4}$	"	"	"	"	"	"						GND	IN	"	"	"	"	IO - $\bar{A}_0$	"	"	"
			127 $\frac{3}{4}$	"	"	"	"	"	"			OUT			"	IN	"	"	"	"	IO - $\bar{A}_1$	"	"	"
			128 $\frac{3}{4}$	"	"	"	"	"	"			OUT			"	IN	2.4V	"	"	"	IO - $\bar{A}_0$	"	"	"
			129 $\frac{3}{4}$	"	"	"	"	"	"			OUT			"	"	"	IN	"	"	IO - $\bar{A}_1$	"	"	"
			130 $\frac{4}{4}$	"	"	"	"	"	"			OUT			"	"	"	IN	GND	"	IO - $\bar{A}_0$	"	"	"
			131 $\frac{3}{4}$	"	"	"	"	"	"			OUT			"	"	"	GND	2.4V	"	IO - $\bar{A}_2$	"	"	"
			132 $\frac{3}{4}$	"	"	"	"	"	"			OUT			"	"	"	"	"	"	IO - $\bar{A}_0$	"	"	"
			133 $\frac{4}{4}$	"	"	"	"	"	"			OUT			"	"	GND	"	"	"	IO - $\bar{A}_1$	"	"	"
			134 $\frac{4}{4}$	"	"	"	"	"	"			OUT			"	GND	2.4V	"	"	"	IO - $\bar{A}_0$	"	"	"
			135 $\frac{3}{4}$	"	"	"	"	"	"			OUT			"	2.4V	"	"	"	"	IO - $\bar{A}_1$	"	"	"
			136 $\frac{3}{4}$	"	"	"	"	"	"			OUT			"	"	"	"	"	"	IO - $\bar{A}_2$	"	"	"
			137 $\frac{3}{4}$	"	"	"	"	2.4V	"			OUT			"	"	"	"	"	"	IO - $\bar{A}_0$	"	"	"
			138 $\frac{3}{4}$	"	"	"	"	"	"			OUT			"	"	"	"	"	"	IO - $\bar{A}_1$	"	"	"
			139 $\frac{3}{4}$	"	"	"	"	"	"			OUT			"	"	"	"	"	"	IO - $\bar{A}_2$	"	"	"
	11		$t_{PHL6}$	Same tests, terminal conditions, and limits as for subgroup 10, except $T_C = -55^\circ\text{C}$ .	140 $\frac{3}{4}$	"	"	"	2.4V	"				OUT	"	IN	"	"			"	IO - $\bar{A}_0$	"	"
141 $\frac{4}{4}$		"			"	"	"	"				OUT	"	GND	2.4V	"	"			"	IO - $\bar{A}_0$	"	"	"
142 $\frac{3}{4}$		"			"	"	"	"				OUT	"	"	"	"	"	"			IO - $\bar{A}_1$	"	"	"
143 $\frac{3}{4}$		"			"	"	"	"				OUT	"	"	IN	2.4V	"	"			IO - $\bar{A}_0$	"	"	"
144 $\frac{3}{4}$		"			"	"	"	"				OUT	"	"	"	"	"	IN	"		IO - $\bar{A}_1$	"	"	"
145 $\frac{4}{4}$		"			"	"	"	"				OUT	"		"	"	GND	"			IO - $\bar{A}_0$	"	"	"
146 $\frac{3}{4}$		"			"	"	"	"				OUT	"		"	"	"	"	"		IO - $\bar{A}_2$	"	"	"
147 $\frac{3}{4}$		"			"	"	"	"	"			OUT	"		"	"	"	"	"		IO - $\bar{A}_0$	"	"	"
148 $\frac{4}{4}$		"			"	"	"	"	"			OUT	"		"	"	GND	"	"		IO - $\bar{A}_1$	"	"	"
149 $\frac{4}{4}$		"			"	"	"	"	"			OUT	"		"	GND	2.4V	"	"		IO - $\bar{A}_0$	"	"	"
150 $\frac{3}{4}$		"			"	"	"	"	"			OUT	"		"	2.4V	"	"	"		IO - $\bar{A}_1$	"	"	"
151 $\frac{3}{4}$		"			"	"	"	"	"			OUT	"		"	"	"	"	"		IO - $\bar{A}_2$	"	"	"
152 $\frac{3}{4}$		"			"	"	"	"	2.4V	"		OUT	"		"	"	"	"	"		IO - $\bar{A}_0$	"	"	"
153 $\frac{3}{4}$		"			"	"	"	"	"	"		OUT	"		"	"	"	"	"		IO - $\bar{A}_1$	"	"	"
154 $\frac{3}{4}$		"			"	"	"	"	"	"		OUT	"		"	"	"	"	"		IO - $\bar{A}_2$	"	"	"

- 1/ Input voltages shown are A = 2.4 V minimum and V = 0.8 V maximum.
- 2/ Output voltages shall be  $H \geq 1.5$  V,  $L \leq 1.5$  V.
- 3/ 2 gate delay.
- 4/ 3 gate delay.

4.3 Qualification inspection. Qualification inspection shall be in accordance with MIL-PRF-38535.

4.4 Technology Conformance inspection (TCI). Technology conformance inspection shall be in accordance with MIL-PRF-38535 and herein for groups A, B, C, and D inspections (see 4.4.1 through 4.4.4).

4.4.1 Group A inspection. Group A inspection shall be in accordance with table III of MIL-PRF-38535 and as follows:

- a. Tests shall be as specified in table II herein.
- b. Subgroups 4, 5, and 6 shall be omitted.

4.4.2 Group B inspection. Group B inspection shall be in accordance with table II of MIL-PRF-38535.

4.4.3 Group C inspection. Group C inspection shall be in accordance with table IV of MIL-PRF-38535 and as follows:

- a. End point electrical parameters shall be as specified in table II herein.
- b. The steady-state life test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.

4.4.4 Group D inspection. Group D inspection shall be in accordance with table V of MIL-PRF-38535. End point electrical parameters shall be as specified in table II herein.

4.5 Methods of inspection. Methods of inspection shall be specified and as follows.

4.5.1 Voltage and current. All voltage values given are referenced to the microcircuit ground terminals. Currents given are conventional current and positive when flowing into the referenced terminal.

## 5. PACKAGING

5.1 Packaging requirements. For acquisition purposes, the packaging requirements shall be as specified in the contract or order (see 6.2). When packaging of materiel is to be performed by DoD personnel, these personnel need to contact the responsible packaging activity to ascertain requisite packaging requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activity within the Military Department of Defense Agency, or within the Military Department's System Command. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

## 6. NOTES

(This section contains information of a general or explanatory nature that may be helpful, but is not mandatory.)

6.1 Intended use. Microcircuits conforming to this specification are intended for original equipment design applications and logistic support of existing equipment.

6.2 Acquisition requirements. Acquisition documents should specify the following:

- a. Title, number, and date of the specification.
- b. Complete part number (see 1.2).
- c. Requirements for delivery of one copy of the quality conformance inspection data pertinent to the device inspection lot to be supplied with each shipment by the device manufacturer, if applicable.
- d. Requirements for certificate of compliance, if applicable.
- e. Requirements for notification of change of product or process to acquiring activity in addition to notification of the qualifying activity, if applicable.
- f. Requirements for failure analysis (including required test condition of MIL-STD-883, method 5003), corrective action and reporting of results, if applicable.
- g. Requirements for product assurance options.
- h. Requirements for special carriers, lead lengths, or lead forming, if applicable. These requirements should not affect the part number. Unless otherwise specified, these requirements will not apply to direct purchase by or direct shipment to the Government.
- i. Requirements for "JAN" marking.
- j. Packaging requirements (see 5.1).

6.3 Qualification. With respect to products requiring qualification, awards will be made only for products which are, at the time of award of contract, qualified for inclusion in Qualified Manufacturers List QML-38535 whether or not such products have actually been so listed by that date. The attention of the contractors is called to these requirements, and manufacturers are urged to arrange to have the products that they propose to offer to the Federal Government tested for qualification in order that they may be eligible to be awarded contracts or purchase orders for the products covered by this specification. Information pertaining to qualification of products may be obtained from DSCC-VQ, 3990 E. Broad Street, Columbus, Ohio 43128-3990.

6.4 Superseding information. The requirements of MIL-M-38510 have been superseded to take advantage of the available Qualified Manufacturer Listing (QML) system provided by MIL-PRF-38535. Previous references to MIL-M-38510 in this document have been replaced by appropriate references to MIL-PRF-38535. All technical requirements now consist of this specification and MIL-PRF-38535. The MIL-M-38510 specification sheet number and PIN have been retained to avoid adversely impacting existing government logistics systems and contractor's parts lists.

6.5 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535 and MIL-STD-1331, and as follows:

GND	.....	Ground zero voltage potential.
$I_{IN}$	.....	Current flowing into an input terminal
$V_{IN}$	.....	Voltage level at an input terminal.

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6.6 Logistic support. Lead materials and finishes (see 3.3) are interchangeable. Unless otherwise specified, microcircuits acquired for Government logistic support will be acquired to device class B (see 1.2.2), lead material and finish A (see 3.4). Longer length leads and lead forming should not affect the part number.

6.7 Substitutability. The cross-reference information below is presented for the convenience of users. Microcircuits covered by this specification will functionally replace the listed generic-industry type. Generic-industry microcircuit types may not have equivalent operational performance characteristics across military temperature ranges or reliability factors equivalent to MIL-M-38510 device types and may have slight physical variations in relation to case size. The presence of this information should not be deemed as permitting substitution of generic-industry types for MIL-M-38510 types or as a waiver of any of the provisions of MIL-PRF-38535.

<u>Military device type</u>	<u>Generic-industry type</u>
01	54147
02	54148
03	9318

6.8 Changes from previous issue. Marginal notations are not used in this revision to identify changes with respect to the previous issue, due to the extensiveness of the changes.

Custodians:	Preparing activity:
Army – CR	DLA - CC
Navy - EC	
Air Force - 11	Project 5962-2100
NASA - NA	
DLA – CC	
Review activities:	
Army - MI, SM	
Navy - AS, CG, MC, SH, TD	
Air Force – 03, 19, 99	

NOTE: The activities listed above were interested in this document as of the date of this document. Since organizations and responsibilities can change, you should verify the currency of the information above using the ASSIST Online database at <http://assist.daps.dla.mil> .